ABET
Self-Study Report
for the
Computer Engineering Program
at the
University of Florida
Gainesville, Florida

June, 2012

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BACKGROUND INFORMATION

Note to the ABET Evaluator: this document contains many hyperlinks to the support documentation provided. Reading the electronic version is suggested.

A. Contact Information

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You may also contact the CISE ABET Coordinator, Dr. Manuel Bermudez (352-392-1200, manuel@cise.ufl.edu). The CISE Undergraduate Coordinator is Dr. Douglas Dankel, (352-505-1578, ddd@cise.ufl.edu). Mr. Paul Rocha (352-392-9758, parocha@ece.ufl.edu) represents the Student Services Center, which is the primary student advising locus for undergraduate Computer Engineering students. All may be reached at the same postal address.

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B. Program History

The University of Florida has a long tradition of producing excellent students in Computer Engineering. The Computer Engineering (CE) program was created in 1992, but our connection to information technology dates back to the very beginning of digital computers. John V. Atanasoff received his B.S. in 1925 from the University of Florida and went on eventually to build the world's first digital computer. We are equally proud of our graduates and students of today. Graduates of the UF Computer Engineering program have a superior placement record and are in great demand from some of the leading technology companies in the field, including Microsoft, Intel, Harris, Raytheon, Lockheed-Martin, Northup-Grumman, Amazon, Oracle, and IBM. Like many computer programs around the country the enrollment dipped significantly since 2000, but appears to have stabilized a few years ago to about nearly 500 undergraduates (in addition to about 190 graduate students).

Our ACM programming contest teams have improved their performance over the past few years. In 2007, one of the four student programming teams we fielded at the ACM Southeast Regional programming competition won first place, for the first time in our history, competing against 75 other teams from 31 universities. Another team won second place in 2008, and in 2010 our team qualified for the IEEExtreme world competition, and won 14th place in the world, and first place in the Americas, competing against nearly 1000 teams from 350 universities from around the world. In November 2011, UF again competed in the IEEExtreme 5.0, a 24 hour marathon programming contest. Out of more than 1500 teams world wide and 227 teams from the USA, our best team--starring Joe Thuemler, Jason Fisher, and Cheran Wu—finished 1st in North America (thus 1st in the USA and region R03) and 13th in the world. [Student_Programming_Teams]

The program offers two tracks, hardware and software, allowing students to opt for a more software or hardware oriented curricula depending upon their interests. The program consists of a common core of courses designed to give students a solid foundation in the skills needed to excel in Computer Engineering. In addition, both tracks emphasize a thorough understanding of hardware, software, and their interactions in a complete computer system. Our alumni are known for their problem solving abilities. Students following the software track also develop skills in networks, databases, and algorithms, while those in the hardware track develop skills in electronic circuits, digital electronic devices, and communications.

Historically, these two tracks have been administered independently by the CISE (software) and ECE (hardware) Departments, with students being advised and admitted through the respective departments. Since 1998 [CISE-ECE-governance], the program has been administered jointly by the two departments. Coordination of the program has in the past been carried out via a joint oversight committee. While this structure was effective in providing an excellent educational program, it was often unwieldy and somewhat inefficient to administer, particularly in light of the increasing need for assessment of student performance. As part of
our on-going program improvement process, in 2006 it was decided to revamp the administrative structure of the program. The new streamlined structure has created a single faculty body (the CE faculty, to oversee the assessment and operation of the program.

The last ABET general review of the CE program was in the Fall of 2006. No concerns, weaknesses or deficiencies were raised.

Specific changes to the program since the last general review are:

1. Our Data Structures and Algorithms course (COP-3530) has been switched from using Java as the instructional language to C++.
2. An additional laboratory hour was added to COP-3504 Introduction to CIS (formerly CIS-3020).
3. The option of students working in groups in COP 3504 (formerly CIS 3020) has been eliminated.
4. Java threading and synchronization has been added as topic to COP-4600 Operating System.
5. A MATLAB component has been added to EEL-3135 Signals and Systems, making it EEL 3135C with 4 credit hours.
6. The content of a number of courses has been modified, mostly to add topics, to follow trends in the discipline.

The Computer Engineering program is currently undergoing a significant re-design and re-structuring. This is discussed in detail in Section 4.D, including the proposed revision, its rationale, and its current status. Four guiding principles have been adopted for the revision:

1. The establishment of a Computer Engineering Core Competency.
2. More flexible specialization, replacing the current two-track system with certificate options in various specialization areas.
3. More explicit and clearer guidance and focus for students.
4. Implementation of the new program with no drastic changes to current course offerings.

C. Options

The Computer Engineering program at the University of Florida is jointly offered by two departments within the College of Engineering. These are the Computer and Information Science and Engineering Department (CISE), and the Electrical and Computer Engineering Department (ECE). The degree program has two tracks: the hardware track (CEE, housed in ECE) and the software track (CEN, housed in CISE). Students in each track are enrolled in their respective departments. The title of the degree is Bachelor of Science in Computer Engineering, regardless of track. Both departments offer additional undergraduate degree programs. The CISE department offers three other degree programs:

- Computer Science, through the College of Engineering.
- Computer Science through the College of Liberal Arts and Sciences.
- Digital Arts and Sciences, through the College of Engineering.
The ECE department also offers its primary undergraduate degree, Electrical Engineering. With this Self-Study Report, we seek accreditation for the Computer Engineering program.

D. Organizational Structure

The figure above shows the organizational structure of the CE program. The program is administered by the CE faculty, which consists of all CISE faculty, and all the ECE faculty. This structure was established through a set of governing procedures agreed to by both departments in 1998, and amended in 2006 to create the Computer Engineering Faculty [CISE-ECE-governance]. The CE Program Director is a member of the CEN faculty who has responsibility for overseeing curriculum-related issues in the CE program. The CE Director serves as the chair of the CE curriculum committee and is responsible for ABET accreditation of the CE program. He/she is appointed by the College of Engineering Associate Dean for Academic Affairs in consultation with the CISE and ECE Chairs and shall report to this Associate Dean.

E. Program Delivery Modes

The CISE and ECE departments jointly administer the Computer Engineering program. It is offered primarily in day mode. A few students elect to alternate one or more semesters of co-op work (which counts toward one semester hour of technical elective when properly documented) with full time enrollment. Co-op students must otherwise satisfy the same curricular requirements as non-co-op students. While up to eight hours of co-op work may be applied toward elective requirements in this manner, it is rare that more than three hours of co-op are used. Far more common is for a student to receive a summer internship and apply for co-op credit for this experience. This can extend the time needed to complete the degree.
requirements. However, because all major requirements are offered each semester, the delay in graduation is minimal.

F. Program Locations

The program’s primary locations are on the campus of the University of Florida. For the CEN track, the CISE Department is housed at E301 CSE Building. For the CEE track, the ECE Department is located at 216 Larsen Hall.

G. Deficiencies, Weaknesses or Concerns from Previous Evaluation(s)

The CE program’s most recent ABET evaluation was in the Fall of 2006. No concerns, weaknesses or deficiencies were identified. The following strengths (from the 2006 ABET Evaluation draft report) were identified.

1. The ECE department established a grade of C+ requirement in certain pre-engineering mathematics and physics courses in Fall 2002. This has resulted in greater retention rates for Computer Engineering students.

2. The ability to function on multidisciplinary teams uses feedback from the supervisor, the instructor and the sponsor of the senior project in order to provide quantitative assessment measures.

3. The Computer Engineering Advisory Board is very involved with the program, interacting with the faculty and students several times a year as well as sponsoring senior projects and creating positions for interns and cooperative education students.

H. Joint Accreditation

The Computer Engineering program is not jointly accredited by more than one commission, nor is it seeking such a joint accreditation.
CRITERION 1. STUDENTS

A. Student Admissions

Students in the Computer Engineering program are herein designated in general as CE students, and specifically as CEN (CISE Department, software track) students or CEE (ECE Department, hardware track) students. This is in accordance with the University Registrar’s major designations. For admission to the program, all CE students are expected to have satisfied the pre-professional requirement within their first 60 university semester hours, with a grade point average of 2.5 or higher based on all attempts in the eight pre-professional courses (MAC 2311, 2312, 2313, MAP 2302, PHY 2048, 2049, CHM 2045, CHM 2046 or a biological/physical science) and a minimum grade in each of these course. For CEN students, the minimum is a C, while for CEE students, it is a C+. The hardware track students (CEE) have a more restrictive requirement due to the necessity for a stronger math and science background for the upper division EEL required courses and electives. The decision to do this for the CEE students was based on correlation of performance in these tracking courses and completion of the program, and discussed in the Self-Study of 2006 for the previous ABET Review. Lower division students who do not meet this requirement are reassigned to the Undecided Engineering major with the College of Engineering Student Services Office if eligible, or are required to change to majors outside the College of Engineering otherwise.

B. Evaluating Student Performance

B.1. Course grades

Students are evaluated in each course on a letter grade basis (A through E, where E denotes a failure). In GPA calculations, an A counts as a 4.0, and an E counts as a 0.0. In the past, “plus” grades added an extra 0.5 (so a B+ was worth 3.5). Prior to the summer term of 2009, there were no “minus” grades. Effective May 11, 2009, the University of Florida adopted the use of “minus” grades. The old and new GPA calculations are shown below. There is no A+ letter grade.

<table>
<thead>
<tr>
<th>Grade</th>
<th>A</th>
<th>B+</th>
<th>B</th>
<th>C+</th>
<th>C</th>
<th>D+</th>
<th>D</th>
<th>E</th>
</tr>
</thead>
<tbody>
<tr>
<td>Grade points</td>
<td>4.0</td>
<td>3.5</td>
<td>4.0</td>
<td>2.5</td>
<td>2.0</td>
<td>1.5</td>
<td>1.0</td>
<td>0</td>
</tr>
</tbody>
</table>

University of Florida grades, prior to May 11, 2009

<table>
<thead>
<tr>
<th>Grade</th>
<th>A</th>
<th>A-</th>
<th>B+</th>
<th>B</th>
<th>B-</th>
<th>C+</th>
<th>C-</th>
<th>D+</th>
<th>D</th>
<th>D-</th>
<th>E</th>
</tr>
</thead>
<tbody>
<tr>
<td>Grade points</td>
<td>4.0</td>
<td>3.67</td>
<td>3.33</td>
<td>3.0</td>
<td>2.67</td>
<td>2.33</td>
<td>2.0</td>
<td>1.67</td>
<td>1.33</td>
<td>1.0</td>
<td>0.67</td>
</tr>
</tbody>
</table>

University of Florida grades, effective May 11, 2009
Although some courses are offered on an S/U (Satisfactory/Unsatisfactory) basis, all courses taken for credit towards the Computer Engineering degree program must be taken for a letter grade.

**B.2. Student progress monitoring**

Student progress has been monitored each semester by the CISE Department’s Student Services Center (SSC), which is staffed by two full-time advisors and one secretary. The ECE Department has an Undergraduate Studies Office (USO), where the primary advisor is Mr. Matthew Williams, and he is located in the Electrical and Computer Engineering Department. Since April 2012, advising has been consolidated to better serve CE students. Mr. Paul Rocha is the primary academic advisor for undergraduate CE students, with assistance from the CISE and ECE Student Services Offices. Others in Student Services Office are also qualified to serve as advisors, and the College of Engineering also assists in this regard. Students can speak with any faculty member about the profession and their career options. Faculty members are encouraged to speak with each student enrolled in their classes concerning guidance for the student’s career goals. The Undergraduate Advisor directs students to speak with the Undergraduate Coordinators (Dr. Dougals Dankel in CISE and Dr. Henry Zmuda in ECE) or with a faculty member working in the students’ areas of specialization.

One month prior to the University’s registration period, an email is sent to all currently enrolled students informing them the necessity of seeing an academic advisor prior to registration for the following semester’s classes. The College of Engineering places an advisement hold on each currently registered student that prevents the students from registering without speaking with an academic advisor. At the advisement appointment time, the student meets with the academic advisor to discuss the student’s progress in the program and what courses the student should take for the next semester. The academic advisor then lifts the hold so the student can register through the Integrated Student Information System (ISIS). Students on a co-op assignment may email their academic advisor with the courses they want to take the following term. The academic advisor reviews it, advises the student of any changes that should be made in their schedule, and then removes the advisement hold, thereby allowing the co-op student to register.

Students are also reminded via email to check their SASS (Student Academic Support System) tracking audit. Students cannot register with ISIS until they have checked their audit. The SASS/Tracking degree audits are reviewed each semester, in the student’s presence, to ensure that course and other requirements have been satisfied and that the student is making satisfactory progress toward his/her degree. Each time a change is made to the curriculum, the students are advised of the change through individual letters if the change will affect them.

Each department receives, every semester, a report listing those students with upper division, cumulative, or departmental grade point averages that are below 2.0. The department then places these students on college or departmental probation and an email is sent to each student summoning them into the CISE Department’s Student Services Center (SSC), or the ECE Department’s Undergraduate Student Office (USO). If the student wishes to remain in the program, a probation contract is written with the conditions of academic probation. Students whose grade point average, either UF cumulative or upper division, is between 2.00 and 2.99
are placed on Zero Deficit Counseling status. These students are advised to seek tutoring as well as to attend the College of Engineering Success workshops on time management, note taking skills, engineering study skills, problem solving for math, science & engineering, and test taking strategies.

In addition to departmental efforts, the University of Florida has established Universal Tracking. Universal Tracking is a system that monitors the progress of all undergraduate majors toward graduation. It is designed to help students find the best path to completion of their degree, to advise them into the most appropriate major as soon as possible, to provide feedback every fall and spring semester on their academic progress, and to ensure that all students receive academic advising from their advisors. Universal Tracking monitors the critical tracking courses (also called pre-professional courses) for all majors and provides feedback to the student and the advisor beginning with the fall semester of the freshman year. This immediate feedback helps the student determine if he/she has chosen the best major and is on track for graduation.

Students who are off track are notified in writing by the university. A hold is placed on their record and they cannot advance register until they have met with an academic advisor and formulated an alternative academic plan at which time the academic adviser removes the hold. A student who is off track for two consecutive terms will be required to change to a different major.

**B.3. Course prerequisites.**

The on-line University of Florida Integrated Student Information System (ISIS) does not allow a student to register for a course unless he/she has satisfied the prerequisites. Exceptions, although rare, can be authorized by the faculty member who will teach the course requiring the pre-requisite. The majority of these situations are those in which a student wishes to take a course concurrently with one of its prerequisites, often as a chance to repeat the pre-requisite course without falling behind in the program. The authorization must be sent in writing to the SSC or USO, and an advisor will authorize it on ISIS so the student can register.

**C. Transfer Students and Transfer Courses**

**C.1. Transfer students**

Transfer student applications are first screened by the Registrar’s office for general, university-wide requirements. Applications are then forwarded to the respective departments (ECE or CISE). The Department checks the appropriateness of the courses required for the major, and makes a recommendation to the College of Engineering. The College makes the final admission decision.

Freshman and sophomore transfers must meet University admission requirements. The individual department accepts upper-division transfer students, using the critical tracking criteria. Students transferring with more than 60 hours of course work must have completed a minimum number of critical tracking courses with a minimum GPA as established by the college and the department. Students transferring from a community college must have
completed their Associate of Arts degree as well as meet the critical tracking criteria. The minimum requirements to transfer into the Computer Engineering program are summarized below [Transfer-student-requirements], and appear on the respective department websites.

- Completion of an Associate of Arts degree (Florida community college Students only)
- An overall 2.7 GPA (Fall 2004, Spring 2005, and Summer 2005 applications)
- An overall 3.0 GPA (Fall 2005 and later applications)
- Completion of six* of the eight critical tracking (CT) courses prior to first term of enrollment at UF:
  - A combined 2.5 critical tracking GPA throughout the completion of all eight critical tracking courses

C.2. Transfer courses

Course work from other universities is evaluated on a two-step basis. First the Office of Admissions verifies that the courses earn college credit from an accredited university and can be posted on the student’s UF transcript. The SSC or USO, along with faculty who teach the material, then evaluate the course to ensure it meets any of the specific degree requirements for that student’s program. Upper division computer courses from public 4-year universities in Florida will generally have the same course prefix and number as the course taught here at UF. If this is not the case, or the course was taken at an out-of-state school or private university, the course must be evaluated and approved by a CISE or ECE professor who has taught the equivalent UF course within the last two years in order to fulfill the requirement. This is done on a case-by-case basis.

The acceptance of transfer credit is provided for both major and non-major courses. Courses taken in the state of Florida are accepted in accordance with the State-wide Course Numbering System (SCNS). The SCNS is used by all public post-secondary institutions in Florida and by two participating private institutions. The major purpose of this system is to facilitate the transfer of courses between participating institutions. It requires state institutions within Florida to recognize courses that the State has established to be equivalent. Courses that the State has evaluated and deemed equivalent are assigned the same three-letter prefix and the same last three digits. In general, transfer credit is allowed only for those courses that are closely coordinated by the State University System Articulation Committee. These include calculus, chemistry, physics, English, humanities and social science courses. Notable exceptions from automatic transfer of credit are courses with the last three digits in the range 900-999; internships, practica, study abroad courses; and graduate courses.

On an individual basis transfer credit may be given for major course work. However, a faculty member who teaches the UF equivalent course must approve the transfer. It is the responsibility of the student to provide documentation (catalog description, course syllabus, textbook, homework and test samples) of the course taken so that the content of the transferred course can be determined by the cognizant faculty member. If the program offering the course is not accredited by EAC/ABET, the evaluation is very strict. Often courses with the same theoretical content are not transferred due to lack of equivalence in the design or laboratory portion. General Education courses in which a student has earned a grade of ‘C’ or higher from another institution are normally acceptable for transfer credit. A maximum of 60 semester hours may be transferred from a community college.
This process ensures that departmental standards are met. Further assurance of the quality and performance of our students is maintained by the University's 30-hour rule, which states that all students must complete the last 30 hours prior to graduation at the University of Florida. Petitions to approve a student taking a course elsewhere that falls into the last 30 hours must be approved by the relevant department curriculum committee, department chairman, and by the college faculty.

The Department of Electrical and Computer Engineering assigns two advisors to each student— a faculty advisor and an academic advisor. The purpose of the faculty advisor is to answer questions concerning the profession of Electrical Engineering and to assist the student in selecting specific technical electives courses that will meet the student’s professional goals and interests. The purpose of the academic advisor is to provide curricular guidance, counseling, mentoring, curriculum planning, and to ensure that all degree requirements are met. The academic advisor is available to students on a walk-in basis during regular business hours.

D. Advising and Career Guidance

The CISE Student Services Center [SSC] staff has been handling professional advisement, degree requirements as well as the administrative responsibilities regarding student record keeping for the CISE department. The CISE Student Services Center currently has two undergraduate advisors, one graduate advisor, one graduate admissions advisor and one secretary and is supervised by the Associate Chair. In addition, the Career Resource Center (CRC) provides these services to all students on campus. A similar arrangement exists in the ECE Department, where the Undergraduate Studies Office (USO) offers a central location where students can obtain academic assistance.

Since April 2012, advising has been consolidated to better serve CE students. Mr. Paul Rocha is the primary academic advisor for undergraduate CE students, with assistance from the CISE and ECE Student Services Offices. Physically, Mr. Rocha is located at the ECE USO at Larsen Hall, which operates during normal business hours, mainly on a “walk-in” basis. An individual academic folder is created for each CE student that contains the student’s photograph, audits by the Student Academic Advisement System (SASS), application for admission, correspondence, and forms processed by the student. The academic folder is kept in the USO. The purpose of the academic advisor is to provide academic advising, counseling, mentoring, curriculum planning and ensuring that all degree requirements are satisfied. Students can speak with any faculty member about the profession and their career options. Faculty is encouraged to speak with each student enrolled in their classes about the student’s career goals.

E. Work in Lieu of Courses

The only work in lieu of courses that is allowed is University approved Advanced Placement and dual enrollment credit. Credit for these courses are evaluated and applied in the process of admission through the admissions and registrar offices.
F. Graduation Requirements

The name of the degree awarded in this program is Bachelor of Science in Computer Engineering, regardless of track.

Students are expected to complete their programs in a timely manner. Their progress is monitored through an on-line Student Academic Support System (SASS). The SASS report, or audit, is available to the student on-line via ISIS, the University of Florida’s Integrated Student Information System. Graduating students are carefully screened to ensure that the published curricula has been followed and successfully completed. The list of degree requirements reflecting the most recent course prefixes and numbers is available on the CISE and ECE Department websites.

All students are required to have a final degree check and to fill out a degree application by the end of the third week of their last semester of courses. This is to ensure that in their final semester they register for all the remaining courses required for their degree. Graduating seniors are encouraged to check with the SSC prior to the end of the drop/add period to be sure they have correctly registered for all necessary classes. An additional check is made approximately halfway through the semester before preliminary degree certification to be sure all requirements for graduation have been met. Final degree certification takes place the day after final grades are due.

The CE degree requirements consist of:

- 18 credit hours of general education requirements
- 21 credit hours of mathematics
- 8 credit hours of physics
- 7 credit hours of chemistry/biology
- 5 credit hours of the engineering core
- 48 credit hours of Computer Engineering major courses
- 1 credit hour of Ethics
- 18 credit hours of Technical Electives

The complete list of the requirements are listed on each department’s website and in the UF Undergraduate Catalog under Computer Engineering.

G. Transcripts of Recent Graduates

The program will provide transcripts from some of the most recent graduates to the visiting team along with any needed explanation of how the transcripts are to be interpreted. The team chair will request these transcripts separately. The program and any program options will be designated on the transcript. (See 2011-2012 APPM, Section II.G.4.a.)
CRITERION 2. PROGRAM EDUCATIONAL OBJECTIVES

A. Mission Statement

A.1. University of Florida Mission Statement (from the University of Florida Undergraduate Catalog):

The University of Florida is a public land-grant, sea-grant and space-grant research university, one of the most comprehensive in the United States. The university encompasses virtually all academic and professional disciplines. It is the largest and oldest of Florida's eleven universities, a member of the Association of American Universities and has high national rankings by academic assessment institutions. Its faculty and staff are dedicated to the common pursuit of the university's threefold mission: teaching, research and service.

The University of Florida belongs to a tradition of great universities. Together with its undergraduate and graduate students, UF faculty participate in an educational process that links the history of Western Europe with the traditions and cultures of all societies, explores the physical and biological universes and nurtures generations of young people from diverse backgrounds to address the needs of the world's societies.

The university welcomes the full exploration of its intellectual boundaries and supports its faculty and students in the creation of new knowledge and the pursuit of new ideas.

- **Teaching** is a fundamental purpose of this university at both the undergraduate and graduate levels.
- **Research and scholarship** are integral to the educational process and to the expansion of our understanding of the natural world, the intellect and the senses.
- **Service** reflects the university's obligation to share the benefits of its research and knowledge for the public good. The university serves the nation's and the state's critical needs by contributing to a well-qualified and broadly diverse citizenry, leadership and workforce.

The University of Florida must create the broadly diverse environment necessary to foster multi-cultural skills and perspectives in its teaching and research for its students to contribute and succeed in the world of the 21st century.

These three interlocking elements — teaching, research and scholarship, and service — span all the university's academic disciplines and represent the university's commitment to lead and serve the state of Florida, the nation and the world by pursuing and disseminating new knowledge while building upon the experiences of the past. The university aspires to advance by strengthening the human condition and improving the quality of life.
A.2. Computer Engineering Program Mission Statement (from the University of Florida Undergraduate Catalog):

- To educate undergraduate and graduate majors as well as the broader campus community in the fundamental concepts of the computing discipline,
- To create and disseminate computing knowledge and technology, and
- To use our expertise in computing to help society solve problems.

B. Program Educational Objectives

The Computer Engineering Program Educational Objectives are listed in the University of Florida Undergraduate Catalog:

Three to five years after graduating with this degree, a Computer Engineering graduate will:

1. Excel in a career utilizing their education in Computer Engineering;
2. Continue to enhance their knowledge;
3. Be effective in multidisciplinary and diverse professional environments
4. Provide leadership and demonstrate professional integrity.

These Program Educational Objectives are published in the UF Undergraduate catalog [UF-catalog-CE], and on the Computer Engineering website. [CE-website].

C. Consistency of the Objectives with the Mission of the Institution

We achieve our mission by producing graduates that achieve these objectives. It has been said that technology transfer is best accomplished through the transfer of people. Our graduates take what they have learned here and disseminate it into the environments where they live and work after graduation. As they succeed and achieve these objectives, they create and disseminate new knowledge and technology, thereby helping solve society’s problems. The first element of our mission (education) is fulfilled by producing graduates who excel in their careers and are effective in diverse environments. The second element of our mission (creating and disseminating knowledge) is fulfilled by producing graduates who excel, continually enhance their knowledge, and provide leadership and demonstrate professional integrity. Finally, the third element of our mission (helping society solve problems) is fulfilled by producing graduates that achieve all four objectives. Therefore, the objectives of the program are consistent with our program’s educational mission.

These objectives are also consistent with university’s mission. Graduates that excel in a career and continue to enhance their knowledge must do so by pursuing and disseminating new knowledge. Graduates that are effective in diverse professional environments, and provide leadership and professional integrity, clearly strengthen the human condition and improve the quality of life.
D. Program Constituencies

We consider our significant constituencies to be:

1. Faculty
2. Alumni
3. Employers

The primary constituents of the process for establishing and reviewing, and assessing objectives are the faculty, the alumni, and the employers. The employers are represented by the Industrial Advisory Board (IAB), which consist almost exclusively of alumni. Alumni at large are the primary source of data for assessing objectives, through alumni surveys.

E. Process for Revision of the Program Educational Objectives

During the 2007-2011 period, the Industrial Advisory Board (IAB) was periodically consulted to determine whether the CE program objectives remained appropriate. We utilize the IAB as a vehicle to receive feedback on our programs in many ways. The IAB is populated by representatives of companies such as Amazon, Barr Systems, Citrix Systems, Dell, Exxon Mobil, Harris, IBM, Info Tech, Intel, Lockheed Martin, Microsoft, Milbank, Tweed, Hadley and McCloy LLP, Raytheon, Solidworks, Tower Hill Insurance Group, Turner Broadcast Systems, Vcom3D, and Walt Disney. These companies hire both our hardware track and software track graduates, and they provide on-going, invaluable feedback on our program. The IAB meets twice a year [IAB-meetings], and we conduct both open discussions as well as questionnaires that IAB members use to give us their opinions on our programs. In 2005-2006, our previous Program Educational Objectives underwent a complete and thorough re-write. Rather than considering possible changes to the previous objectives, these were discarded and the new objectives formulated from scratch. The new objectives were established through discussions at a faculty retreat, and discussions with our IAB. After such a thorough revision, during 2007-2011, the IAB and faculty reviewed the objectives periodically, but suggested no changes.

i. The Computer Engineering Program Oversight Committee (later streamlined to consist of the CISE ABET Coordinator) receives recommendations from the IAB, and extracts recommendations and possible objective revisions from the alumni surveys.

ii. Any resulting recommendations are distributed, as necessary, to the appropriate administrative bodies (the CISE and/or ECE Curriculum Committee, Department Chairs, and/or space and facilities committees).

iii. Recommendations for changes in the curriculum/objectives from committees are voted on by the CE faculty, and if passed, submitted to the College of Engineering for approval.
iv. Changes are communicated to the IAB the following semester, and the process repeats.

The process is shown graphically in Figure 2-1.

Although the IAB also represents alumni, the alumni at large are also surveyed annually [Alumni-surveys], both to assess the attainment of the objectives (see Criterion 4A), and to continue to assess and evaluate the attainment of the objectives. The alumni surveys suggested no changes to our Program Objectives during this period.

The process for revision of the objectives remained the same throughout 2006-2011. In 2012, the process was enhanced by naming a CE Program Director, and a CE Curriculum Committee consisting of three faculty from each Department. This committee has proposed a major revision to the program’s curriculum, which is has not yet received final approval, nor has been implemented. These curricular changes are discussed in Section 4D.
CRITERION 3. STUDENT OUTCOMES

A. Student Outcomes

Philosophically, the objective of the program leading to the degree of Bachelor of Science in Computer Engineering is to provide students with a strong theoretical and practical background in computer hardware and software, along with the engineering analysis, design, and implementation skills necessary to work between the two. A computer engineer is someone with the ability to design a complete computer system -- from its circuits to its operating system to the application programs that run on it.

The UF Computer Engineering program has the eleven outcomes listed in Criterion 3. In 2005, as part of an extensive re-examination of our objectives and outcomes, we tailored each of the eleven outcomes (a) through (k) to the specific context of Computer Engineering, eliminated some additional outcomes we previously had, and introduced the new outcome (l). In 2012 we reverted back to the outcomes (a) through (k), after feedback from a mock ABET visit in 2011, thus dropping outcome (l). These changes are discussed in Section 4D.

The program outcomes are published in the CISE and ECE Department websites [CISE, ECE]. Program criteria are well embedded in the outcomes. The mathematics and statistics criterion is included in outcome (a), basic science, engineering and Computer Science needed to analyze complex hardware and software systems is included in outcomes (a), (c), and (e), and the requirement for breadth and depth is included primarily in outcomes (c), (e), (h), and (k). Whenever knowledge of and ability to use mathematics, statistics, and probability are assessed, they are done so in the context of Computer Engineering problems. When considering a complex system (such as a complete computer or an operating system), analysis is indispensable to implement a successful design. Breadth cannot be measured in a single course as an outcome per se, but by the breadth in the curriculum and measurement of the ability to design, to formulate and solve problems, and to use modern methods and tools in several courses with very different subject material, assessment of breadth is achieved. The outcomes of the Computer Engineering Program are as follows.

A graduate of the Computer Engineering program will demonstrate:

a. an ability to apply knowledge of mathematics, science, and engineering
b. an ability to design and conduct experiments, as well as to analyze and interpret data
c. an ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability
d. an ability to function on multidisciplinary teams
e. an ability to identify, formulate, and solve engineering problems
f. an understanding of professional and ethical responsibility
g. an ability to communicate effectively
h. the broad education necessary to understand the impact of engineering solutions in a global, economic, environmental, and societal context
i. a recognition of the need for, and an ability to engage in life-long learning
j. a knowledge of contemporary issues
k. an ability to use the techniques, skills, and modern engineering tools necessary for engineering practice.

B. Relationship of Student Outcomes to Program Educational Objectives

Here we describe how the student outcomes prepare graduates to attain the program educational objectives. First, Table B-3-1 maps the strength of interaction between each PEO and each PO. Table 3-B-1 shows that each of the program objectives is supported by multiple outcomes.

<table>
<thead>
<tr>
<th>Program Outcomes (abbreviated)</th>
<th>Program Educational Objectives</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>1) Excel in Career</td>
</tr>
<tr>
<td>a) Apply math, science, engineering</td>
<td>S</td>
</tr>
<tr>
<td>b) Design experiment, analyze data</td>
<td>S</td>
</tr>
<tr>
<td>c) Design system, component, or process</td>
<td>S</td>
</tr>
<tr>
<td>d) Function on multidisciplinary teams</td>
<td>S</td>
</tr>
<tr>
<td>e) Solve engineering problems</td>
<td>S</td>
</tr>
<tr>
<td>f) Understand prof., ethical responsibility</td>
<td>M</td>
</tr>
<tr>
<td>g) Ability to communicate effectively</td>
<td>S</td>
</tr>
<tr>
<td>h) Understand impact of eng. solutions</td>
<td>S</td>
</tr>
<tr>
<td>i) Recognize need for lifelong learning</td>
<td>S</td>
</tr>
<tr>
<td>j) Knowledge of contemporary issues</td>
<td>S</td>
</tr>
<tr>
<td>k) Ability to use eng. Techniques, tools</td>
<td>S</td>
</tr>
</tbody>
</table>

Table 3-B-1. Relationships between program objectives and program outcomes

Key: S = Strong relationship; M = Moderate relationship

(a) an ability to apply knowledge of mathematics, science, and engineering

The three primary foundations of Computer Engineering are calculus, discrete mathematics and physics. Calculus is the foundation for any engineering endeavor. Discrete mathematics is the basis for the Computer Science related components of the program. Physics is the basis for the Electrical Engineering related components of the program. Students must have a firm foundation in math, science, and engineering in order to apply that knowledge to solve Computer Engineering problems in any career that utilizes a Computer Engineering education (objective 1). Mathematics and engineering techniques are part of the required analysis, design, and implementation skills, which are necessary to enhance further knowledge in the field (objective 2). Effectiveness, including on teams (objective 3), depends in part on technical competence, which is supported by outcome (a).

Outcome (a) strongly supports objectives (1) and (2), and objective (3) moderately.

(b) an ability to design and conduct experiments, as well as to analyze and interpret data
The Computer Engineering curriculum includes required laboratory experience that combines elements of theory and practice in the area of general science, computer fundamentals, and engineering applications. These skills are reinforced in many of the technical electives, and are necessary to solve problems in a successful career (objective 1) and to be effective on teams (objective 3). The same skills are also necessary to enhance further knowledge in the field (objective 2). Statistics is a key component of the curriculum as well. One could also argue that proper interpretation of data is also necessary for effective leadership.

**Outcome (b) strongly supports objective (1), and objectives (2) and (3) moderately.**

(c) an ability to design hardware and software systems, components, or processes to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability

Our curriculum requires the student to design many systems involving software or hardware, or systems that require a synthesis of the two. The first step of the software engineering process is to develop a specification and ensure that it meets the needs of the customer. Subsequently, a system (software and/or hardware) must be designed to meet those specifications. Any useful design must meet ethical, legal, and social constraints. Ongoing enhancement of knowledge (objective 2) is also supported by this outcome. The ability to design appropriate systems and processes is important to being an effective team member and for leadership.

**Outcome (c) strongly supports objective (1), and objectives (2), (3) and (4) moderately.**

(d) an ability to function on multi-disciplinary teams

Computer engineering is a cross-cutting discipline in that it is applied in a wide variety of domains. Furthermore, it is itself a multi-disciplinary domain, bringing together relevant aspects of Computer Science, Electrical Engineering, applied mathematics, and statistics. Practically any team experience in Computer Engineering is thus fundamentally a multi-disciplinary activity. Solving large problems in Computer Engineering requires a team effort. Engineers work with one another to combine their strengths. The level of work and depth of detailed knowledge of all the components of a solution are too great to be handled by a single person. Large system projects require teamwork in order to be effectively carried out, and the objectives will be met with different levels of competence by each computer engineer Thus, this outcome strongly supports career success, effectiveness in diverse environments, and leadership. It also moderately supports continuing knowledge enhancement.

**Outcome (d) strongly supports objectives (1), (3), and (4), and objective (2) moderately.**

(e) an ability to identify, formulate, and solve engineering problems
Computer engineering involves problem solving at two levels – the use of computer technology (hardware and software) to solve a useful real-world problem, and the problem solving ability required to design and develop a computer system (hardware and software). These skills strongly impact career success, and moderately impact effectiveness in diverse environments and leadership. Recognition of the need to continue to learn about various problem solving tools and methods comes about from observation of the astounding advancements that continue to be made in the field.

**Outcome (e) strongly supports objective (1), and objectives (2), (3), and (4) moderately.**

(f) **an understanding of professional and ethical responsibility**

Some of the ethical, legal, and social issues in Computer Engineering are the same as in any area of engineering, while some are specific to Computer Engineering itself, such as electronic privacy issues, software copyright and intellectual property (IP) issues, computer crime, patents of mathematical relationships (algorithms), etc. This outcome moderately affects career success, but strongly impacts multi-disciplinary environment effectiveness and leadership. Due to the continuing struggle of society and the legal systems around the world to deal with issues of IP, computer crime, etc., our graduates will continue have an incentive to stay abreast of developments in these areas.

**Outcome (f) strongly supports objectives (3) and (4), and objective (1) moderately.**

(g) **an ability to communicate effectively**

Without good communication between team members, effective teamwork and leadership (and thus objectives 3 and 4) become impossible. When faced with a large multi-disciplinary problem, a single engineer rarely has an adequate depth of knowledge in all areas needed to solve the problem. Computer engineers must complement each other with their distinct knowledge, and combine their strengths. This cooperative effort requires good communication skills. Without good communication between engineers, managers, and customers, the specification of a problem and the needs of the customer will be ill-stated, incomplete, and likely flawed. Further, success in selling a project (internally or externally) or promoting a company hinges in part on good presentation skills. Communication skills thus moderately impact knowledge enhancement, but strongly impact career success, effectiveness in diverse environments, and leadership.

**Outcome (g) strongly support objectives (1), (3) and (4), and objective (2) moderately.**
(h) the broad education necessary to understand the impact of engineering solutions in a global, economic, environmental, and societal context

Understanding the impact of engineering solutions in their larger context is crucial to a successful career, and to operate effectively in multi-disciplinary environments. In a less crucial manner, this outcome impacts enhanced learning and leadership.

*Outcome (h) strongly supports objectives (1) and (3), and objectives (2) and (4) moderately.*

(i) a recognition of the need for, and an ability to engage in life-long learning

This outcome is essentially the same as for any engineer. Since computing techniques change so rapidly, some aspects of the knowledge of a computer engineer quickly become obsolete, while others (such as the mathematical foundations of Computer Science) remain constant. Without outcome (i), a computer engineer’s knowledge becomes obsolete within a few years, making all four objectives unattainable, especially objectives 1 and 2. To work in diverse disciplinary, ethnic, or cultural environments, one must be open to learning foreign ways. Hence, this outcome also strongly supports objective 3.

*Outcome (i) strongly supports objectives (1), (2), and (3), and objective (4) moderately.*

(j) a knowledge of contemporary issues

This outcome is essentially the same as for any engineer. Knowledge of contemporary issues at the time of graduation is essential to career advancement and leadership, and moderately important to continuing knowledge enhancement and effectiveness in multi-disciplinary teams.

*Outcome (j) strongly supports objectives (1) and (4), and objectives (2) and (3) moderately.*

(k) an ability to use the techniques, skills, and modern engineering tools necessary for engineering practice.

Computer Engineering graduates must be equipped with an ability to use modern computing techniques and skills, and software and hardware tools needed to solve Computer Engineering problems, at both levels of problem-solving discussed in outcome (e). Such skills strongly impact career success and continuing enhancement of knowledge, and moderately impact effectiveness in multi-disciplinary environments and leadership.

*Outcome (k) strongly supports objectives (1) and (2), and objectives (3) and (4) moderately.*
CRITERION 4. CONTINUOUS IMPROVEMENT

A. Program Educational Objectives

Description of the assessment process.

The primary process for assessing and evaluating the objectives consists of analyzing and evaluating our annual alumni survey [Alumni Surveys]. As part of the reformulation of our outcomes and objectives in 2005, we decided to collect evidence of specific accomplishments by our alumni that would demonstrate attainment of the objectives. The annual alumni survey includes the questions shown in Table 4-1.

<table>
<thead>
<tr>
<th>YES/NO Questions</th>
<th>Obj 1</th>
<th>Obj 2</th>
<th>Obj 3</th>
<th>Obj 4</th>
</tr>
</thead>
<tbody>
<tr>
<td>Attended graduate school?</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Received an advanced degree?</td>
<td></td>
<td>Yes</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Authored technical papers?</td>
<td></td>
<td></td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td>Attended any technical conferences?</td>
<td></td>
<td>Yes</td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td>Participated in continuing education?</td>
<td></td>
<td></td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td>International activities?</td>
<td></td>
<td></td>
<td></td>
<td>Yes</td>
</tr>
<tr>
<td>Contributions to employer?</td>
<td></td>
<td>Yes</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Employed in Computer Engineering?</td>
<td></td>
<td>Yes</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Mentored junior staff?</td>
<td></td>
<td>Yes</td>
<td>Yes</td>
<td></td>
</tr>
<tr>
<td>Made ethical decisions?</td>
<td></td>
<td>Yes</td>
<td></td>
<td></td>
</tr>
<tr>
<td>Dealt with financial, legal, cultural constraints?</td>
<td>Yes</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Leader/Member/No Questions</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Member or leader of a multidisciplinary team?</td>
<td>leader</td>
<td>lead/mem</td>
<td>leader</td>
<td></td>
</tr>
<tr>
<td>Leader or member of a professional society?</td>
<td>leader</td>
<td>member</td>
<td>leader</td>
<td></td>
</tr>
<tr>
<td>Leader or member of volunteer group?</td>
<td></td>
<td>lead/mem</td>
<td>leader</td>
<td></td>
</tr>
<tr>
<td>Participate in community outreach?</td>
<td></td>
<td>lead/mem</td>
<td></td>
<td></td>
</tr>
<tr>
<td><strong>Rank 1-5 Questions</strong></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Number of promotions received?</td>
<td>&gt;=1</td>
<td></td>
<td>&gt;=1</td>
<td></td>
</tr>
<tr>
<td>Prepared to deal with ethical decisions?</td>
<td>&gt;=3</td>
<td></td>
<td>&gt;=3</td>
<td></td>
</tr>
<tr>
<td>Make presentations?</td>
<td>&gt;=3</td>
<td></td>
<td>&gt;=3</td>
<td></td>
</tr>
<tr>
<td>Adequately prepared for career?</td>
<td>&gt;=3</td>
<td>&gt;=3</td>
<td></td>
<td>&gt;=3</td>
</tr>
<tr>
<td>Objective 1 (excel in career)?</td>
<td>&gt;=2</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Objective 2 (enhance knowledge)?</td>
<td>&gt;=2</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Objective 3 (diverse environments)?</td>
<td>&gt;=2</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Objective 4 (provide leadership)?</td>
<td>&gt;=2</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Table 4-1. Alumni survey questions and their relevance to the objectives.
The survey asks four types of questions:

- Yes/No questions.
- Leader/Participant (or member)/None questions.
- Questions in which the respondent enters a ranking on a 1-5 scale. These include four questions (one per objective) on how well the alumnus believes they have achieved the objectives, and four others (also one per objective) ranking the suitability of the objective.
- Five other questions. These are: the year of graduation, their field of employment, whether their employer has ever been fined for environmental issues, the graduate school the alumnus is attending, and suggestions for additional objectives. These five were not included in the statistical analysis. The last two (graduate schools and suggestions) are discussed in item A-4 below.

Table 4-1 maps these questions to the objectives.

For us to consider an objective to be attained by an alumnus, the alumnus must say that they have attained it, and must also have achieved one or more of the specific accomplishments that are relevant to that objective.

In short, for the objective to be attained, the alumnus must say they did, and provide evidence of it elsewhere in the survey. For example, following column “Obj 4” in Table 4-1, for an alumnus to be considered to have attained Objective 4 (provide leadership), the alumnus must rank their attainment of Objective 4 as a 2 or higher, AND one or more of the following:

- Authored technical papers,
- Attended technical conferences
- Mentored junior staff
- Lead a multidisciplinary team
- Lead a group in a professional society
- Lead a volunteer group
- Been promoted at least once
- Been required to make presentations (3 or better)
- Rate herself to be adequately prepared for her career (3 or better).

Frequency of the assessment processes

We collect the alumni surveys, process them statistically, and evaluate the attainment of the objectives, annually.

Expected level of attainment of the objectives

We expect 90% (on average) of our alumni to attain each of the objectives.

Summary of Results and Analysis.
<table>
<thead>
<tr>
<th>Number of respondents</th>
<th>2007</th>
<th>2008</th>
<th>2009</th>
<th>2010</th>
<th>2011</th>
<th>07-11</th>
</tr>
</thead>
<tbody>
<tr>
<td>Attended graduate school?</td>
<td>39%</td>
<td>38%</td>
<td>48%</td>
<td>48%</td>
<td>61%</td>
<td>47%</td>
</tr>
<tr>
<td>Received an advanced degree?</td>
<td>29%</td>
<td>24%</td>
<td>33%</td>
<td>45%</td>
<td>52%</td>
<td>37%</td>
</tr>
<tr>
<td>Authored technical papers?</td>
<td>10%</td>
<td>11%</td>
<td>14%</td>
<td>16%</td>
<td>35%</td>
<td>17%</td>
</tr>
<tr>
<td>Attended any technical conferences?</td>
<td>39%</td>
<td>50%</td>
<td>57%</td>
<td>50%</td>
<td>65%</td>
<td>52%</td>
</tr>
<tr>
<td>Participated in continuing education?</td>
<td>44%</td>
<td>58%</td>
<td>60%</td>
<td>43%</td>
<td>55%</td>
<td>52%</td>
</tr>
<tr>
<td>International activities?</td>
<td>44%</td>
<td>63%</td>
<td>57%</td>
<td>57%</td>
<td>68%</td>
<td>58%</td>
</tr>
<tr>
<td>Contributions to employer?</td>
<td>76%</td>
<td>81%</td>
<td>74%</td>
<td>76%</td>
<td>84%</td>
<td>78%</td>
</tr>
<tr>
<td>Employed in Computer Engineering?</td>
<td>68%</td>
<td>69%</td>
<td>64%</td>
<td>72%</td>
<td>81%</td>
<td>71%</td>
</tr>
<tr>
<td>Mentored junior staff?</td>
<td>60%</td>
<td>59%</td>
<td>60%</td>
<td>60%</td>
<td>71%</td>
<td>62%</td>
</tr>
<tr>
<td>Made ethical decisions?</td>
<td>65%</td>
<td>66%</td>
<td>52%</td>
<td>64%</td>
<td>65%</td>
<td>62%</td>
</tr>
<tr>
<td>Dealt with financial, legal, cultural constraints?</td>
<td>71%</td>
<td>77%</td>
<td>67%</td>
<td>72%</td>
<td>74%</td>
<td>72%</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Leader or Member</th>
<th>Leader or Member</th>
</tr>
</thead>
<tbody>
<tr>
<td>Member or leader of a multidisciplinary team?</td>
<td>68%</td>
</tr>
<tr>
<td>Leader or member of a professional society?</td>
<td>35%</td>
</tr>
<tr>
<td>Leader or member of volunteer group?</td>
<td>36%</td>
</tr>
<tr>
<td>Participate in community outreach?</td>
<td>60%</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Rank 1-5 Questions</th>
<th>Mean</th>
</tr>
</thead>
<tbody>
<tr>
<td>Number of promotions received?</td>
<td>1.37</td>
</tr>
<tr>
<td>Prepared to deal with ethical decisions?</td>
<td>4.31</td>
</tr>
<tr>
<td>Make presentations?</td>
<td>3.57</td>
</tr>
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<td>Adequately prepared for career?</td>
<td>3.94</td>
</tr>
<tr>
<td>Objective 1 (excel in career)?</td>
<td>3.58</td>
</tr>
<tr>
<td>Objective 2 (enhance knowledge)?</td>
<td>4.00</td>
</tr>
<tr>
<td>Objective 3 (diverse environments)?</td>
<td>4.18</td>
</tr>
<tr>
<td>Objective 4 (provide leadership)?</td>
<td>4.06</td>
</tr>
<tr>
<td>Objective 1 suitable (excel in career)?</td>
<td>4.13</td>
</tr>
<tr>
<td>Objective 2 suitable (enhance knowledge)?</td>
<td>4.29</td>
</tr>
<tr>
<td>Objective 3 suitable (diverse environments)?</td>
<td>4.04</td>
</tr>
<tr>
<td>Objective 4 suitable (provide leadership)?</td>
<td>3.94</td>
</tr>
</tbody>
</table>

Software (CEN) or Hardware (CEE)?

<table>
<thead>
<tr>
<th>Software Track (the rest are Hardware)</th>
</tr>
</thead>
<tbody>
<tr>
<td>78%</td>
</tr>
</tbody>
</table>

Table 4-2: Summary of results, Alumni Surveys, 2007-2011.

<table>
<thead>
<tr>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Objective 1 (excel in career) attained</td>
<td>93%</td>
<td>88%</td>
<td>89%</td>
<td>95%</td>
<td>90%</td>
<td>91%</td>
</tr>
<tr>
<td>Objective 2 (enhance knowledge) attained</td>
<td>97%</td>
<td>93%</td>
<td>98%</td>
<td>98%</td>
<td>100%</td>
<td>97%</td>
</tr>
<tr>
<td>Objective 3 (multi-disciplinary diversity) attained</td>
<td>92%</td>
<td>94%</td>
<td>91%</td>
<td>91%</td>
<td>100%</td>
<td>94%</td>
</tr>
<tr>
<td>Objective 4 (leadership) attained</td>
<td>99%</td>
<td>96%</td>
<td>100%</td>
<td>98%</td>
<td>97%</td>
<td>98%</td>
</tr>
</tbody>
</table>

Table 4-3. Results of Evaluation of Objectives.
Results of the alumni survey for the period 2007-2011 are summarized in Tables 4-2 and Table 4-3. The complete statistical analysis, for each year and for the entire 2007-2011 period, appears in a single Excel spreadsheet, in [Alumni-survey-analysis]. Additional statistics for each annual survey also appear in [Alumni-surveys]. As shown in Table 4-2, we had an average of nearly 64 respondents each year to the survey. Nearly two-thirds of the respondents had been CEN students, and the rest had been CEE students. Since this roughly corresponds to the graduation rate of each track, we conclude that the surveys introduce no bias in favor of either track. More than two-thirds of the respondents are employed in Computer Engineering. About half have attended graduate school or other formal education, and nearly a third have earned additional degrees. One-fifth have authored technical papers, and more than half have attended technical conferences. About half have engaged in continuing education, and more than half have worked with customers/vendors/colleagues outside the United States. 78% say they have made significant technical contributions, 62% say they have mentored junior colleagues, and 62% say they have been involved in making ethical decisions in their workplaces. 72% say they have dealt with financial, legal, or cultural constraints. Although fairly few (27%) are members of professional societies, 70% have been leaders or members of multidisciplinary teams, and about half have done volunteer or outreach work. The majority of them have been promoted at least once (1.27 times on average), most say they are prepared to make ethical decisions (4.35/5.0 ranking), most are required to make presentations as part of their duties (3.63/5.0 ranking), and most say they were better prepared by our program than colleagues from other institutions (3.81/5.0 ranking, Go Gators!).

Table 4-3 summarizes the results of our evaluation of the objectives. As stated earlier, we expect 90% of our alumni to attain each of the objectives. Objective 1 (excel in career) fell (slightly) below this expectation in 2008 and 2009. However,

the averages over the entire period show that each objective is indeed attained by 90% or more of our surveyed alumni.

The alumni survey collects additional information from our graduates. Here we discuss two items: the graduate schools our alumni attend, and their suggestions for improvement/additions to the objectives. The list of graduate schools attended by our alumni is impressive:

<table>
<thead>
<tr>
<th>University of Florida (ECE, CISE)</th>
<th>Georgia Institute of Technology</th>
</tr>
</thead>
<tbody>
<tr>
<td>University of Georgia</td>
<td>University of Florida (Law, Education)</td>
</tr>
<tr>
<td>Stetson Nova Southeastern University</td>
<td>New York University</td>
</tr>
<tr>
<td>University of South Florida</td>
<td>Florida Institute of Technology</td>
</tr>
<tr>
<td>Mercer University</td>
<td>Harvard Business School</td>
</tr>
<tr>
<td>University of Maryland</td>
<td>Florida State University</td>
</tr>
<tr>
<td>Stetson University College of Law</td>
<td>Old Dominion University</td>
</tr>
<tr>
<td>Florida Atlantic University</td>
<td>Columbia University</td>
</tr>
<tr>
<td>Fordham Law</td>
<td>University of North Florida</td>
</tr>
<tr>
<td>Midwestern State University</td>
<td>Virginia Polytechnic University</td>
</tr>
<tr>
<td>University of Colorado at Boulder</td>
<td>University of Central Florida</td>
</tr>
</tbody>
</table>
Finally, the alumni survey collects suggestions for additional objectives. We summarize here the responses that constitute feedback to the program. Although the survey question requested additional objectives, many alumni appear to have taken the opportunity to make general (and sometimes very specific) suggestions on the program, and to other graduates [Alumni Surveys-Analysis].

- Testing, debugging, testing developed applications!
- Strong ethical education focusing on more real world cases. I'd like to see something more than 1 credit hour.
- Adaptability to changing job situation and opportunities.
- Work experience with internships and Co-ops should be the highest priority for current students and administrative goals.
- Courses with objectives to develop business sense and financial accounting should be included in list of electives as many projects revolve around financial solutions.
- Communicate during clearly and effectively in both writing and speaking/presentations
- Effectively assess the market and take opportunities where needed. Adapted to the changing environment/industry.
- Better understanding of the employment and business models of computer companies.
- Teach students not only how to write software... but also how to arrive at the most EFFICIENT solution.
- The only real suggestion I can offer is please put more emphasis on the relationship between hardware and software.
- Provide a commitment to/and positive relationship for future growth & cooperation between the student and university.
- Effective communication skills are a must
- Expect change and adapt to it fairly quickly
- Prepare students with relevant technical work.
- All the objectives above are too vague to have any meaning.
- Provide more opportunities to allow students to pursue diverse topics relating to Computer Engineering
- The CE program at UF had a combination of software and hardware instruction that has proven vital to growth in my current position as a DSP engineer.
- Figure out how to better retain your students.
- I think continuing to enhance knowledge goes without saying
- 1. Be knowledgeable in industry best practices and methodologies. 2. Demonstrate mastery in contemporary industrial and enterprise systems design and development including database management and administration as well as solutions design with industrial-quality modeling
tools and languages. 3. Confidence in project planning, management, solution design, team leadership, source control strategies, extreme programming, etc.

- Now that I'm attending graduate school outside the USA, I realize the difference in the level of university curriculum between the USA and the rest of the world (from speaking with other foreign students), or at least Israel.
- While it's nice to THINK that everyone from UF could excel in their careers, UF isn't MIT. Some people have to be average. Most of those from UF are.
- Entrepreneurship, Innovation, Making a Difference
- Leave out all of the community service nonsense from your metrics..
- More specific than "Excel"....more like be honored as a top employee/student
- Critical thinking skills. Less reliance on dogma. Learn in areas outside of comfort zone.
- Most of this questionnaire is written in a manner that is irrelevant

Results documentation and maintenance.

As mentioned above, all results of the alumni surveys for the period 2007-2011, and results of their statistical processing, appear in [Alumni-surveys]

B. Student Outcomes

Description of assessment processes.

B.1.1 Direct Assessments.

The process for direct assessment of outcomes has three components (items a,b,c below) [Assessment-forms].

a) **Quantitative measurement of achievement of each outcome assessed in a subset of the required courses in the program.** This analysis is performed and reported by the instructor of each course in the form of the per-course Course Outcomes Assessment report. Each semester, the instructor of each CISE and ECE course that is charged with assessing outcomes, fills out one Course Outcomes Assessment Form for each outcome that is assessed in the course. The instructor establishes the instrument(s) to be used to assess each outcome. These are typically questions embedded in student assignments, exams, quizzes, or other evaluative mechanisms. The instructor also establishes the Likert-scale threshold(s), which maps the instrument’s scale set by the instructor (most likely the grading scale) to the 1-5 Likert scale for achieving each outcome. The instructor also supplies the relevant statistics for the course. These include the number of students, the grading scale and the average score for the embedded question, the score required to minimally achieve the outcome (Likert 2), the percentage of students who achieved the outcome, and the average Likert-scale value. Finally, the instructor makes any relevant comments regarding the achievement of the outcome. In addition, the instructor of each course prepares a set of course materials, which includes the course syllabus, copies of the Course Outcomes Assessment Reports, copies of the instruments used to assess the outcomes, and sample graded student work. The respective Departments store this information. These materials are the primary source
of information for the next level of the assessment process, the Course Committee Report. The outcomes assessed in each course, each semester, are shown in Table 4-4.

<table>
<thead>
<tr>
<th>Course #</th>
<th>Course Name</th>
<th>Outcomes</th>
</tr>
</thead>
<tbody>
<tr>
<td>COP-3504</td>
<td>Introduction to CIS (CIS-3020)</td>
<td>a</td>
</tr>
<tr>
<td>COT-3100</td>
<td>Applied Discrete Structures</td>
<td>b</td>
</tr>
<tr>
<td>COP-3530</td>
<td>Data Structures and Algorithms</td>
<td>c</td>
</tr>
<tr>
<td>CDA-3101</td>
<td>Introduction to Computer Organization</td>
<td>d</td>
</tr>
<tr>
<td>CEN-3031</td>
<td>Introduction to Software Engineering</td>
<td>e</td>
</tr>
<tr>
<td>COP-4600</td>
<td>Operating Systems</td>
<td>f</td>
</tr>
<tr>
<td>COT-4501</td>
<td>Numerical Analysis</td>
<td>g</td>
</tr>
<tr>
<td>CEN-4007C</td>
<td>Comp. Networking Fund. (CEN-4500C)</td>
<td>h</td>
</tr>
<tr>
<td>CGS-3065</td>
<td>Legal and Social Issues in Computing</td>
<td>i</td>
</tr>
<tr>
<td>CIS/CEN-4914</td>
<td>Senior Design</td>
<td>j</td>
</tr>
<tr>
<td>EEL-3111C</td>
<td>Circuits 1</td>
<td>k</td>
</tr>
<tr>
<td>EEL-3135</td>
<td>Discrete-Time Signals and Systems</td>
<td>l</td>
</tr>
<tr>
<td>EEL-3308C</td>
<td>Electronic Circuits 1</td>
<td>m</td>
</tr>
<tr>
<td>EEL-3701C</td>
<td>Digital Logic and Computer Systems</td>
<td>n</td>
</tr>
<tr>
<td>EEL-4712C</td>
<td>Digital Design</td>
<td>o</td>
</tr>
<tr>
<td>EEL-4744C</td>
<td>Microprocessor Applications</td>
<td>p</td>
</tr>
<tr>
<td>EEL-4914C</td>
<td>Senior Design</td>
<td>q</td>
</tr>
</tbody>
</table>

Table 4-4. Course-Outcomes Assessments Mapping.

b) **Qualitative evaluation of the achievement of all outcomes assessed in each course.**

This evaluation is performed and reported by a course committee, consisting of at least three faculty members who are involved in either teaching the course or otherwise have expressed interest in it. This committee makes recommendations and suggestions for improvements in the course and its relation to other courses in the curriculum, improvements in the achievement of the outcomes, and improvements in the process itself. They produce the **Course Committee Evaluation** report containing their evaluations and recommendations. Each semester, the course committee is convened by the instructor of each course in which program outcomes are assessed. As mentioned earlier, this includes ten CISE courses, and seven ECE courses. The course committees for all semesters from 2007 to 2012, along with the messages sent to faculty requesting the assessments, appear in **Course Committees**. Each committee is tasked with the following:

i. To evaluate the course in terms of its contents and its place within the curriculum.

ii. To perform a **qualitative** analysis of the quantitative data in the Course Outcomes Assessment Report and course materials supplied by the instructor.

iii. To examine, evaluate, and ratify the quantitative criteria used, the instruments chosen, the statistics provided, the Likert scale values used, and the sample student graded work.

iv. To generate suggestions/recommendations in three categories:
(i) Recommendations to future instructors.
(ii) Recommendations to curriculum governance.
(iii) Recommendations on improvement of the process.

The course committee’s findings are recorded on the Course Committee Evaluation form. The form has three parts:

i. Course issues, such as the adequacy of the textbook
ii. Program Issues, such as course pre-requisites
iii. Evaluation of Outcomes.

The course committee fills in one table per outcome assessed in the course, with evaluative comments on the instruments chosen, the statistics provided, the Likert scale values used, and the sample student graded work. The Course Committee Evaluation Reports from the courses in which outcomes are assessed are collected by the ABET Coordinator, for the third and final component of the program outcomes assessment process.

c) **Overall analysis of the achievement of each outcome, across all courses in which it is assessed.** This analysis is performed by the CISE ABET Coordinator, who analyzes the reports produced by each individual course committee, collects (and generates further) recommendations for improvements at all levels, directs those recommendations to the proper governance bodies, and follows up on actions triggered by those recommendations. Once per semester, the ABET Coordinator collects the Course Committee Evaluation Reports from the courses that assess outcomes, and takes the “birds-eye” view of each outcome, examining the results and recommendations across all courses that assess that outcome. He also gathers any feedback from other, program-level indirect assessment mechanisms. The ABET Coordinator refers suggestions and recommendations to the proper governance bodies, particularly the CE Curriculum Committee, for consideration and/or action. The Coordinator is also charged with following up in subsequent semesters on such actions, and determining whether recommendations initiated earlier to address any shortcomings have engendered program improvements.

**Process Summary:**

Quantitative data are obtained in direct form from assessments administered by course instructors. These are validated by the Course Committees, and performance is normalized to the Likert Scale. Instructors apply the results of their assessments and Course Committee evaluations directly within their classes to remediate any shortcomings. Repeated underperformance triggers examination by cognizant instructors to determine whether it is the assessment mechanism or the learning that is the problem. If learning is the issue, then these instructors consider the materials covered and how they are addressed to consider alternatives.
The Course Committees evaluate courses and outcomes measured within each course qualitatively, and their reports, are sent to the ABET Coordinator.

The ABET Coordinator considers these issues at the program level. Each outcome is considered across all courses, and areas where improvement can be made in specific courses, in the curriculum as a whole, in the facilities, or in the assessment/improvement process itself, are considered. Course Report recommendations are cataloged, and forwarded to the appropriate governance or executive body. Issues from previous terms are tracked, and effects of changes monitored. Most often, the issues are sent to the Curriculum Committee for consideration, since curricular matters are the most common. However, at times the issues concern space, facilities, or other non-curricular matters. These are referred to the chair or to the appropriate committee. The ABET Coordinator may also consider quantitative data from national benchmarks, course grades and evaluations, as well as qualitative data from student focus groups, emails, Industrial Advisory Board (IAB) discussions, and governance bodies, and indirect quantitative data obtained from student exit surveys.

B.1.2. Indirect Assessments.

Indirect assessments are carried out via student focus groups, and student exit surveys.

Student focus groups.

Annual student focus groups have been carried out in CEN-3031 Introduction to Software Engineering since 2009. Prior to that time they were carried out at a lower frequency (the first in 1999). The CEN 3031 course was chosen because it is required in both tracks, and because of its primary role in assessing outcome (d), ability to function on multi-disciplinary teams), and (g), ability to communicate effectively. Students develop software in multi-disciplinary teams in this course, and are required to make presentations on their projects during the semester. A full class period is reserved for this purpose. The students are shown the outcomes, and asked a number of questions about the quality of the program, including whether they think they have attained the outcomes, or are likely to attain them by graduation. The questions asked of the students appear in [Student-focus-groups]. Students are then given an opportunity to comment on the program. Comments fall into three categories:

- Suggestions for additional courses, or deletion of courses.
- Suggestions for modification of existing courses.
- Suggestions for changes in various course pre-requisites.

In the 2010 and 2011 focus groups, students were asked for their thoughts on some of the program changes being considered.

- Two-semester Senior Design?
- Mandatory group work in Senior Design?
- Should Java threading and synchronization be covered in Operating Systems?
- Is the lack of C++ a limitation?
- Should C++ be the language for Data structures?
- Should an extra lab hour be added to Intro to CIS?
- Should working in groups be forbidden in Intro to CIS?

In the 2012 focus group, students were asked to consider the curricular changes being considered by the CE Curriculum Committee [Student-focus-group-2012].

- Has advising to make early career choices been good?
- Are course offerings adequate?
- Are there any courses that should be dropped?
- Any courses to be added/merged?
- Any courses with problems?
- What do you think about COP 3502 in Java and COP 3503 in C++?
- What do you think about a two-semester senior design?

**Student exit surveys**

Students are asked to fill out an exit survey before they graduate. Students are asked the following questions:

- Post-Graduation Plans: (will attend graduate school, will be seeking employment, will be self-employed, will work in a company, Other).
- What means or services did you use to locate this position?
- What are the specific responsibilities for this position?
- What will your starting salary be? (Below $30,000, $30,000-35,000, $35,001-40,000, $40,001-45,000, $45,001-50,000, $50,001-55,000, $55,001-60,000, $60,001-65,000, $65,001-70,000, above $70,000)
- Did your undergraduate experience at UF allow you the flexibility to take the courses you wanted to take? (If no, please explain.)
- Were there any required courses that seemed unnecessary or irrelevant?
- Were there subject areas not covered by CISE courses that you believe should be in the future?
- Have you experienced any CISE course-related problems that you believe need to be improved? (If yes, please describe the nature of these problems.)
- Please rate the CISE Department's Undergraduate Advising services on a scale of one to five [Poor, Needs Improvement, Fair, Good, Excellent] (Availability, Professional Demeanor, Assisting with Academic Problems, Clarifying/Communicating Academic Requirements, Overall Effectiveness.
- How can Undergraduate Advising be improved?
- Indicate the amount of attention each of the following should be given by the CE program in the future. Options: [Much Less Attention, Less Attention, Same Amount of Attention, More Attention, Much More Attention] (Co-op/Internship Opportunities, Study Abroad Opportunities, Undergraduate Research Oppor-
tunities, Student Organizations/Clubs, Career Development Workshops, Graduate School Opportunities.

- Do you wish to stay in contact with the Department after graduation (receive Newsletters, etc.)?

The answers to these questions are analyzed statistically, and the results are shown in Section 4B.2.

Frequency of assessment processes.

The direct assessments are performed each semester. Whenever practical, this has included Summer terms. The indirect assessments are performed yearly. Annual student focus groups began in 2009. Data on student exit surveys are available beginning in 2008.

Expected level of attainment of each outcome.

Our overall goal is to ensure that 80% of the students achieve each outcome (on average) in each assessment, and an average Likert value of 3.0 or better.

Results of evaluation process.

B.4.1 Direct Assessments

Below is a summary of the results of the assessment process of each of our outcomes. The two critical figures in each table below are SAO (Percentage of Students Achieving the Outcome), and ALV (Average Likert Value). As mentioned above, in order to consider that an outcome has been achieved, we expect an average SAO of 80% or better, and an average ALV of 3.0 or better. Comprehensive assessment information and results for the 2007-2011 period, organized by year, by course and by outcome, appear in [Assessments].

Note to the ABET Evaluator: the Assessments portion of the CE program website contains sample graded student work, and is password-protected due to student record confidentiality. Please contact the Program Director to obtain login and password information.

In each table shown below, assessments values are averages for 2007-2011.

Outcome a): an ability to apply knowledge of mathematics, science, and engineering;
We use eight different courses to assess this outcome. Two are early in the curriculum (Discrete Structures and Data Structures) and give us feedback on student preparation as well as their ability to apply concepts from math and statistics. In Numerical Analysis we can assess student ability to apply math and calculus to numerical problems. In Circuits I (EEL 3111C), we can assess student ability to use linear algebra and their understanding of work and energy. In Signals and Systems (EEL 3135), we can assess students’ ability to solve differential equations using engineering techniques as well as their ability to work with complex numbers and exponentials. In either Senior Design course, we once again
assess whether students have the basic tools they need to be successful in design. This outcome is also assessed in IPPD (Integrated Product and Process Design), a highly successful alternative capstone course option, discussed in Section 5A.6. The average percentage of Students Achieving the Outcome (SAO) and Average Likert Value (ALV), for 2007-2011, appear in the table below.

<table>
<thead>
<tr>
<th>SUMMARY 2007-2011</th>
<th>SAO:</th>
<th>ALV:</th>
</tr>
</thead>
<tbody>
<tr>
<td>COT 3100 Discrete Structures</td>
<td>76.5</td>
<td>3.4</td>
</tr>
<tr>
<td>COP 3530 Data Structures</td>
<td>73.5</td>
<td>3.3</td>
</tr>
<tr>
<td>CDA 3101 Computer Organization</td>
<td>79.3</td>
<td>3.8</td>
</tr>
<tr>
<td>7COT 4501 Numerical Analysis</td>
<td>83.9</td>
<td>3.5</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>94.2</td>
<td>4.5</td>
</tr>
<tr>
<td>EEL-4914C Senior Design</td>
<td>96.0</td>
<td>4.4</td>
</tr>
<tr>
<td>EEL 3111C Circuits 1</td>
<td>72.5</td>
<td>3.8</td>
</tr>
<tr>
<td>EEL 3135 Signals and Systems</td>
<td>74.2</td>
<td>3.6</td>
</tr>
<tr>
<td>EEL 4744C Microprocessors</td>
<td>90.5</td>
<td>4.25</td>
</tr>
</tbody>
</table>

Overall Averages 84% 3.5

Outcome b): an ability to design and conduct experiments, as well as to organize, analyze and interpret data; We use five different courses to assess this outcome. One is early in the program (Circuits 1), and it gives us feedback on student ability to conduct experiments. In Numerical Analysis we can assess student ability to conduct experiments in numerical problems. In Computer Networking, we can assess students’ ability to conduct benchmarking experiments and analyze data related to networking traffic. In the Senior Design course (and IPPD), we once again assess whether students have the experimentation and analysis skills they need. Outcome (b) has been quantitatively assessed as follows:

<table>
<thead>
<tr>
<th>SUMMARY 2007-2011</th>
<th>SAO:</th>
<th>ALV:</th>
</tr>
</thead>
<tbody>
<tr>
<td>COT 4501 Numerical Analysis</td>
<td>70.8</td>
<td>3.6</td>
</tr>
<tr>
<td>CEN 4500C/4007C Networking</td>
<td>82.9</td>
<td>3.8</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>96.0</td>
<td>4.4</td>
</tr>
<tr>
<td>EEL 3308C Electronic Circuits 1</td>
<td>100</td>
<td>4.2</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>100</td>
<td>4.5</td>
</tr>
</tbody>
</table>

Overall Averages 90% 4.1

Outcome c): an ability to design a system, component, or process to meet desired needs within realistic constraints such as economic, environmental, social, political, ethical, health and safety, manufacturability, and sustainability; We use ten different courses to assess this all-important outcome. The design outcome is assessed from the earliest courses in the program, beginning with COP 3504 (formerly CIS 3020), where we assess student ability to design computer programs. Similarly, in Computer Organization, Circuits 1 and Digital Logic we assess student ability to design hardware components and
systems. Electronic Circuits I focuses on basic amplifiers and Digital Logic looks at design of digital subsystems and state machines. The primary software course in which we assess this outcome is Operating Systems, where we receive feedback on student ability to design components of such a large, complex system. In the Senior Design course (and IPPD), we again assess whether students have the design skills they need. Outcome (c) has been quantitatively assessed as follows:

<table>
<thead>
<tr>
<th>Course Description</th>
<th>SAO</th>
<th>ALV</th>
</tr>
</thead>
<tbody>
<tr>
<td>COP 3504 Adv Programming Fund. for CIS</td>
<td>83.5</td>
<td>4.2</td>
</tr>
<tr>
<td>COP 3502/3503 Programming Fund. for CIS</td>
<td>75.4</td>
<td>3.7</td>
</tr>
<tr>
<td>CDA 3101 Computer Organization</td>
<td>77.9</td>
<td>3.5</td>
</tr>
<tr>
<td>COP 4600 Operating Systems</td>
<td>75.3</td>
<td>3.8</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>93.2</td>
<td>4.6</td>
</tr>
<tr>
<td>EEL 3308C Electronic Circuits 1</td>
<td>98</td>
<td>4.2</td>
</tr>
<tr>
<td>EEL 3701C Digital Logic</td>
<td>85</td>
<td>3.9</td>
</tr>
<tr>
<td>EEL 4712C Digital Design</td>
<td>99.0</td>
<td>4.4</td>
</tr>
<tr>
<td>EEL 4744C Microprocessors</td>
<td>90.5</td>
<td>4.2</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>100</td>
<td>4.5</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>86%</strong></td>
<td><strong>4.1</strong></td>
</tr>
</tbody>
</table>

**Outcome d): an ability to function on multi-disciplinary teams:** We have focused on the small number of courses in which group work makes the most sense. This outcome is assessed primarily in the Software Engineering course (required by both tracks), in which group work is an inherent part of the course material. This course strongly emphasizes techniques and methodologies for collaborative efforts that are necessary for any significant software development project. The Senior Design course provides us with feedback on this outcome as well. Work group work is not required in the CISE Senior Design course, but it is required in the ECE Senior Design course. As an alternative to Senior Design course, students may apply for the Integrated Product and Process Design Program [IPPD] This highly successful program was begun in the Fall 1996 Semester. Engineering students of all disciplines seeking to enhance their qualifications through a practice-oriented program may apply for this program. Students that elect our Integrated Product and Process Design course are directly placed on interdisciplinary teams. We can then monitor how these students do as a sample of the student body at large. Generally, students are successful at these projects and that indicates their ability to function on teams. Some of these teams have attracted national attention and have been featured in the popular press. Spring of 2011 saw a hardware team build an unmanned ATV for firefighters and others working in high threat environments that will help them orient to their team members in hazardous conditions using GPS. A software team developed a Google-style search engine, to aid in the search for construction items (similar, but different across different states) in a large database of state infrastructure construction contracts. Several IPPD designs have developed into commercial products.
The goal of the IPPD program is to have students in their senior year practice engineering on real, industry-sponsored design projects to improve their engineering education and to enhance their opportunity for employment. The program provides both classroom and laboratory experience. Through this program, the student learns how fundamental engineering science is relevant to effective product and process design; that design involves not just project function but also producibility, cost, schedule, reliability, customer preference and life cycle issues; how to completed projects on time and within budget, that engineering is a multi-disciplinary effort. Working in small multi-disciplinary project teams, students get important practical experience in teamwork and communication as well as in developing their leadership, management and people skills. Each team is coached by a faculty member and interfaces with liaison engineers of the companies that sponsor design projects. Several design reviews are required throughout the two semesters and held at the companies. The IPPD program requires six hours of course work and is offered as a sequence of two three-credit courses (EEL 4912 and EEL 4913 in ECE, CIS 4914 and technical elective in CIS) during fall and spring terms of the senior year. These two courses are pre-approved substitutes for a technical elective and for the capstone course. The ECE department considers IPPD to be extremely important and therefore assigns 25% FTE to the faculty members involved as coaches. Extensive documentation of IPPD self-assessment is available. All ABET outcomes are assessed in IPPD, using the [IPPD Team Outcomes Evaluation form].

Outcome (d) has been quantitatively assessed as follows:

<table>
<thead>
<tr>
<th></th>
<th>SAO</th>
<th>ALV</th>
</tr>
</thead>
<tbody>
<tr>
<td>CIS 4914 Senior Design</td>
<td>87.3</td>
<td>4.3</td>
</tr>
<tr>
<td>CEN 3031 Software Engineering</td>
<td>92.7</td>
<td>4.4</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>100</td>
<td>4.7</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>93%</strong></td>
<td><strong>4.5</strong></td>
</tr>
</tbody>
</table>

**Outcome e): an ability to identify, formulate, and solve engineering problems;** This outcome is assessed in six courses. We assess how well our students solve software problems in COP 3504 / 3502 / 3503 (formerly CISE 3020 / 3022 / 3023) and in COP 3530 Data Structures and Algorithms, where the fundamental tools in software are discovered, such as object-oriented programming, data structures, and algorithms. Problem solving in the hardware area is assessed in the Computer Organization (CDA 3101) course. Hardware issues related to error propagation and inexactness of data representation of numerical data on computers is assessed in COT 4501 Numerical Analysis. We once again assess this outcome in the Senior Design course.
<table>
<thead>
<tr>
<th>Course</th>
<th>SAO</th>
<th>ALV</th>
</tr>
</thead>
<tbody>
<tr>
<td>COP 3504 Adv Prog. Fund. for CIS</td>
<td>90.2</td>
<td>4.5</td>
</tr>
<tr>
<td>COP 3502 / 3503 Prog Fund. for CIS</td>
<td>88.8</td>
<td>4.2</td>
</tr>
<tr>
<td>COP 3530 Data Structures</td>
<td>71.2</td>
<td>3.2</td>
</tr>
<tr>
<td>CDA 3101 Computer Organization</td>
<td>72.8</td>
<td>3.4</td>
</tr>
<tr>
<td>COT 4501 Numerical Analysis</td>
<td>71.0</td>
<td>3.3</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>93.3</td>
<td>4.6</td>
</tr>
<tr>
<td>EEL 4712C Digital Design</td>
<td>99.0</td>
<td>4.4</td>
</tr>
<tr>
<td>EEL 4744C Microprocessors</td>
<td>90.5</td>
<td>4.2</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>100</td>
<td>4.9</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>84%</strong></td>
<td><strong>4.0</strong></td>
</tr>
</tbody>
</table>

**Outcome f): an understanding of professional and ethical responsibility:** We assess this outcome primarily in the Legal and Social Issues course, as well as Senior Design course, or IPPD, in which faculty supervisors of the students projects assess whether the students have achieve an understanding of the issues involved. Outcome (f) has been quantitatively assessed as follows:

<table>
<thead>
<tr>
<th>Course</th>
<th>SAO</th>
<th>ALV</th>
</tr>
</thead>
<tbody>
<tr>
<td>CGS 3065 Legal and Social Issues</td>
<td>93.3</td>
<td>4.6</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>96.7</td>
<td>4.4</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>96.0</td>
<td>4.4</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>95%</strong></td>
<td><strong>4.5</strong></td>
</tr>
</tbody>
</table>

**Outcome g): an ability to communicate effectively:** The University of Florida has always emphasized the need for effective communications for all students. All UF students must satisfy the “Gordon Rule” that requires a significant writing experience. Before graduation, students must have written 24,000 words across a minimum of four different courses. No course counts for more than 6000 words.

We also require all students to take a Technical Writing course (ENC 3245). This is a sophomore-level course that counts 6000 words against the Gordon Rule. The remaining writing comes from the student’s selection of courses that fulfill the general education requirement of the university. We assess this outcome primarily in the Software Engineering course, as well as in Senior Design. Outcome (g) has been quantitatively assessed as follows:

<table>
<thead>
<tr>
<th>Course</th>
<th>SAO</th>
<th>ALV</th>
</tr>
</thead>
<tbody>
<tr>
<td>CIS4914 Senior Design</td>
<td>95.0</td>
<td>4.6</td>
</tr>
<tr>
<td>CEN-3031 Software Engineering</td>
<td>89.8</td>
<td>4.2</td>
</tr>
<tr>
<td>EEL 4744C Microprocessors</td>
<td>90.5</td>
<td>4.2</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>100</td>
<td>4.1</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>95%</strong></td>
<td><strong>4.3</strong></td>
</tr>
</tbody>
</table>
Outcome h): the broad education necessary to understand the impact of engineering solutions in a global, economic, environmental, and societal context; This is one of the most difficult outcomes to assess. We have a General Education requirement that should expose students to a global and societal context. Students are required to take a mix of both social sciences and humanities courses to fulfill general education requirements. Some of these must have an international focus as well. We assess this outcome in Senior Design. ECE students are asked to consider manufacturability and sustainability in their designs. CISE students are asked to consider maintainability of software in theirs. This outcome is primarily assessed in the Senior Design course, in which faculty supervisors of the student projects and the course coordinators perform the assessments. We also assess this outcome in CGS-3065 Legal and Social Issues in Computing. Outcome (h) has been quantitatively assessed as follows:

<table>
<thead>
<tr>
<th>SUMMARY 2007-2011</th>
<th>SAO:</th>
<th>ALV:</th>
</tr>
</thead>
<tbody>
<tr>
<td>CGS 3065 Legal and Social Issues</td>
<td>92.0</td>
<td>4.6</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>95.9</td>
<td>4.5</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>100</td>
<td>4.0</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>96%</strong></td>
<td><strong>4.4</strong></td>
</tr>
</tbody>
</table>

Outcome i) a recognition of the need for, and an ability to engage in life-long learning; This outcome is assessed primarily in the Senior Design course, along with two other courses, one in each department: Legal and Social Issues, and Electronic Circuits 1. Outcome (i) has been quantitatively assessed as follows:

<table>
<thead>
<tr>
<th>SUMMARY 2007-2011</th>
<th>SAO:</th>
<th>ALV:</th>
</tr>
</thead>
<tbody>
<tr>
<td>CGS 3065 Legal and Social Issues</td>
<td>92.0</td>
<td>4.6</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>97.1</td>
<td>4.5</td>
</tr>
<tr>
<td>EEL 3308C Electronic Circuits</td>
<td>99.0</td>
<td>4.2</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>100</td>
<td>4.0</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>95%</strong></td>
<td><strong>4.4</strong></td>
</tr>
</tbody>
</table>

Outcome j): a knowledge of contemporary issues; We assess this outcome primarily in the Senior Design course, where it is assessed by project supervisors and the course instructors. In addition, contemporary issues are at the core of the topics in the Legal and Social Issues in Computing, and this outcome is assessed there as well. Outcome (j) has been quantitatively assessed as follows:
Outcome k) an ability to use the techniques, skills, and modern engineering tools necessary for engineering practice; This outcome is assessed in thirteen different courses. Techniques and tools for Computer Engineering practice are the mainstay of CE courses from the earliest stages in the program, beginning with CIS-3020. In Applied Discrete Structures, we assess how well our students master the mathematical techniques for determining the computational complexity of algorithms. This knowledge is applied extensively in Data Structures and Algorithms, and later in Operating Systems, Networks, and ECE courses. In Digital Logic, we assess how well students have mastered techniques and tools for designing basic hardware systems. In Signals and Systems, we assess how well they master the techniques and tools used in designing, building and analyzing communication systems. Finally, we once again assess this outcome in Senior Design, in which the techniques and tools students have learned in earlier courses are applied in their design projects. Outcome (k) has been quantitatively assessed as follows:

<table>
<thead>
<tr>
<th>Course</th>
<th>SAO</th>
<th>ALV</th>
</tr>
</thead>
<tbody>
<tr>
<td>CGS 3065 Legal and Social Issues</td>
<td>92.0</td>
<td>4.6</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>96.6</td>
<td>4.7</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>100</td>
<td>3.0</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>96%</strong></td>
<td><strong>4.1</strong></td>
</tr>
</tbody>
</table>

**SUMMARY 2007-2011**

<table>
<thead>
<tr>
<th>Course</th>
<th>SAO</th>
<th>ALV</th>
</tr>
</thead>
<tbody>
<tr>
<td>COP 3504 Adv Programming Fund. for CIS</td>
<td>84.0</td>
<td>4.3</td>
</tr>
<tr>
<td>COP 3502 / 3503 Programming Fund. for CIS</td>
<td>79.8</td>
<td>3.8</td>
</tr>
<tr>
<td>COT 3100 Discrete Structures</td>
<td>75.1</td>
<td>3.4</td>
</tr>
<tr>
<td>COP 3530 Data Structures</td>
<td>79.0</td>
<td>3.5</td>
</tr>
<tr>
<td>COP 4600 Operating Systems</td>
<td>78.5</td>
<td>3.8</td>
</tr>
<tr>
<td>COT 4500C Numerical Analysis</td>
<td>78.2</td>
<td>3.7</td>
</tr>
<tr>
<td>CIS 4914 Senior Design</td>
<td>97.5</td>
<td>3.7</td>
</tr>
<tr>
<td>EEL 3111C Circuits 1</td>
<td>96</td>
<td>4.6</td>
</tr>
<tr>
<td>EEL 3135 Discrete Systems</td>
<td>92</td>
<td>3.2</td>
</tr>
<tr>
<td>EEL 3701C Digital Logic</td>
<td>88</td>
<td>4.0</td>
</tr>
<tr>
<td>EEL 4712C Digital Design</td>
<td>93.0</td>
<td>4.3</td>
</tr>
<tr>
<td>EEL 4744C Microprocessors</td>
<td>90.5</td>
<td>4.2</td>
</tr>
<tr>
<td>EEL 4914C Senior Design</td>
<td>96.0</td>
<td>4.4</td>
</tr>
<tr>
<td><strong>Overall Averages</strong></td>
<td><strong>85%</strong></td>
<td><strong>3.8</strong></td>
</tr>
</tbody>
</table>

**Overall Summary of Outcomes Assessments**

The above data is summarized by outcome in the table below. As can be seen in the table, the Average Likert Values (ALV) well exceed our expectations of an average of 3.0 or
better. In fact, the lowest average is 3.5, and most of them are above 4.0. Regarding the percentage of Students Achieving Outcomes (SAO), all of them are above 80%, with most of them rising above 90%.

<table>
<thead>
<tr>
<th>Outcome</th>
<th>SAO</th>
<th>ALV</th>
</tr>
</thead>
<tbody>
<tr>
<td>(a)</td>
<td>85%</td>
<td>3.5</td>
</tr>
<tr>
<td>(b)</td>
<td>90%</td>
<td>4.1</td>
</tr>
<tr>
<td>(c)</td>
<td>86%</td>
<td>4.1</td>
</tr>
<tr>
<td>(d)</td>
<td>93%</td>
<td>4.5</td>
</tr>
<tr>
<td>(e)</td>
<td>84%</td>
<td>4.0</td>
</tr>
<tr>
<td>(f)</td>
<td>95%</td>
<td>4.5</td>
</tr>
<tr>
<td>(g)</td>
<td>95%</td>
<td>4.3</td>
</tr>
<tr>
<td>(h)</td>
<td>96%</td>
<td>4.4</td>
</tr>
<tr>
<td>(i)</td>
<td>96%</td>
<td>4.4</td>
</tr>
<tr>
<td>(j)</td>
<td>95%</td>
<td>4.1</td>
</tr>
<tr>
<td>(k)</td>
<td>85%</td>
<td>3.8</td>
</tr>
<tr>
<td>Overall</td>
<td>91%</td>
<td>4.1</td>
</tr>
</tbody>
</table>

We conclude that each and every one of the outcomes, as assessed, has been attained by our students.

B.4.2 Indirect Assessments

Student focus groups

Annual student focus groups have been carried out in CEN 3031 Introduction to Software Engineering since 2009. This course was chosen because it is required in both tracks, and because of its primary role in assessing outcome (d), ability to function on multi-disciplinary teams), and (g), ability to communicate effectively. Students develop software in multi-disciplinary teams in this course, and are required to make presentations on their projects during the semester. A full class period is reserved for this purpose. The students are shown the outcomes, and asked a number of questions about the quality of the program, including whether they think they have attained the outcomes, or are likely to attain them by graduation. Although no formal statistics are collected, the majority of the students invariably respond affirmatively (with occasional dissension), that they do believe they are attaining each of the outcomes. of the students appear in [Student-focus-groups]. Students are then given an opportunity to comment on the program. Comments fall into three categories. Below is a summary of the student comments, which appear in [Student-focus-groups].

- Suggestions for additional courses, or deletion of courses. Results:
- Add a course on Web technologies;
- Add a course on iPhone/Android development;
- Add a course on C/C++ programming;
- Add a course on dynamic languages/system administrator languages;
- Add a course on computer security.
- Remove Numerical Analysis;
- Remove Database 2; replace it with the graduate course.
- Why do we need physics, chemistry?
- Too much math in the major.

- Suggestions for modification of existing courses. Results:
  - More Algorithms in Data Structures;
  - Use C++ in Data Structures;
  - C course weak on pointers;
  - Overlap between Digital Logic, Computer Organization, & Microprocessors;
  - Overlap between Signals and Systems, and Electronic circuits;
  - Overlap between Discrete Structures and Intro to CIS;
  - Cover threading and synchronization in Operating systems.

- Suggestions for changes in various course pre-requisites. Results:
  - Discrete Structures should be a co-requisite for the Database course;
  - C programming should be a pre-requisite for Operating Systems.

In the 2009 and 2010 focus groups, one particular issue appeared in all three categories: particular dissatisfaction was expressed due to the lack of an appropriate course in C/C++, particularly as a pre-requisite for Operating Systems. An existing course, CGS 3460 C Programming, is for non-majors. Majors may take it, but do not receive credit for it in the program. In the 2010 focus group, a resounding 100% answered “yes” to the question “Is the lack of C++ a limitation?”

This feedback, as well as comments from the Data Structures course committees and the IAB, led to one of our primary program improvements, namely switching from Java to C++ in the Data Structures course (see Section 4C on Continuous Improvement).

In the 2010 and 2011 focus groups, students were asked for their thoughts on some of the program changes being considered.

- Two-semester Senior Design? Consensus: good as an option, but requiring it could be an unnecessary hardship.
- Mandatory group work in Senior Design? Consensus: not necessary.
- Should Java threading and synchronization be covered in Operating Systems? Consensus: Yes, although many have not yet taken it.
• Is the lack of C++ a limitation? Unanimous response: Yes.
• Should C++ be the language for Data structures? Consensus: Yes.
• Should an extra lab hour be added to Intro to CIS? Consensus: No.
• Should working in groups be forbidden in Intro to CIS? No consensus.

Although the students nearly unanimously thought it unnecessary to add an extra lab hour to COP 3504 (Advanced Programming Fundamentals for CIS), this change is another one of our primary program improvements (see Section 4C on Continuous Improvement). Although there was no consensus on whether working in groups should be allowed in COP 3504, this was another of our program improvements (see Section 4C on Continuous Improvement).

Assessment results from the 2012 student focus group

The results of the 2012 student focus group are summarized below. The full minutes are appear in [Student-focus-groups-comments-2012].

• Has advising to make early career choices been good?  
  Reply: Yes. The advisors are very helpful.

• Are course offerings adequate?  
  Reply: There is a need for more summer courses, particularly to support students taking internships.

• Are there any courses that should be dropped?  
  Reply: There is some overlap between these three courses: CDA 3101 Computer Organization, EEL 3701C Digital Logic, and EEL 4744C Microprocessors. In the linear algebra and numerical methods courses, there is a need to spend time in MATLAB first. The topic is not relevant or useful for most students. Regarding the two-course intro programming courses (COP 3502, COP 3503): these are helpful for those with no experience at all, but one-semester is fine for most students. The circuits course (EEL 3111C) is useless for software students. It is a prerequisite for digital logic, but the two courses are not related. Regarding the capstone senior design: internship & IPPD are far more valuable than Senior Design. Internships should count for senior project (or at least more credit).

• Any courses to be added/merged?  
  Reply: web technologies, user-interface, and online summer programming courses should be added. Need more software engineering integrated into the curriculum.

• Any courses with problems?  
  Reply: Data structures & Algorithms (COP 3530) needs a different textbook. The use of C++ reduces the effectiveness of the course. Students should learn C++ but not at the same time as data structures and algorithms. More online courses would make internships easier.
• **What do you think about COP 3502 in Java and COP 3503 in C++?**
  Students enthusiastically endorsed the idea

• **What do you think about a two-semester senior design?**
  Students had mixed reactions to the idea of a two-semester senior design, as well as making it mandatory to work in groups in senior design.

### Student exit surveys

A summary of the exit surveys appears in [Exit Surveys-Summary](#). We discuss the most significant results here:

- The average starting salary for our graduates is $61,428.
- 78% thought the course availability was adequate.
- Courses most often mentioned as unnecessary: Digital Logic (14%), Numerical Analysis (6.5%), Engineering Core (4.7%), Chemistry (4.5%), Discrete Math (3.9%), General Education and Technical Writing (2.4% each), and a few others (Physics, Biology, Data Structures, Software Engineering) less than 1%.
- Topics most often mentioned as not required, but should be: C/C++ (29.1%), Web Design (8.6%), other languages (7.7%), Object-Oriented Programming/ Design Patterns (4.7%), Compilers/Linux (3.6%), Database and Computer Security (2.7% each), IDEs and Game Design (2.1% each), and others (.NET, Networking, Mobile Computing, Software Design, Distributed Computing, SQL, Parallel Computing, OpenGL, and Theory of Computation) at less than 1% each.
- Most common problems with courses: C/C++ (5.9%), need to teach better (4.7%), too theoretical (2.7%), Professors/TAs that don’t speak English (2.4%), Courses too hard or too easy (2.1%), Software Engineering (1.8%), and about a dozen others, each at less than 1%.
- On how to improve advising, 40.9% did not answer the question, and 16.6% said the advising was very good, for total of 57.5 having no thoughts on improving advising. 21.7% thought advisors require more knowledge of specific courses, and a few (3) though that the advisors should be engineers themselves. 7.1% mentioned that advisors should be retained longer. 7.8% said there should be more advisors or more availability, and 3.8% said there should be less paperwork. A half-dozen other improvements were mentioned, such as understanding student needs better and streamlining the advising appointments process, by 1% of the respondents.
- The rating of the advising was overall between good and excellent, at 4.14/5.0. The students rated the advisors highest on their demeanor (4.4/5), followed by their availability (4.2/5), their communication skills (4.0/5), and finally their ability to assist students (3.9/5).
- The remaining issues were whether the program should pay less/same/more attention to course offerings, internships, study abroad, undergraduate research, student organizations, career workshops, and graduate school. The averages on all

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five were in the 2.4-2.9 range, which we interpret as “largely the same, but somewhat less”. The highest was Course Offerings at 2.9, and the lowest, not surprisingly, were Study Abroad and Career Workshops, as the University does most of the Career Workshop advising.

The most significant of these is the role of C/C++ in the program, which was by far the most frequently mentioned course that the students considered should be required. It is noteworthy that this issue even spilled over into the “Problems with Courses” survey question, where the lack of C/C++ was mentioned more often than any kind of problem with any other existing course. This shortcoming, along with comments from the course committees, faculty meetings, and our IAB, led to one of our primary program improvements, namely switching from Java to C++ in the Data Structures course (see Section 4C on Continuous Improvement).

Documentation and maintenance of results.

Results of the outcomes assessments appear in [Assessments]. Transcripts of the focus groups appear in [Student Focus Groups]. Student exit surveys appear in [Exit surveys].

C. Continuous Improvement

During the 2007-2011 period we have made the following improvements to the CE program.

1. Switching the Data Structures course form Java to C++.

   As mentioned in previous sections, data from assessments by course committees in COP-3530 Data Structures and Algorithms, beginning in 2007 [Course Assessments], suggested that not requiring C/C++ programming was a program limitation. This was confirmed in IAB meetings [IAB meetings], where alumni expressed the need for covering memory management in either C or C++. Course instructors also expressed the need for students to have knowledge of C or C++ prior to taking COP-4600 Operating Systems. In the past, students who had no experience in C or C++ were asked to take CGS-3460 C Programming, but this course is for non-majors, and cannot be taken for credit towards the major by CE students. In addition, student feedback in exit interviews stated this course is too easy, and did not cover pointers and memory management in sufficient depth [Exit Surveys]. In the 2009 and 2010 student focus groups [Student Focus Groups], particular dissatisfaction was expressed due to the lack of an appropriate course in C/C++, as a pre-requisite for Operating Systems. In the 2010 focus group, a resounding 100% answered “yes” to the question “Is the lack of C++ a limitation?” This change was discussed and approved in our Curriculum Committee in January 2010 [Curriculum Committee], and also discussed and approved in a CE faculty mini-retreat in March 2010 [CE faculty-mini-retreat]. This change required no further approvals: the undergraduate catalog does not specify the language
to be used in COP-3530 Data Structures and Algorithms. The change was implemented in Summer 2010. Subsequent assessments in Data Structures, Operating Systems, IAB, student exit interviews and focus groups have expressed their satisfaction with this change [Assessments]

2. **One laboratory hour was added to CIS-3020 Introduction to CIS (now COP 3504 Advanced Programming Fundamentals for CISE Majors)**

Data from assessments by course committees, beginning in 2007 [Assessments], suggested adding one laboratory hour to CIS-3020, which was renumbered and renamed as COP-3504 Programming Fundamentals for CISE majors in 2010. CIS-3020/COP-3504 is an intensive course for students entering the major with prior programming experience. Students with no prior programming experience are asked instead to take two courses, CIS-3022 and CIS-3022 (now COP 3502/3503 Programming for CISE Majors 1&2). The rationale for this change was to increase the amount of laboratory experience in COP-3504, to better match the laboratory experience provided by the two-course alternate sequence taken by students with no prior programming experience. This change was discussed and approved in our Curriculum Committee in January 2010 [Curriculum Committee-2010], and also discussed and approved in a CE faculty mini-retreat in March 2010 [CE faculty-mini-retreat]. The change required no further approvals: the undergraduate catalog does not specify the number of laboratory hours in this course. The change was implemented in Summer 2010. Subsequent assessments in COP-3504, and our IAB have expressed their satisfaction with this change. It is noteworthy to point out that student exit interviews and focus groups did not raise this issue. In the 2009 Student Focus Group, students thought that less material is covered in CIS-3020 than in CIS-3022/3023, and that CIS-3020 was not ambitious enough. One year later, when asked about this (at that time) proposed change [Student Focus Group-2010], the consensus among the students was that it was not necessary.

3. **Group work forbidden in CIS-3020 (now COP-3405).**

Informal feedback to the CISE Department Chair in 2008 revealed that allowing students to work in groups in CIS 3020 (now COP-3504) had allowed at least one student to pass the course with insufficient individual programming skills. Course Committee reports from CIS 3022/3023 (now COP 3502 / 3503) in Spring 2009 also recommended strictly individual programming projects CIS-3022-Course-Committee-Spring-2009. Although this issue was not reflected in the achievement of the student outcomes, and students in this course are assumed to have had prior programming experience, the CISE Department Chair decided to forbid student programming projects in CIS 3020 (now COP-3504). The change did not require consideration by the Curriculum Committee, nor a change to the program or the undergraduate catalog. It was implemented immediately, and no further such feedback has been received. In the student focus group of Spring 2009, one participant mentioned that less material was covered in CIS-3020 than in the two course sequence CIS 3022 / 3023 (now COP 3502 / 3503), and that CIS 3020 (now COP 3504) was not “ambitious enough”. Other
participants disagreed. In the student focus group of Spring 2010 this issue did not come up. When asked whether the major emphasized individual programming skills enough, 80% of the participants either said yes, or had no opinion [Focus-group-2010].

4. Java threading and Synchronization added to COP 4600 Operating Systems.

Beginning in 2007, informal discussions among faculty revealed a concern that multi-threaded programming, specifically Java threading and synchronization, was not being covered in COP-4600 Operating Systems, although this knowledge is needed in the Networking course CEN4500C (now CNT4007C). The course committee report in Summer 2008 for COP-4600 mentions that this was a necessary change to the Operating Systems course [Course-Committee-COP-4600-Summer-2008]. The material has been discussed in COP 4600 since Fall 2009, and subsequent course committees have been satisfied with the results.

5. MATLAB lab component added to EEL 3135.

In Spring 2010, the ECE Department decided to add a software (MATLAB) lab component to EEL 3135 Intro. Signals and Systems, making it EEL 3135C with 4 credit hours. The rationale is:

- Many students struggle with the abstract and mathematical material in EEL 3135, and consequently become frustrated about the class. Since EEL 3135 is the beginning class that introduces basic ECE concepts, such as frequency domain analysis and feedback, this frustration ripples through the remainder of our curriculum.
- Software labs provide hand-on and practical experiences to students by allowing them to "see, hear, and feel" the applications of the abstract material taught in class. This can motivate students to better understand and learn the material.
- The proposed labs are mainly MATLAB programming exercises with applications to Signals & Systems. They constitute a more formal avenue to train students about programming in MATLAB.
- Past EEL 3112 instructors do not think they need 4 credit hours to cover the material listed in the syllabus.


1. Feedback from the 2011 Mock ABET visit

In 2011 the College of Engineering hired a consulting group to play the role of a visiting ABET evaluation team. Our entire draft ABET Self-Study report was submitted to this team, and a comprehensive, ABET-style visit took place. Our program evaluator was Dr. Nick Tredennick. Dr. Tredennick made the following recommendations:
Criterion 2: Program Educational Objectives.

From 2006 to 2011, our objectives remained unchanged, following a significant overhaul of the objectives in 2005. Throughout this period, the Objectives were discussed twice yearly in the Industrial Advisory Board meetings [IAB meetings]. In 2011, the mock ABET consulting team recommended that all programs in the College of Engineering revise their Objectives, with the goal of simplifying them. The Objectives were discussed in advance of the mock accreditation visit at the IAB meeting on October 6, 2011, specifically whether to revise and/or eliminate Objective 4, but no consensus was reached. The issue was discussed again at the IAB meeting on January 24, 2012. The IAB proposed specific changes in the wording, and the elimination of Objective 3 (“Be effective in multidisciplinary and diverse professional environments”). The issue was passed to the newly formed CEN Curriculum Committee, which decided to retain the wording changes, but not eliminate Objective 3. In addition, the committee proposed to change Objective 4 from “Provide leadership and demonstrate good citizenship”, to “Provide leadership and demonstrate professional integrity”. The Objectives Revision Proposal details the proposed changes. It was approved in the CEN Curriculum meeting on January 26, 2012, and approved unanimously by the entire CEN faculty [Objectives Revision Approval]. These changes to the Objectives required us to revise the questions in the Alumni Survey. The new Alumni Survey questions appear on the CEN website [New Alumni Survey].

Criterion 3: Outcomes

From 2006 to 2011, our outcomes (a) through (k) were essentially the same as the ABET (a) through (k), outcomes, but they were customized specifically for computer engineering, rather than engineering in general. We also had an additional outcome (l). The entire consulting team in 2011 recommended that all programs use the ABET (a) through (k) outcomes with no modifications or customizations. After consulting with our industrial advisory board [IAB-10-6-2011], and after a CEN Committee meeting [CEN-Committee-2-23-2010], we have adopted this change.

Criterion 4: Continuous Improvement

Our consultant (Dr. Tredennick), and the entire consulting team stated that we are performing our assessments too often, and in some cases in too many courses. This same concern was made regarding the entire College. They stated that the assessment burden on faculty is excessive and may not be sustainable. We are currently considering changing the frequency of the assessments, and performing assessments in fewer courses.

Tredennick stated that outcome (b), "an ability to design and conduct experiments, as well as to organize, analyze, and interpret data" was not adequately supported by our
assessment data. He found plenty of evidence that we assess students’ ability to analyze experimental data, but not to design experiments. We have begun addressing this issue in COT 4501 Numerical Methods. In that course in Fall 2011, for example, students designed an experiment to evaluate the performance profile of $x=A\backslash b$ in MATLAB, for matrices of various sizes. Our assessments for that semester showed that students mastered this topic very well, with 81% of the students achieving an acceptable outcome, and an average Likert score of 3.7. [Assessments-COT-4501-Fall-2011-Outcome-b].

Criterion 6: faculty.

The mock ABET Evaluator expressed a concern regarding the joint CISE/ECE governance of the program, specifically the lack of a joint Curriculum Committee.

In the Fall of 2011, a Joint Computer Engineering Curriculum Committee was formed, with 3 faculty members from each of the two Departments. It was tasked with taking a broad and deep review of the entire Computer Engineering curriculum, and to propose changes to improve it. Dr. Herman Lam, ECE, was appointed Director of the Computer Engineering program, and chairs the Committee. The committee’s findings and proposed changes to the curriculum appear in [Proposed-CE-Curriculum], and are summarized below. These changes are still pending as of June 2012, having been proposed to the Computer Engineering faculty on April 10, 2012 for their discussion and approval [Proposal to CE Faculty].


The joint CE program Curriculum Committee determined that there is too much emphasis on an early distinction between the hardware (CEE) and software (CEN) tracks. Students must commit early to one track or the other. Once committed, they follow different study plans. The further down the track, the more difficult it is to change to the other track. This is illustrated by the difference between the two plans of study listed in Tables 5-1 and 5-2. The distinction between the tracks leads to advising issues, since there is limited advising on how to select a track. A student must select a track before significant advising can be done. Other differences in the two tracks include difference in computer programming requirements, the capstone senior design courses (one semester in CEN versus two semesters in CEE), and different GPA requirements. This makes it difficult for students to switch between tracks. Furthermore, while many changes and improvements have been made to the program (Criterion 4), the proposed changes discussed here reflect the most comprehensive examination and reform of the program since 1999.

Differences between the two existing tracks

Students in the software track (CEN) take the following required courses, which the hardware track does not take (they are electives for the hardware track):
• CNT 4007C Computer Network Fundamentals (formerly CEN 4500C)
• COT 4501 Numerical Analysis-A Computational Approach
• MAS 3114 Computational Linear Algebra

Students in the hardware track (CEE) take the following required courses, which the software track does not take (they are electives for the software track):

• EEL 3105 Analytical Methods in Electrical Engineering
• EEL 3112 Circuits 2
• EEL 3135 Introduction to Signals and Systems
• EEE 3396 Solid-State Electronic Devices

In addition, 12 of the 18 credits of technical electives in both tracks must be taken in the student’s major department (CISE or ECE).

Students in ECE take a two-semester capstone senior design, whereas CISE students complete their senior design experience in one semester.

Guiding Principles for the proposed revision

The objective of this curricular revision process is to develop a joint CE curriculum for all Computer Engineering students, replacing the two distinct curricula for the two existing tracks. With a common core set of courses, and a set of electives organized around the concept of certificates. Four guiding principles were established for the examination of the curriculum:

1. **Computer Engineering core competency (breadth):** The core competency of the Computer Engineering major brings the unique value of integrated knowledge in both computer software and hardware (as opposed to Electrical Engineering or Computer Science majors).

2. **Flexible specialization (depth):** The student’s choice of career path should be selected when appropriate, not necessarily early in the curriculum.

3. **Explicit and clear guidance and focus:** for students, advisors, and employers.

4. **Additional goals:** The changes must be implementable with no drastic changes to existing course offerings. Course changes have been proposed where they are deemed to be necessary, primarily in the capstone design course sequence, and in the introductory programming courses.

Proposed Curriculum for the Joint Computer Engineering Program

• There are 102 credits that are common to the two existing software and hardware tracks. This common core will be kept.
An introductory programming course in Java will be added (COP 3502 Programming for CIS majors 1) for students with no prior programming experience. Students with sufficient prior Java programming experience will be able to obtain a course waiver. Typically, these are students who studied Java in Advanced Placement courses in high school, or learned on their own [Proposed programming requirements].

COP 3503 (Programming for CISE majors 2) will become a C++-based course, not Java. This proposed change addresses feedback from our Industrial Advisory Board, which recommends that students be adept at C++. It should also address concerns about COP-3530 Data Structures and Algorithms, which is now taught using C++, and is considered to be very difficult for students with no prior experience in C++. These concerns have been expressed by current and former students, in focus groups and exit surveys [Proposed programming requirements].

The capstone senior design will become a two-semester sequence for all Computer Engineering students [Capstone course proposal]. Details are given below.

The software and hardware tracks are to be eliminated. A number of 3-course certificates in various topics in Computer Engineering is proposed instead. All courses that were formerly required for one track but not the other will become electives for all students.

Flexible specialization: the 18 credits of technical electives may be taken from either department. At most one programming language course can count towards the technical electives in the current curriculum; the new curriculum proposes no change to this constraint.

Certificate options will be provided for explicit and clear guidance and focus when students are selecting courses to meet their 18 credit technical elective requirement. Each certificate will consist of at least three courses (typically 9 credits) so students can focus on two certificates. The certificates will be officially recognized on students’ transcripts. The certifications are optional; a student may elect to take a wide range of technical electives that do not fulfill the requirements of any certificate. Sample certificates are listed below. Note that some of the courses in the certificates are required courses.

**Proposed Sample Certificate Options**

**Artificial Intelligence**
- CAP 4053 Artificial Intelligence for Computer Games
- CAP 4621 Artificial Intelligence and Heuristics
- CAP 4680 Knowledge-Based System: Theory and Practice

**Data Computer Communications & Networking**
- EEL 4713 or CDA 4102 Computer Architecture
- CNT 4007C Computer Network Fundamentals
- EEL 4598 Data Computer Communications

**Database Systems**
- COP 3530 Data Structures and Algorithms
- CIS 4301 Information and Database Systems 1
- CIS 4720 Information and Database Systems 2

**Modeling and Interaction**
- CAP 4800 Systems Simulation
- CAP 4730 Computational Structures in Computer Graphics
- CIS 4930 Human-Computer Interaction (currently a special topic course)

**Interaction Programming for Industrial Systems and Operations**
- COT 4501 Numerical Methods
- ESI 4312 Operations Research
- CAP 4680 Knowledge-Based System: Theory and Practice

**Optical Networks**
- EEE 3396 Solid-State Electronic Devices
- EEL 4440 Optical Communication Systems
- EEL 4598 Data Computer Communications

**Reconfigurable Computing**
- EEL 4712 Digital Design
- EEL 4713 Digital Computer Architecture
- EEL 4930 Reconfigurable Computing

**Robotics**
- EEL 4744 Microprocessor Applications
- EEL 4665 Intelligent Machines Design Laboratory
- CAP 4621 Artificial Intelligence and Heuristics

**Sensor Networks**
- ABE 4304 Remote Sensing in Engineering
- EEL 4598 Intelligent Machines Design Laboratory
- Remaining course to be determined

**Software System Development**
- CEN 3031 Introduction to Software Engineering
- CEN 4072 Software Testing and Verification
- Remaining course to be determined

**VLSI**
- EEE 3396 Solid-State Electronic Devices
- EEE 3308C Electronic Circuits 1
- EEE 4331 Microelectronic Fabrication Technologies

Computational Science
- COT 4501 Numerical Methods
- CIS 4930 Sparse Matrix Algorithms
- CAP 4800 Systems Simulation

Proposed Computer Engineering Capstone Sequence

A sub-committee of the Computer Engineering Curriculum Committee was formed to examine the Computer Engineering capstone course sequence. Discussion notes of this sub-committee appear in [Capstone Course Discussion Notes]. The capstone course sequence is proposed to become a two-semester sequence for all students in the program. The first course will provide the training and integration of skills necessary for successful completion of the second course. The project itself will be carried out during the second course. It can range from one extreme (all software) to the other (all hardware) or any mixture of the two.

The first course in the sequence will be taught in two different sections. One of them will be a general design course that will prepare the student for either a software or hardware project. The CISE department will offer this section. The second section will be for students (typically in ECE) who plan to perform a project with a heavy hardware emphasis. The ECE department will offer this second section.

The second semester will also be done in two different sections: one for a software emphasis and to be offered by the CISE department, and one for a hardware emphasis to be offered by the ECE department. In this second design course, students will be required to use knowledge from multiple previous courses. It will be a team project, with an external advisor (a faculty member in any department, or an industrial advisor). An oral presentation and final written report will be required. The complete proposal appears in the CE website, at Capstone course proposal.

Timeline

The joint CISE/ECE Curriculum Committee met regularly over the 2011-2012 academic year and proposed these draft revisions to the program on April 10, 2012. The revisions are currently under discussion by the Computer Engineering faculty, and will likely undergo some changes before being formally voted on. We include these proposed changes to the program in this ABET Self-Study report to document our ongoing improvements to the program.

Documentation of these proposed changes is provided on the CE program website, including [CEN committee minutes] and the slides presented to the CE faculty on April 10, 2012 [Proposal to CE Faculty].
CRITERION 5. CURRICULUM

A. Program Curriculum

A.1. Plan of Study

Table 5-1 shows the Plan of Study for the software track of the Computer Engineering program. Table 5-2 shows the Study Plan for the Hardware track. The University of Florida operates on a semester system. The academic year begins and ends in August. There are two semesters averaging 15 weeks of instruction, plus a week of final examinations and two six-week summer terms. Semesters begin in August, January, and May, with summer term offered as a whole as Term C, or in two half-terms, with Term A beginning in May and Term B beginning in June.

A.2. Curriculum alignment with program educational objectives

Students graduating with the Bachelor of Science in Computer Engineering degree are prepared for engineering practice. Referring to Table 5-1 Basic-Level Curriculum, the curriculum requires in excess of one year a combination of college level mathematics and basic sciences. Required laboratories for chemistry and physics courses ensure experimental experience for every student in the program. The basic science courses are followed by engineering science courses, and courses in Computer Science and Electrical Engineering. The general education component consisting of a minimum of 18 semester hours of humanities, social science, and technical writing assures the student has achieved a well-rounded education. Exposure to aspects of engineering professionalism and ethics assures the student has been introduced to contemporary engineering issues. All these factors contribute to the achievement of Program Objective 1, to excel in a career utilizing their education in Computer Engineering, and to Program Objective 2, to continue to enhance their knowledge.

In the general education courses, students gain fresh perspectives, methods and tools for understanding the traditional and the newly discovered knowledge. The Composition courses equip students with the skills necessary to complete successfully the reading and writing requirements of their discipline. The Mathematical Sciences courses help students acquire concepts and skills in logic, inductive and deductive reasoning, and abstract and quantitative thinking. The Humanities courses enable students to think critically about what artists and thinkers (past and present) have to teach us about the non-material qualities of human beings and human values. The Social and Behavioral Sciences courses enable students to investigate human behavior in its social context. The Physical and Biological Science courses introduce students to the basic concepts of science and the scientific method and enhance awareness of scientific developments and their impact on society and the environment. All these factors contribute to the achievement of Program Objective 3, to be effective in multidisciplinary and diverse professional environments, and to Objective 4, to provide leadership and demonstrate professional integrity.
A.3. Pre-requisite structure support for outcomes attainment.

Students gain a solid foundation in mathematics and sciences before embarking in the Computer Engineering area. They are required to take a minimum of 39 hours (CEN, 42 hours in CEE) and in mathematics, statistics, and basic sciences. This includes 4 semesters of calculus and differential equations, as well as 4 semesters (with lab) of physics, chemistry, and biology. Computational linear algebra, discrete mathematics, engineering statistics, and numerical analysis are also required, as is professional communications. These courses give them the fundamental science and the mathematical tools to design and to analyze designs effectively.

General concepts in the introductory CIS course (COP 3504, formerly CIS 3020), logic, discrete mathematics, data structures and algorithms prepare students for advanced software work. Work in these courses is generally at the individual level to ensure that all students are prepared for later assignments. There is small scale design in the introductory course, but design starts to play a major role in Data Structures and Algorithms. In this course, software components are built and modified. Typical components include linked lists, queues, search trees, hash tables, various graph and tabular structures, etc. Figure 5-1 below shows the required courses with significant design component.

![Diagram of Design in Computer Engineering Program](image-url)

*Figure 5-1 – Prerequisites and Design in Computer Engineering Program*
In parallel, students learn basic Electrical Engineering concepts and practice in circuits, solid-state electronics (though this is optional for the software track, most software track students still take it to fulfill engineering breadth requirements), and Signals and Systems. These courses have some design component, but design becomes a major emphasis in Digital Logic and Computer Systems, and in Digital Design. In these courses, logic circuits are built from the basic elements to large-scale integration. In Digital Logic and Computer Systems, the hardware and firmware for a complete computer system are built. This avenue culminates in the microprocessors course, in which applications for microprocessor systems are built using additional hardware as needed and programming a microprocessor in assembly language.

COP 3504 (Adv. Programing Fundamentals) is a prerequisite for several of the advanced digital design courses. The Computer Organization, Digital Logic and Computer Systems, and Microprocessor Applications courses act as bridges between the CEE side and the CEN side of the program, and relate machine instructions to programming languages and higher constructs. While in the Microprocessor Applications course, a computer is programmed in assembly language, a high level programming language (for the most part) is used to modify and extend an operating system in the Operating Systems class. In addition to putting together hardware and software in Operating Systems, algorithms, data structures, and analysis play a large role.

In both Software Engineering and Operating Systems, large software systems are designed, built, and maintained. In Operating Systems, students must design and implement a system call, then integrate it with a real operating system. They then must redesign and integrate various other components, such as device drivers, memory management routines, and file system components. In Software Engineering, teams design and build a large software project over the course of the semester. Though the nature of the projects may vary, it is a significant undertaking that requires a sustained team effort. For example, a recent project had teams build a movie rental management system. This project had two versions, with refactoring between versions. Students were required to estimate effort and log actual effort needed for each task. Other recent projects include games developed under the Android operating system to run on cell phones, software packages to visualize the behavior of algorithms, and social networking applications. All projects in Software Engineering require students to work in multi-disciplinary groups, in sub-groups that design and implement user interfaces, communications, artificial intelligence (in games), and graphics.

Software track students are required to take Computer Network Fundamentals and Numeric Methods. Design of network protocols and distributed applications is a major emphasis of the former course, while design and analysis of numeric algorithms is emphasized in the latter.

Two semesters of other engineering core courses round out the fixed course requirements. Several elective courses are also available to provide particular concentration in the areas of electronics, communications, databases, software engineering, artificial intelligence, etc.
Many of these courses have significant design components, but we do not count on them to provide the necessary design experience.

Team concepts are taught and team work is required in Software Engineering, as is resource estimation. Students also are required to communicate orally and in writing throughout that course and in the capstone course. The course in legal and social issues addresses societal context, ethics, contemporary issues, and the need for lifelong learning directly, though these are also addressed throughout the curriculum.

Many of the courses have strong engineering design components, reflected in both paper designs required in homeworks and examinations, as well as the implementations required in projects. Required courses with particularly strong design components include:

- **COP 3530 Data Structures and Algorithms (both CEN and CEE):** Students are required to design and implement or to augment existing data structures, and to analyze their performance. Functional and performance characteristics are the driving requirements.

- **CEN 3031 Software Engineering (both):** Students work in teams to create specifications and then design software that meets those specs. They also design test mechanisms to ensure their software is robust.

- **COT 4501 Numerical Methods (CEN):** CISE students are required to design and analyze numerical algorithms for performance and for error propagation.

- **EEL 3112C Circuits 2 (CEE) and EEL 4712C Digital Design (both):** Students design and implement electrical and logical circuits and components as specified.

- **EEL 4744C Microprocessor Applications (both):** Students design, build, and program whole computers from components. Computer design also takes place in CDA 3101 Computer Organization.

- **COP 4600 Operating Systems (both):** Students have several projects in which they enhance an existing operating system to meet a requirement, then verify their modifications.

- **CNT 4007C Computer Network Fundamentals (CISE):** Requires students to develop code that satisfies the requirements for distributed applications or network management as required.

- **CIS 4914 Senior Design (CEN) or EEL 3923C / EEL 4924 Senior Design (CEE):** The capstone course (in either option) has design as its primary focus. Here, the student must obtain requirements from the sponsor, create and execute the design, and document both the process and the product. Alternatively, students can take the CIS / EEL 4912C / 4913C sequence of Integrated Product and Process Design, in which students interact closely with an industrial partner to create a design that meets a specific need.

In addition, most of the elective courses have a significant design component.
A.4. Program prerequisite structure.

Figure 5-2 shows the prerequisite structure of the Computer Engineering program. Common courses to both tracks are shown in the middle, ECE courses on the left, and CISE courses on the right.

A.5. Hours and depth of study requirements.

Referring to Tables 5-1 and 5-2, the minimum ABET requirements for credit hours in basic math and science is 32. The software track exceeds this minimum with 39 hours, and the Hardware track exceeds it with 42 hours. The minimum requirement for credit hours in design is 48. The software track exceeds this with 69 hours, and the Hardware track exceeds it with 65 hours. Percentage-wise, the minimum ABET requirement for math and science is 25%, and in design it is 37.5%. The software track exceeds these with 31% and 55%, and the Hardware track exceeds these with 31% and 51.5%, respectively.

A.6. Major design experience

Students have two options for the capstone design experience. They may take the traditional, one semester Senior Project class (CEN students), or the two-semester Senior Design sequence (CEE students). Alternatively, they may apply for the selective and limited two-semester IPPD program.

In the traditional Senior Project class students work either individually or on a small team (2-3 students, usually all in the Computer Engineering or Electrical Engineering program) on a project that allows them to make use of the skills and knowledge they have acquired over the course of the program. This is done under the supervision of a faculty member from either the CISE or the ECE department. All students in the Senior Project course meet regularly with the course supervisor, and they make presentations on their progress, ending with a Senior Project presentation to the group. A specific form for evaluating outcomes in Senior Design is filled out and signed by the project advisor. [Senior Project Advisor Form]
Alternatively, students in either track may apply for the Integrated Product and Process Design [IPPD] program, which entails a two-semester commitment under the supervision of a faculty member, usually working on a project for an external institution or corporation that sponsors the project. IPPD requires two semesters, with the second semester counting in the curriculum as a technical elective. Here, an organization (the external sponsor) sets a goal, and the team of students and their faculty coach go through the engineering and business processes needed to achieve that goal. These include requirements specification, business plan, technology planning, system design and specification, test planning, etc.

Typically the interaction with the organizational liaison is high, but the student team is given a fair amount of leeway in solving the problems with which they are tasked. Visits to the organization’s site are arranged, and all teams give a program-wide presentation at the end of each semester. During the course of the terms, teams of coaches review each student teams’ progress and plans. The first term involves regular lectures on engineering development processes in addition to learning about team concepts and the team-specific tasks. Several deliverables are required during the course of the term, culminating in the Preliminary System Level Design Document that is presented at the end of the first term. The second term usually involves implementation of that design, along with testing and a few presentations for tracking purposes. It culminates in final demonstration and delivery along with 20-minute talks and poster sessions, in a formal, conference-style setting attended by representatives of the external organizations. A specific Outcomes Evaluation form is filled out for each IPPD team [IPPD Outcomes Evaluation Form]

In both the regular senior project and in the IPPD option, the student is obligated to meet the requirements of the project as specified by the project “customer.” This may be the supervising faculty, or it may be a sponsor from outside the department. The student is required to document these, and to obtain the written agreement of the customer before implementing the project. Student and customer communicate and negotiate to determine standards and realistic constraints, compliance with which is assessed in Outcome (c).

A.7. Cooperative education.

CISE Department Co-Op program

The CISE Department offers students an opportunity to integrate classroom instruction with practical work experience. The Co-op/Internship program gives students the opportunity to verify academic and vocational interests, gain valuable experience in the chosen field prior to graduation, and earn income that can be used to defray college expenses. The work experience is conducted under approved industrial, business, institutional, or governmental agency supervision.

CIS 4940, Practical Work (Internship) is a one credit course that may be applied toward the technical electives requirement for CISE undergraduate degree programs. Students may register for CIS 4940 a maximum of three times for a total of three credits. It may only be taken S/U (Satisfactory/Unsatisfactory). Registration for the course is handled through CISE Student Services.
To register for Practical Work (Internship), a formal letter from the sponsoring company describing the nature of the internship must be on file in Student Services before the end of the registration drop/add period. The letter must indicate that the student will be working approximately 40 hours a week over the term of the semester (approximately 600 hours total) and that the internship is Computer Science/Engineering related, provide a description of the student's responsibilities, and provide detailed contact information of the person or persons who will be supervising the student during the internship.

To successfully complete Practical Work (Internship) with a grade of Satisfactory, a 2-4 page Post-Internship Report written by the student, and a formal Evaluation Letter written by the student's supervisor(s) is due in the CISE Student Services office one week prior to the end of finals week. The Evaluation Letter must include a review of the student's work during the internship, an evaluation by the student's supervisor(s) indicating whether or not the student has satisfactorily completed the internship, and detailed contact information of the person or persons who have supervised and evaluated the student's performance.

CIS 4949 Co-op is a one-credit course that may be applied toward the technical electives requirement for CISE undergraduate degree programs. Students may register for CIS 4949 a maximum of three times for a total of three credits. It may only be taken S/U (Satisfactory/Unsatisfactory). The Co-op can either be taken as an Alternating Co-op in which a student works full-time in non-consecutive semesters with a company while only registering for CIS 4949 or as a Parallel Co-op in which a student works part-time for consecutive semesters with a company while enrolled as a full-time student at UF. Registration for the course is handled through CISE Student Services. Co-op students are also asked to meet with an advisor at the Career Resource Center before the Co-op begins. The Career Resource Center [CRC] will keep a file on the students Co-op experience.

To register for a Co-op a formal letter from the sponsoring company describing the nature of the Co-op must be on file in Student Services before the end of the registration drop/add period. In the case of an Alternating Co-op, the letter must indicate that the student will be working approximately 40 hours a week over the term of the semester (approximately 600 hours total). In the case of a Parallel Co-op, the letter must indicate that the student will be working approximately 20 hours a week over the term of the semester (approximately 300 hours total). The letter must also indicate that the internship is Computer Science/Engineering related, provide a description of the student's responsibilities, and provide detailed contact information of the person or persons who will be supervising the student during the internship.

To successfully complete the Co-op with a grade of Satisfactory, a 2-4 page Post-Internship Report written by the student, and a formal Evaluation Letter written by the student's supervisor(s) is due in the CISE Student Services office within one week prior to the end of finals week. The Evaluation Letter must include a review of the student's work during the internship, an evaluation by the student's supervisor(s) indicating whether or not the student has satisfactorily completed the internship, and detailed contact information of the person or persons who have supervised and evaluated the student's performance.
ECE Department Co-OP program

The ECE Department’s Cooperative Education Program (Co-Op) offers students an opportunity to integrate classroom instruction with practical work experience. The program gives students the opportunity to verify academic and vocational interests, gain valuable experience in the chosen field prior to graduation and earn income that can be used to defray college expenses. The job assignment must be related to Electrical Engineering design. Job assignments such as technician, data entry operator, and clerical are not acceptable. Students wishing to participate in the Co-Op Program must have completed their freshman year (30 semester hours) and have a grade point average of 2.00 or higher. The ECE Department prefers that students wait until they have completed some of the major Electrical and Computer Engineering courses before participating in co-ops or internships. Students are expected to engage in work experiences that are relevant to their professional discipline and increasingly challenging in each successive term.

The ECE Department’s Co-Op Program has three plans. The first plan is “alternating”. The student alternates between a full-time academic schedule one term and full-time work assignment the following term for a minimum of two work semesters. The second plan is “consecutive”, where the student works two consecutive terms. The third plan is “parallel”. The student, while carrying academic course work, is also employed in an appropriate work assignment. In either work plan, the student registers for the Co-Op course number EEL 4949. This course only carries the grade option of Satisfactory/Unsatisfactory. The Department does not allow EE students to receive credit towards the degree for the “parallel” assignment. The Department does not believe that an EE student can receive the full benefit of the full-time Co-Op experience while only working part-time.

For students who do not wish to participate in the Co-Op Program, ECE offers students a one-term practical work experience called Internship. Students on an Internship are treated the same as Co-Op students. In order to receive technical elective credit, the job assignment must be related to Electrical Engineering and follow the same guidelines and procedures as the Co-Op Program. Students register for 1 credit of EEL 4948, which carries the grade option of Satisfactory/Unsatisfactory.

A.8. Course materials.

Course materials (syllabi, assessments, course outcome reports, course committee reports, sample student work) appear on the ABET CEN program website [Assessments]. Student sample work for each course, for each semester, appears in the same directory as the assessments of outcomes in that course. Copies of textbooks will be available during the evaluator visit.

B. Course Syllabi

Course Syllabi appear in Appendices A1 and A2.
## Table 5-1 Curriculum (Software Track)

*(Computer Engineering, Software track)*

List all courses in the program by term starting with first term of first year and ending with the last term of the final year.

<table>
<thead>
<tr>
<th>Course (Department, Number, Title)</th>
<th>Indicate Whether Course is Required, Elective or a Selected Elective by an R, an E or an SE.</th>
<th>Curricular Area (Credit Hours)</th>
<th>Engineering Topics Check if Contains Significant Design (√)</th>
<th>General Education</th>
<th>Other</th>
<th>Last Two Terms the Course was Offered: Year and, Semester, or Quarter</th>
<th>Average Section Enrollment for the Last Two Terms the Course was Offered</th>
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<td>MAS 3114</td>
<td>Computational Linear Algebra</td>
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<td>Year 4 – Fall</td>
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<td>COP 4600—Operating Systems</td>
<td>R</td>
<td>3 (√)</td>
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<td>CEN 3031—Introduction to Software Engineering</td>
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</table>

Add rows as needed to show all courses in the curriculum.

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<td>Total must satisfy either credit hours or percentage Minimum Semester Credit Hours</td>
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<td>48 Hours</td>
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<td>Minimum Percentage</td>
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</table>

1. For courses that include multiple elements (lecture, laboratory, recitation, etc.), indicate the average enrollment in each element.
2. Required courses are required of all students in the program, elective courses are optional for students, and selected electives are courses where students must take one or more courses from a specified group.

Instructional materials and student work verifying compliance with ABET criteria for the categories indicated above will be required during the campus visit.
Table 5-2 Curriculum (Hardware Track)
(Computer Engineering, Hardware track)

List all courses in the program by term starting with first term of first year and ending with the last term of the final year.

<table>
<thead>
<tr>
<th>Course (Department, Number, Title)</th>
<th>Indicate Whether Course is Required, Elective or a Selected Elective by an R, an E or an SE.2</th>
<th>Curricular Area (Credit Hours)</th>
<th>Engineering Topics Check if Contains Significant Design (√)</th>
<th>General Education</th>
<th>Other</th>
<th>Last Two Terms the Course was Offered: Year and, Semester, or Quarter</th>
<th>Average Section Enrollment for the Last Two Terms the Course was Offered1</th>
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<tbody>
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<td>Co-op or internship, if desired</td>
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<table>
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<tr>
<td>EEL 4744C — Microprocessor Applications</td>
<td>R</td>
<td>4 (√)</td>
<td>F2011, S2012 9</td>
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<tr>
<td>STA 3032 Engineering Statistics</td>
<td>R</td>
<td>3</td>
<td>F2011, S2012 130</td>
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<tr>
<td>Electrical Eng. technical elective (any EEE/EEL except EEL3003)</td>
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### Year 4 – Spring

<table>
<thead>
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<th>Semester Code</th>
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<th>S2012</th>
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</thead>
<tbody>
<tr>
<td>COP 4600 — Operating Systems</td>
<td></td>
<td>R</td>
<td>3 (√)</td>
<td></td>
</tr>
<tr>
<td>CEN 3031 — Introduction to Software Engineering</td>
<td></td>
<td>R</td>
<td>3 (√)</td>
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<tr>
<td>EEL 3923C Electrical and Computer Engineering Design 1</td>
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<td>R</td>
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<td>EGN 4034 — Professional Issues in Engineering</td>
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### Year 5 – Fall

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<th>S2012</th>
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<tr>
<td>EEL 4712C — Digital Design or EEL 4713C Digital Comp. Architecture</td>
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<td>SE</td>
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<tr>
<td>Electrical Eng. technical electives (any EEE/EEL except EEL3003)</td>
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<td>SE</td>
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<tr>
<td>Technical Elective, from approved list</td>
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### Year 5 – Spring

<table>
<thead>
<tr>
<th>Course Title</th>
<th>Credit</th>
<th>Semester Code</th>
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<th>S2012</th>
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<tr>
<td>EEE 4924 Senior Design</td>
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<tr>
<td>Technical Elective, from approved list</td>
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</table>

Add rows as needed to show all courses in the curriculum.

### TOTALS-ABET BASIC-LEVEL REQUIREMENTS

<table>
<thead>
<tr>
<th>Minimum Semester Credit Hours</th>
<th>32 Hours</th>
<th>48 Hours</th>
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</thead>
<tbody>
<tr>
<td>Minimum Percentage</td>
<td>25%</td>
<td>37.5%</td>
</tr>
</tbody>
</table>

1. For courses that with multiple elements (lecture, laboratory, recitation, etc.), the average enrollment in each element is indicated.
2. Required courses are required of all students in the program, elective courses are optional for students, and selected electives are courses where students must take one or more courses from a specified group.

Instructional materials and student work verifying compliance with ABET criteria for the categories indicated above will be required during the campus visit.
CRITERION 6. FACULTY

A. Faculty Qualifications

The faculty is well educated and prepared for their educational and research missions. All tenure-track faculty have doctoral degrees, as do most of our instructors. The rest of our instructors hold at least Masters degrees. The faculty adequately covers all areas of the curriculum. The faculty are extremely well qualified, productive, and nationally and internationally recognized. Faculty Vitae appear in Appendices B1 and B2. The faculty qualifications appear in Tables 6-1a (CISE Department) and 6-1b (ECE Department).

The faculty span the spectrum of Computer Engineering, from specialists in theoretical Computer Science in CISE, to hardware specialists in ECE. In CISE, 15 faculty teach and do research in theoretical Computer Science areas, 21 other work in the area of computer systems, and 22 mainly focus on applications. In ECE, there are 38 full-time or tenure accruing faculty and 3 full time lecturers.

Many faculty bridge two or more areas, and several instructors primarily teach service courses. Many have industrial experience, and many have on-going consulting activities (Davis is the author of $x=A\backslash b$ in MATLAB when $A$ is sparse, for example). Their education and experience allow the faculty to provide students with meaningful examples relating conceptual material in the class to real-life applications. All faculty members are very active in research and professional activities. Several are editors of prestigious journals. Most participate in conference program committees and other conference activities. Many have served on NSF panels, and a few have served as program directors in the NSF (Chen, Chow). A few hold degrees in other areas related to their research. However, as is typical for Computer Engineering faculty, few are registered as professional engineers. Many professors have industrial experience and continuing industrial relationships. A few have their own companies, and regularly consult for industry. In addition, IPPD coaches and the numerous professors with industrial research contracts and grants have frequent contact with industry and real-life engineering issues. Many have long-standing industrial relationships with companies such as Lockheed-Martin, Cisco, Intel, and Raytheon). Our semi-annual Industrial Advisory Board meetings provide further opportunities for industrial interactions.

The faculty members are extremely well qualified, productive, and nationally and internationally recognized. They publish hundreds of journal and conference proceedings papers annually.
B. Faculty Workload

Given the diversity of the Computer Engineering faculty, there are a variety of teaching assignments. The system is designed to be flexible, accommodating a wide range of individual interests and skills, while maintaining accountability and a reasonable level of equity. Faculty teaching load is determined by examining the level of research activity to try and maintain fairness in workload assignments. Faculty in both Departments are assigned between 2 and 5 courses in the nine month academic year. Courses are assigned collegially. In the CISE Department, courses are assigned to faculty through interaction with the Associate Chair. The ECE Department has five divisions and courses are assigned to a division, sometimes more than one division. Faculty in the division hold a meeting to determine individual course assignments. The associate chair coordinates these requests for assignment and any problems and gaps are identified. These are usually solved with faculty input and minimal effort.

Tables 6-2a (CISE Department) and 6-2b (ECE Department), Faculty Workload Summary, describe the faculty workload.

C. Faculty Size

With 73 tenured or tenure-track faculty (35 in CISE, 41 in ECE), the number of faculty is more than adequate to teach the courses in the program. Each department has a professional advising staff; CISE has a Student Services Center with two full-time undergraduate advisors and two support staff for them, while ECE has the Undergraduate Services Office. These shoulder the primary advising burden for undergraduates, providing tutoring, and interfacing with the Career Resource Center. This approach provides consistent advising for the undergraduates, and allows faculty to concentrate their interactions in curricular matters and life advice, rather than on the more mechanical aspects of program compliance. In each Department, the departmental governance positions are adequately staffed, as are the college-level positions. In addition, each department contributes their expertise to the university senate and to other College and University-level bodies. As a faculty, we supervise the ACM programming team, the student chapters of the ACM and the IEEE, and have been advisors to many undergraduate organizations (Theta Tau, Alpha Phi Omega, Aikido Club to name a few). In addition, several faculty members have served as minority mentors and advisors to minority student organizations, have hosted minority undergraduates for a summer research experience, have hosted high school teachers and high school students for summer internships, etc. Beyond the classroom and the laboratory, students have extended contact with professors through the IPPD program, ACM programming team, IEEE CS and ACM supervision, etc. All faculty are required to keep regular, posted office hours, and routinely meet students outside these times as needed.

The CISE Department has added four faculty in the 2007-2011 period: Drs. Alireza Entezari, Ahmed Helmy, Rong Zhang, and Daisy Wang. The ECE Department has added ten new faculty since the last ABET visit in 2006. They are Drs. Ann Gordon-
Ross, Greg Stitt, Qun Jane Gu, Xiaolin Andy Li, Nima Maghari, Kamran Mohseni (dual appointment with Mechanical and Aerospace Engineering), Robert Moore, Sean Meyn, Allen Turner, and Y. K. Yoon.

The majority of the faculty are considered career advisors. Students are encouraged to speak with the faculty concerning their career goals and objectives. Faculty also serve as advisors to student groups and as advisors to students engaged in departmental high honors theses. All faculty are encouraged to engage in their own professional development. Interactions with industry are encouraged and are accomplished through sponsored and unsponsored research as well as student design projects. Several faculty serve as mentors in the IPPD program.

All faculty serve on departmental, college, and university committees to ensure effective communication and efficient operation and delivery of our program. Many faculty serve as industry consultants or, in some cases, engage in entrepreneurial endeavors that enhance the prestige of the university as well as providing the students with inspiration and an enhanced educational perspectives and opportunities.

D. Professional Development

Faculty members are very active in research and professional activities. Several are editors of prestigious journals. Most participate in conference program committees and other conference activities. Many have served on NSF panels and a few have served as program directors in the NSF (Chen, Chow). A few hold degrees in other areas related to their research. However, as is typical for Computer Engineering faculty, very few are registered as professional engineers.

Professional development opportunities for faculty fall into two categories; research and education. From the research perspective, most faculty routinely attend professional conferences where they are authors, session chairs, program committee members and chairs, and conference chairs. This aspect of professional development for faculty provides prestige for the university and ensures that the faculty is on the cutting-edge of the latest developments in their fields. This is turn helps to eliminate legacy material from coursework as well as allowing faculty to share with their students the latest trends and developments in the courses they teach. Faculty are also eligible for sabbaticals every six years. Many professors have industrial experience and continuing industrial relationships. A few have their own companies, and several regularly consult for industry. In addition, IPPD coaches and the numerous professors with industrial research contracts and grants have frequent contact with industry and real-life engineering issues. Many have long-standing industrial relationships. Our semi-annual Industrial Advisory Board meetings provide further opportunity for industrial interactions.

The university also provides numerous opportunities for faculty to enhance our skills as engineering educators. This includes university and college-level workshops on effective teaching skills. Both the university and the college have a guest lecture program where
innovators in engineering education from around the country are invited to speak on issues and methods of interest to engineering educators.

Both Departments, as well as the College of Engineering both routinely support travel expenses for faculty to attend workshops in teaching excellence such as those given by the American Society for Engineering Education (ASEE).

Sabbaticals are awarded by the University, via recommendation by the College. Faculty are eligible for a sabbatical after six years of service. Traditionally, there have been two types of sabbaticals: one semester with full-pay, or two-semesters with half-pay. Recently, UF added a new type of sabbatical, with full pay for two semesters. UF has committed to awarding a minimum of 50 such sabbaticals per year. Sabbatical policies are listed on UF’s Academic Personnel website [UF-sabbaticals]. In addition, UF offers the Faculty Enhancement Opportunity (FEO) program, with monetary awards for creative and flexible faculty development projects, including educational experiences and/or conferences, work with consultants/experts, and release time for intensive writing or creative work. The FEO program is administered by the Provost’s office [FEO].

E. Authority and Responsibility of Faculty

Input from the faculty concerning the guidance of the program is mainly received through regular faculty meetings. In addition faculty are encouraged to contact the Department Chair, Associate Chair, or Undergraduate Coordinator with any observations or concerns they may have. The faculty also have input on the success of our students at meeting the program objectives. The faculty monitor alumni and we closely monitor the success of our undergraduates in our own graduate program. They also monitor the survey and review board feedback to see if we need to incorporate program changes to better achieve the program objectives.
Table 6-1. Faculty Qualifications
Bachelor of Science in Computer Engineering

<table>
<thead>
<tr>
<th>Faculty Name</th>
<th>Highest Degree Earned- Field and Year</th>
<th>Rank</th>
<th>Type of Academic Appointment</th>
<th>Years of Experience</th>
<th>Professional Registration/ Certification</th>
<th>Level of Activity</th>
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<td>Arnold, David P.</td>
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Instructions: Complete table for each member of the faculty in the program. Add additional rows or use additional sheets if necessary. Updated information is to be provided at the time of the visit.

1. Code: P = Professor  ASC = Associate Professor  AST = Assistant Professor  I = Instructor  A = Adjunct  O = Other
2. Code: T = Tenured  TT = Tenure Track  NTT = Non Tenure Track
3. Code: FT = Full-time  PT = Part-time  Appointment at the institution.
4. The level of activity (high, medium or low) should reflect an average over the year prior to the visit plus the two previous years.
<table>
<thead>
<tr>
<th>Faculty Name</th>
<th>Highest Degree Earned- Field and Year</th>
<th>Rank</th>
<th>Type of Academic Appointment</th>
<th>FT or PT</th>
<th>Years of Experience</th>
<th>Professional Registration/ Certification</th>
<th>Level of Activity</th>
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Bachelor of Science in Computer Engineering

<table>
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<th>Faculty Name</th>
<th>Highest Degree Earned- Field and Year</th>
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<th>Years of Experience</th>
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<td>Fang, Yuguang ‘Michael’</td>
<td>PhD, EECS, 1997</td>
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<td>Fortes, Jose</td>
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Instructions: Complete table for each member of the faculty in the program. Add additional rows or use additional sheets if necessary. Updated information is to be provided at the time of the visit.

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<thead>
<tr>
<th>Faculty Name</th>
<th>Highest Degree Earned- Field and Year</th>
<th>Rank</th>
<th>Type of Academic Appointment</th>
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<tr>
<td>Fox, Robert</td>
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<td>T, FT</td>
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<tr>
<td>Gader, Paul</td>
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<td>George, Alan</td>
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<td>Gordon-Ross, Ann</td>
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<td>Gu, Qun ‘Jane’</td>
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<td>Gugel, Karl</td>
<td>PhD, ECE, 1993</td>
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|                   | Highest Degree Earned- Field and Year | Rank | Type of Academic Appointment |=| Years of Experience | Professional Registration/ Certification | Level of Activity |
|-------------------|--------------------------------------|------|-------------------------------||---------------------|-------------------------------|------------------|
| Fox, Robert       | PhD, EE, 1986                        | ASC  | T, FT                         | | 7                  | 26          | None             | M, H, L          |
| Gader, Paul       | PhD, MA, 1986                        | P    | T, FT                         | | 3                  | 23          | 11               | M, H, H          |
| George, Alan      | PhD, CSC, 1991                       | P    | T, FT                         | | 7                  | 25          | 15               | M, H, L          |
| Gordon-Ross, Ann  | PhD, CSE, 2007                       | AST  | TT, FT                        | | 0                  | 8           | 5                | H, H, L          |
| Gu, Qun ‘Jane’    | PhD, EE, 2007                        | AST  | TT, FT                        | | 3                  | 2           | 2                | M, H, L          |
| Gugel, Karl       | PhD, ECE, 1993                       | O    | T, FT                         | | 13                 | 10          | 10               | M, L, L          |

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Instructions: Complete table for each member of the faculty in the program. Add additional rows or use additional sheets if necessary. Updated information is to be provided at the time of the visit.

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4. The level of activity (high, medium or low) should reflect an average over the year prior to the visit plus the two previous years.
### Table 6-1. Faculty Qualifications (continued)
**Bachelor of Science in Computer Engineering**

<table>
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<th>Highest Degree Earned- Field and Year</th>
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<th>Years of Experience</th>
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### Table 6-2. Faculty Workload Summary
**Bachelor of Science in Computer Engineering**

<table>
<thead>
<tr>
<th>Faculty Member (name)</th>
<th>PT or FT(^1)</th>
<th>Classes Taught (Course No./Credit Hrs.) Term and Year(^2)</th>
<th>Program Activity Distribution(^3)</th>
<th>% of Time Devoted to the Program(^5)</th>
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</table>

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1. FT = Full Time Faculty or PT = Part Time Faculty, at the institution
2. For the academic year for which the self-study is being prepared.
3. Program activity distribution should be in percent of effort in the program and should total 100%.
4. Indicate sabbatical leave, etc., under "Other."
5. Out of the total time employed at the institution.
Table 6-2. Faculty Workload Summary (continued)
Bachelor of Science in Computer Engineering

<table>
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<tr>
<th>Faculty Member (name)</th>
<th>PT or FT¹</th>
<th>Classes Taught (Course No./Credit Hrs.) Term and Year²</th>
<th>Program Activity Distribution³</th>
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</table>

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2. For the academic year for which the self-study is being prepared.
3. Program activity distribution should be in percent of effort in the program and should total 100%.
4. Indicate sabbatical leave, etc., under "Other."
5. Out of the total time employed at the institution.
Table 6-2. Faculty Workload Summary (continued)
Bachelor of Science in Computer Engineering

<table>
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<tr>
<th>Faculty Member (name)</th>
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<th>Classes Taught (Course No./Credit Hrs.) Term and Year²</th>
<th>Program Activity Distribution³</th>
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3. Program activity distribution should be in percent of effort in the program and should total 100%.
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5. Out of the total time employed at the institution.
**Table 6-2. Faculty Workload Summary (continued)**  
**Bachelor of Science in Computer Engineering**

<table>
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<th>Faculty Member (name)</th>
<th>PT or FT(^1)</th>
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<th>Program Activity Distribution³</th>
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Bachelor of Science in Computer Engineering

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<th>Classes Taught (Course No./Credit Hrs.)</th>
<th>Program Activity Distribution³</th>
<th>% of Time Devoted to the Program⁵</th>
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5. Out of the total time employed at the institution.
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<th>Faculty Member (name)</th>
<th>PT or FT¹</th>
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5. Out of the total time employed at the institution.
Table 6-2. Faculty Workload Summary (continued)
Bachelor of Science in Computer Engineering

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<tr>
<th>Faculty Member (name)</th>
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### Table 6-2. Faculty Workload Summary (continued)
**Bachelor of Science in Computer Engineering**

<table>
<thead>
<tr>
<th>Faculty Member (name)</th>
<th>PT or FT¹</th>
<th>Classes Taught (Course No./Credit Hrs.) Term and Year²</th>
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2. For the academic year for which the self-study is being prepared.
3. Program activity distribution should be in percent of effort in the program and should total 100%.
4. Indicate sabbatical leave, etc., under “Other.”
5. Out of the total time employed at the institution.
Table 6-2. Faculty Workload Summary (continued)
Bachelor of Science in Computer Engineering

<table>
<thead>
<tr>
<th>Faculty Member (name)</th>
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<th>Classes Taught (Course No./Credit Hrs.) Term and Year</th>
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3. Program activity distribution should be in percent of effort in the program and should total 100%.
4. Indicate sabbatical leave, etc., under "Other."
5. Out of the total time employed at the institution.
Table 6-2. Faculty Workload Summary (continued)
Bachelor of Science in Computer Engineering

<table>
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<tr>
<th>Faculty Member (name)</th>
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<th>Classes Taught (Course No./Credit Hrs.) Term and Year&lt;sup&gt;2&lt;/sup&gt;</th>
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Table 6-2. Faculty Workload Summary (continued)
Bachelor of Science in Computer Engineering

<table>
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<tr>
<th>Faculty Member (name)</th>
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**Table 6-2. Faculty Workload Summary (continued)**

**Bachelor of Science in Computer Engineering**

<table>
<thead>
<tr>
<th>Faculty Member (name)</th>
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Table 6-2. Faculty Workload Summary (continued)
Bachelor of Science in Computer Engineering

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<th>Faculty Member (name)</th>
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<th>Classes Taught (Course No./Credit Hrs.) Term and Year²</th>
<th>Program Activity Distribution³</th>
<th>% of Time Devoted to the Program⁵</th>
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5. Out of the total time employed at the institution.
### Table 6-2. Faculty Workload Summary (continued)

#### Bachelor of Science in Computer Engineering

<table>
<thead>
<tr>
<th>Faculty Member (name)</th>
<th>PT or FT</th>
<th>Classes Taught (Course No./Credit Hrs.) Term and Year</th>
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5. Out of the total time employed at the institution.
<table>
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<th>Faculty Member (name)</th>
<th>PT or FT(^1)</th>
<th>Classes Taught (Course No./Credit Hrs.) Term and Year(^2)</th>
<th>Program Activity Distribution(^3)</th>
<th>% of Time Devoted to the Program(^5)</th>
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| Zhang, Rong           | FT              | Fall 2011: COP3502  
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                     | 94 | 0 | 6 | 100 |
| Zmuda, Henry          | FT              | F 2011: EEL4458-3, EEL5441-3  
                     | Sp 2012: EEL4930-3, EEL6447-3 | 60 | 20 | 20 | 100 |

1. FT = Full Time Faculty or PT = Part Time Faculty, at the institution  
2. For the academic year for which the self-study is being prepared.  
3. Program activity distribution should be in percent of effort in the program and should total 100%.  
4. Indicate sabbatical leave, etc., under "Other."  
5. Out of the total time employed at the institution.
CRITERION 7. FACILITIES

A. Offices, Classrooms and Laboratories

CISE Departmental Offices, Classrooms, and Labs

In the main building (CSE Building, Bldg #42), CISE now has 48,096 square feet of space. Of this, 33,128 SF is office space, 5,495 SF is research laboratory space, 4,950 SF is instructional space, 1,875 SF is conference and seminar rooms, and 2,648 SF is miscellaneous space for systems rooms, maintenance, and storage. In addition, we now have 416 SF (room CSE-E202). The instructional space is either totally dedicated to CISE (as in labs) or is allotted to other departments on a per-semester basis if CISE does not claim it for a particular period (for classrooms only).

Six teaching laboratories are provided in the CSE Building, in addition to the general UF computer laboratories. These focus on multimedia, architecture, and graphics, and are of a size suitable for the classes assigned to them. Students often meet with instructors in these areas to acquire skills in a hands-on setting requiring specialized resources. These are:

- Room CSE E113: 16 PCs running Linux.
- Room CSE E114: 12 PCs running Linux, and 31 PCs running Windows.
- Room CSE E115: 24 PCs running Windows.
- Room CSE E116: 17 PCs running Linux.
- Room CSE E313: 3 PCS running Windows, and 12 PCs running Linux, all with high-end graphics capabilities.
- Room CSE E309: 18 PCS running Windows, used primarily for TAs to hold office hours.

CISE public labs are available 24/7 to anyone who has a CISE account. Labs are locked between 5PM and 7AM, and require a valid Gator 1 card to access them. Also, labs are monitored with security cameras. After-hours access to computer labs is granted using enrollment information. This information comes from the College of Engineering who in turn works directly with the Registrar. Downloads of enrollment information are done as often as the College permits. At the beginning of the semester downloads are done weekly. Afterwards, downloads are only done by special requests.

Four additional labs are available for course-specific work, corresponding to our four main Research Centers:

- Center for Vision, Graphics, and Medical Imaging (CVGMI)
- Database Systems Research and Development Center
- Mobile and Pervasive Computing Laboratory
- Computational Science and Intelligence Laboratory
Although these laboratories are primarily for research, the faculty who operate them often engage undergraduate students in their activities.

ECE Departmental Offices, Classrooms, and Labs

The ECE Department provides separate, well-equipped student teaching labs for electronic circuits, digital logic, communications, digital design, control systems, power systems, microcontrollers, and senior design. These laboratories are located on the second floor of the New Engineering Building and are available during laboratory class hours.

Since the 2006 ABET review significant investment has been made to ensure that the undergraduate educational labs are safe, comfortable, and equipped with appropriate, well-maintained instrumentation. Significant space relocation has resulted in a smoothly running environment free from usage conflicts. Laboratory infrastructure (work benches, lab computers, etc.) has been replaced or upgraded. Lab fees assessed on the students are now used to depreciate equipment so that measurement equipment is upgraded or replaced as soon as needed.

The Circuits Lab, located in NEB 250, includes 14 seats of National Instruments LabView and ELVIS II (Educational Library Virtual Instrument Suite) stations and associated multi-function I/O DAQ cards. These provide the students with a full complement of test and measurement equipment including a power supply, a digital multimeter, an oscilloscope and a function generator. Students construct various types of circuits and perform voltage, current and resistance network measurements to reinforce theory.

The Digital Logic Lab, located in NEB 248, students assemble and solder a complete printed circuit board that they use to program their CPLDs (Complex Programmable Logic Device), used to analyze the performance of digital logic circuits.

The Electronics Circuits Lab, located in NEB 213B, has Tektronix storage oscilloscopes and Agilent power supplies and function generators, which facilitate the study and analysis of both analog and digital electronics. Additionally, each seat has a workstation for PSpice or Multisim circuit simulations.

In the Digital Design and Microprocessors Lab, located in NEB 281, has Tektronix oscilloscopes and logic analyzers to explore the numerous functions and abilities of their microprocessor or Field-Programmable Gate Array (FPGA) development boards.

The Communications and Controls Lab, located in NEB 211, allows students the opportunity to explore a wide range of both digital and analog communication circuits. This lab is shared with the control systems lab. Each station is equipped with Agilent spectrum analyzers, frequency generators, mixed signal oscilloscopes, and power supplies. Each seat also has a workstation, which supports PSpice, LTspice, or Multisim circuit analysis along with Mathcad and Matlab.
The Power Lab, located in NEB 289, has four LabVolt Mechanical Training Systems allowing students to explore the principals of electric machines, transformers and magnetic circuits. Additionally, students see the effects that different loads (resistive, capacitive and inductive) have on a multi-phase power system. Each station also comes equipped with an Agilent mixed signal oscilloscope, power supply, function generator, and digital multimeter, as well as workstations with Pspice, LTspice, MathCAD, and MatLab.

The Senior Design Lab, located in NEB 212, has a variety of all the above equipment that the students use to develop a working product, which could potentially be patented and sold. This course is one of the capstones of the Electrical and Computer Engineering students’ education; students must utilize analog and digital design as well as software development to successfully develop their projects. Students get the benefit of experienced consultation from our lab engineers for design advice, power efficiencies, and analog and digital circuit troubleshooting. Additionally, successful students learn how to protect their product and intellectual property, and develop a business plan for its manufacture.

B. Computing Resources

University Laboratories:

UF Academic Technology manages and coordinates general computing equipment across the UF campus. They operate six laboratories:

The Architecture 188 Open Area has 4iMacs, 16PCs, and 5 color scanners. The ARCH 116 Classroom has 32 PCs and a teacher workstation. The ARCH 120 classroom has 22 iMacs, 5 color scanners, and teacher workstation. [ARCH]

CBD Building 105 operates 24 PCs, 6 color scanners, and a teacher workstation in a classroom (room 110B). They provide 4 PCs in an open area (room 110A). [CDB]

The CSE E211 Open Area operates 191 PCs, 11 iMacs, 10 laptop plug-in stations, and 25 color scanners. Up to 98 of the PCs can be reserved for student exams. The CSE E211A classroom operates 29 PCs, 8 color scanners, and a teacher workstation. This facility is located in the same building as the CISE Department, which makes it very convenient for students. [CSE]

The Hub 0120 and 0124 Computer Area operates 13 PCs. There is also a large area for students to congregate near these facilities (including a StarBucks), which facilitates collaboration. [HUB]

The Normal Hall G514 Open Area operates 16 iMacs, 13 PCs, and 5 color scanners. The Normal G514i classroom operates 23 iMacs and a teacher workstation. The Norman G512 classroom operates 26 iMacs, 6 color scanners, and teacher workstation. [NOR]
The Weil Hall 408 Open Area operates 18 PCs and 5 color scanners. The Weil 408A classroom operates 26 PCs, a color scanner, and a teacher workstation. The Weil Hall classrooms in 408A, 408D, and 408E each operate 27 PCs, a color scanner, and a teacher workstation.

**CISE Departmental Computing Resources**

Five CPU servers (a Solaris SPARC, two Linux AMD 64 and two Windows 20008R2 servers) are available via SSH, VNC or remotedesktop to all users to run jobs on, and to log in to from remote locations. Services provided include web, email, database (Oracle, Postgres and MySQL), Kerberos/LDAP authentication, DNS, DHCP, backups, samba, NFS, and security related services. These tend to be some of the fastest machines in the department and have the most memory.

All faculty offices are equipped with a Windows or Linux workstation. Standard software installations include Ubuntu 10.04 or Windows 7, Java, jGRASP, many Microsoft packages due to the Microsoft Development Academic Alliance, Mozilla Firefox, Second Life, and XMing (X Windows on a Windows PC). Database software includes MySQL, PostgreSQL, and Oracle. Wireless access is available throughout the CSE Building and all of campus, including student dorms, cafeterias, and other public areas.

The classrooms in the CSE building have all been provided with multimedia support and a computer housed in a locked kiosk. In addition, all classrooms have access to the university’s wireless network. That, combined with the college’s requirement that all students possess an adequately provisioned laptop computer, makes it easy to access resources in the classrooms.

The bulk of the CISE’s disk storage comes from an Oracle 7410 with 66TB of raw disk space. Other servers provide an additional 60TB. There are about 35 servers running a mix of Red Hat Enterprise Linux 6 and Solaris 10 providing such services as:

- Web hosting;
- Email;
- Database hosting—MySQL, PostgreSQL, Oracle;
- Kerberos / LDAP authentication;
- DNS;
- DHCP;
- Backups via Tivoli Storage Manager and disk based rsyncs;
- Samba;
- NFS;
- and security related services.

Our web servers run on a Sun T5220 server with Solaris 10, 32GB of memory, and 1.2 GHz UltraSPARC-T2 CPUs. They serve Department content, user content, and various web applications that support the Department. We have another Sun T5220, also running...
Solaris 10, which is available to all Department users to run jobs on. It has 64GB of memory and UltraSPARC-T2 CPUs running at 1.2 GHz. We also have two Linux-based CPU servers with 16GB of memory and quad-core processors running at 2 GHz. We have, in total, about 100 Linux PCs running Ubuntu Desktop 10.04 and 130 Windows 7 PCs. They serve as lab machines and workstations for students, Teaching Assistants, Research Assistants, and Faculty. Of these, 58 Windows PCs and 65 Linux PCs are in public labs that are intended for general student use as well as use in lab sections of graduate and undergraduate classes.

We provide a compute cluster consisting of a head node with dual Opterons, 16GB of memory and 3.5TB of storage with 20 worker nodes with dual Opterons and 32GB of memory running Linux (Ubuntu Server 10.04). We also provide a GPU compute cluster of five machines, each with up to three different high end GPUs for those that make use of the unique compute capabilities that GPUs provide. These machines have dual twelve core CPU’s, 64Gigabytes of memory and five TB of storage per node.

The networking in the Department consists mainly of 100 Mb and 1 Gb connections, except for the servers which utilize a minimum of 1 Gb connections, sometimes higher via EtherChannel. Cisco hardware is utilized as the backbone of our network and consists of one Catalyst 6513, one Catalyst 6509E, and three Catalyst 4506s—provides routing and switch capabilities to the more than 600 devices and 80 virtual networks in the Department. Our external connection is a 1 Gb fiber connection to the University of Florida’s core network.

A unique printing solution allows the department to offering free printing based upon a quota system to all students and Teaching and Research assistance.

Our five CISE computer support staff is well equipped to handle the diverse array of equipment and software required to support the Computer Engineering program. Each staff person has their various primary expertise: Unix / Linux, Windows, applications, web, and hardware maintenance.

**ECE Departmental Computing Resources**

The Computer Teaching Laboratory, located in NEB 288, contains 34 Redhat Linux and eight Windows workstations and is open to students at all times. Additionally, students can access a remote group of 21 Redhat Linux and 21 Windows servers to accomplish their computing needs. This laboratory supports all Departmental undergraduate teaching. All of the Windows and Linux workstations are connected to the UNIX/Samba server and each has access to networked printers. The main Windows software includes many software packages such as Microsoft Office, PSPICE, MathCAD, Matlab, Agilent's ADS and RFDS, Ansoft's Designer, Saber, Maxwell and HFSS, and Altera's Quartus Web Edition. The main UNIX programs available are from Cadence, MathSoft, Ansoft, Agilent as well as several local computational resources such as the FLOOPS/FLOODS (FLorida Object Oriented Device and Process Simulator) program from the UF SWAMP (Software and Analysis of Advanced Materials Processing Center) Center.
Many of these commercial and open source resources derive from the interdisciplinary research results of both ECE and CISE faculty. For example, Dr. Mark Law (ECE) is the author of the FLOOPS/FLOODS semiconductor device and process simulation package. MATLAB, FLOOPS/FLOODS, and the packages from Cadence and Ansoft all rely on the same suite of sparse matrix solvers created by the research results by Dr. Timothy Davis (CISE). Davis’ award-winning sparse solvers appear as $x=A\backslash b$ in MATLAB when $A$ is sparse, for example, and are used in countless other commercial and open source applications such as these.

C. Guidance

Both Departments, as well as UF Academic Technology, have extensive documentation on their respective websites, listing the available labs, and rules and regulations for appropriate and safe use of computing equipment. [CISE, ECE, AT]

The ECE Departmental laboratories employ full-time staff members who manage the laboratories. These individuals offer regular (formal) seminars of training and instruction on the proper usage of laboratory measurement equipment, tools, and software. They are also available on an individual basis to both students and faculty members for any special situations that may arise.

D. Maintenance and Upgrading of Facilities

Both Departments maintains the tools and equipment by establishing the useful life of each piece and the number of students that use it annually. With this information we determine the cost per student hour of depreciating the equipment over its useful life. Whenever necessary, the University establishes a laboratory fee that enables each Department to recover the costs of equipment depreciation and degradation. Each Department has a Facilities Committee that monitors the use of equipment, and plans for computing equipment becoming obsolete.

E. Library Services

The Libraries of the University of Florida form the largest information resource system in the state of Florida. Collections cover virtually all disciplines and include a wide array of formats – from books and journals to manuscripts, maps, and recorded music. Increasingly collections are digital and are accessible on the Internet via the library web page or the library catalog. The George A. Smathers Libraries provide primary support to all academic programs except those served by the Lawton Chiles Legal Information Center. The major library facilities on campus are:

- **Library West** holds collections in the humanities and social sciences, including resources supporting the College of Business, Africana Area Studies, East Asian Area Studies, and the Isser and Rae Price Library of Judaica.
- **Smathers Library** holds the Latin American Collection and the Special and Area Studies Collections that include the University Archives, the PK Yonge Library of
Florida History, Baldwin Library of Historical Children’s Literature, and the Rare Book Collection.

- **Marston Science Library** supports agriculture, engineering, mathematics, and the natural and physical sciences. The building also houses the Government Documents Department and the Map and Imagery Library.

- **Architecture and Fine Arts Library** (201 Fine Arts Building A) provides collections and services for architecture, fine arts, interior design, building construction, landscape architecture, and urban design.

- **Education Library** (1500 Norman Hall) holds education, child development, higher education, psychology, counseling, and children’s literature collections.

- **Allen H. Neuharth Journalism and Communications Library** (1060 Weimer Hall) holds a collection relating to journalism, advertising, public relations, and telecommunication.

- **Music Library** (231 Music Building) holds music collections including books, scores, and recorded music.

- **UF Digital Collections** comprise a constantly growing collection of digital resources from the University of Florida’s library collections as well as partner institutions.

- **Health Science Center Libraries** serve the academic, research and clinical information needs of the six UF health science colleges of Dentistry, Medicine, Nursing, Pharmacy, Public Health & Health Professions and Veterinary Medicine. The **Borland Library** (2nd floor, Learning Resource Center) is our Jacksonville branch, and the **Veterinary Medicine Reading Room** is located in room V1-110 in the CoVM Medicine Building.

- **Lawton Chiles Legal Information Center** at the Fredric G. Levin College of Law contains legal research materials supporting the study of state, federal, and international law. Notable collection areas include Florida, United States federal taxation, and British Commonwealth materials.

Specific to the program is the Marston Science Library, which supports agriculture, engineering, mathematics, and the natural and physical sciences. It is housed in the West Wing of the CSE Building, and has an extensive collections of Science Databases and Science journals, as well as science e-books. Faculty routinely place copies of course textbooks on reserve in the Marston Science Library. For the College of Engineering alone, the Marston Library has 6 subject specialists on staff, one of which is dedicated to the areas of Computer & Information Science and Engineering, and Electrical Engineering (Denise Bennett). The online system for searching for journal articles, books and e-books, dissertations [CSE, EE] is more than adequate for the needs of faculty and students in the Computer Engineering program.

UF Digital Collections comprise a constantly growing collection of digital resources from the University of Florida’s library collections as well as its partner institutions.
Research assistance and help in locating information are available on site and by phone in each of the libraries. The Ask a Librarian service links users to librarians via instant messaging or email.

Requests for obtaining books or subscriptions are handled through the Technology & Support Services Division of the George A. Smathers Libraries at the University of Florida consists of seven departments of technical operations: Acquisitions Department; Cataloging & Metadata Department; Preservation Department; Access Support; Digital Services; Facilities & Security; and Information Technology.

Technology & Support Services serves the University of Florida’s community by acquiring, organizing and preserving academic and scholarly information to support academic programs. The expertise in these departments encourages research endeavors and ensures access to print and digital resources through Web discovery systems, enhanced finding tools and Endeca, the Library catalog.

F. Overall Comments on Facilities

System staff in the CISE Department and the ECE Department are responsible for installation, maintenance, and upgrading of the equipment used in the program, including the safe operation and maintenance of the equipment facilities.

Safe practice and proper usage of ECE laboratory facilities are carefully ensured for each and every laboratory course. The first meeting of every lab course covers lab conduct and policy including the necessary safety procedures. This information is provided by the lab instructor, generally a graduate teaching assistant, and overseen by the full-time laboratory support specialists and the faculty member responsible for the lab.

It is ECE Department policy that safety can never be stressed enough and safety awareness and training is dealt with proactively at the departmental level and is accomplished through a program of lab safety training.

In general, the protocol for lab safety training consists of two steps: (1) general safety training by Environment, Health and Safety (EH&S) and (2) lab specific training. EH&S general training is provided at least once a semester. Lab specific training is arranged by the faculty member responsible for the lab. Either each faculty member trains his/her students in person or requests help from EH&S for training. The goals of the training are to:

- Educate the students about the importance of lab safety
- Prevent any safety accidents
- Minimize any damage if accidents occur.

Implementation and oversight of the safety-training program for the department is under the supervision of ECE faculty member Dr. Y.K. Yoon.
CRITERION 8. INSTITUTIONAL SUPPORT

A. Leadership

Both Departments are led by a Chair, and have at least one Associate Chair. In addition, both Departments have faculty designated as Undergraduate Coordinator and Graduate Coordinator. The Chairs of both Departments have recently ended long-term appointments, Dr. Sartaj Sahni for 10 years in CISE, and Dr. Mark Law in ECE for a similar number of years. These long-term appointments have given the Departments stability and growth. CISE Department currently has an interim Chair, Dr. Paul Gader in CISE. Dr. John Harris was interim chair for ECE in 2010 and is now the permanent chair of that department.

In both Departments, Chairs have overall responsibility for all academic programs administered by their Department, and are responsible for ensuring a collegial atmosphere that’s conducive to the quality of the Computer Engineering program. Chairs convene and preside over regular faculty meetings, which are the primary mechanism for decision making in the Department.

B. Program Budget and Financial Support

Department budgets are allocated by the Dean of the College of Engineering, in consultation with the respective Chair of each Department. Department Chairs report annually to the Dean their budget needs.

Teaching is supported by assigning Teaching Assistants to many courses, primarily on the basis of enrollments. In the CISE Department, a course with 20-40 students, for example, would typically be assigned one (25% FTE) TA (the ratio of TAs to students enrolled decreases significantly as class size increases). A similar arrangement operates in the ECE Department. Teaching Assistants are invariably graduate students in the discipline.

Both Departments actively engage their contacts in industry, to secure gifts of laboratory equipment and infrastructure. Harris Corporation has made several donations to the CISE Department in recent years, including a recent remodeling of the CISE Department’s central office area.

All of the resources mentioned above are adequate for the needs of the Computer Engineering students.
C. Staffing

Each Department has several full-time administrative positions (secretarial, financial) to help in daily operations.

CISE Staffing

The CISE Department has a total of 14.5 staff positions. These include 10 administrative staff positions: two undergraduate academic advisors, two graduate academic advisors, one grants specialist, three secretaries, a director of financial operations (shared with another department) and one office manager. CISE also has four system administration positions, who manage the hardware and software resources of the Department. Two additional staff lines are unfilled. The staff information appear son the CISE website [CISE-staff].

ECE Staffing

The ECE Department has a total of 35 staff positions: 10 administrative staff positions, and 25 technical staff positions. These include full-time positions to manage several computer and electrical equipment hardware laboratories. These positions appear on the ECE webpage [ECE-staff].

D. Faculty Hiring and Retention

Each Department actively engages in comprehensive recruiting efforts of new faculty each year. Faculty positions are advertised nationally, and a Department committee ranks the candidates and makes recommendations to the Chair for interview invitations. Faculty interviews typically last two days, during which prospective faculty meet with most of the faculty in the Department, as well as the Dean of the College. Faculty meet to discuss the candidates, and the Chair makes offers in consultation with the Dean of the College. These recruiting efforts have been very successful in recent years.

New faculty usually join a group of established colleagues, and a mentor is informally assigned to them, usually a senior faculty member who works in the same research area. These mentorships are meant to guide young faculty through their tenure-track years, helping them become better teachers and researchers.

E. Support of Faculty Professional Development

Support is very good for faculty professional development. Sabbaticals are awarded by the University, via recommendation by the College. Faculty are eligible for a sabbatical after six years of service. Traditionally, there have been two types of sabbaticals: one semester with full-pay, or two-semesters with half-pay. Recently, UF added a new type of sabbatical, with full pay for two semesters. UF has committed to awarding a minimum of 50 such sabbaticals per year. Sabbatical policies are listed on UF’s Academic Personnel website [UF-sabbaticals]. In addition, UF offers the Faculty Enhancement
Opportunity (FEO) program, with monetary awards for creative and flexible faculty development projects, including educational experiences/conferences, work with consultants/experts, and release time for intensive writing or creative work. The FEO program is administered by the Provost’s office [FEO].
PROGRAM CRITERIA

ABET program criteria for Electrical, Computer, and similar named Engineering programs are listed below in italics:

Curriculum:

- The structure of the curriculum must provide both breadth and depth across the range of engineering topics implied by the title of the program.

  As discussed in Criterion 5: Curriculum, our range of required and elective courses satisfies this requirement.

- The curriculum must include probability and statistics, including applications appropriate to the program name; mathematics through differential and integral calculus; sciences (defined as biological, chemical, or physical science); and engineering topics (including computing science) necessary to analyze and design complex electrical and electronic devices, software, and systems containing hardware and software components.

  As discussed in Criterion 5: Curriculum, all of the above coursework is required in the Computer Engineering program.

- The curriculum for programs containing the modifier “electrical” in the title must include advanced mathematics, such as differential equations, linear algebra, complex variables, and discrete mathematics.

  Although the modifier “electrical” is not included in the title of our program, the hardware track of the Computer Engineering has much in common with the discipline of Electrical Engineering. The program requires, as discussed in Criterion 5: Curriculum, advanced mathematics, specifically differential equations and discrete mathematics.

- The curriculum for programs containing the modifier “computer” in the title must include discrete mathematics.

  As discussed in Criterion 5: Curriculum, the required course COT-3100 Discrete Structures, covers this topic.
APPENDICES

Appendix A1 – Course Syllabi (CISE courses)

- CEN 3031 Introduction to Software Engineering
- CGS 3065 Legal and Social Issues in Computing
- COT 3100 Applications of Discrete Structures
- CDA 3101 Introduction to Computer Organization
- COP 3229 Computer Programming Using C++
- COP 3275 Computer Programming Using C
- COP 3502 Programming Fundamentals for CIS Majors 1
- COP 3503 Programming Fundamentals for CIS Majors 2
- COP 3504 Advanced Programming Fundamentals for CIS Majors (formerly CIS 3020)
- COP 3530 Data Structures and Algorithms
- CNT 4007C Computer Network Fundamentals (formerly CEN 4500C)
- COP 4020 Programming Language Concepts
- CAP 4053 Artificial Intelligence for Computer Games
- CEN 4072 Software Testing and Verification
- CDA 4102 Computer Architecture
- CIS 4301 Information and Database Systems 1
- COP 4331 Object-oriented Programming
- COP 4343 UNIX System Administration
- CAP 4403 Introduction to Aesthetic Computing
- CAP 4410 Digital Image Processing
- COT 4501 Numerical Analysis-A Computational Approach
- COP 4600 Operating Systems
- COP 4620 Translators and Translator Writing Systems
- CAP 4621 Artificial Intelligence and Heuristics
- CAP 4680 Knowledge-Based System: Theory and Practice
- CAP 4730 Computational Structures in Computer Graphics
- CAP 4800 Systems Simulation
- CIS 4905 Individual Study in CISE
- CIS 4912C Integrated Product and Process Design 1
- CIS 4913C Integrated Product and Process Design 2
- CIS 4914 Senior Project (also listed as CEN 4914)
- CIS 4930 Special Topics in CISE
- CIS 4940 Practical Work
- CIS 4949 Co-Op Work in CISE
- CIS 4956 Overseas Studies 1
Course Number & Name: CEN 3031 Introduction to Software Engineering

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Manuel Bermudez


a. Supplemental Material: None

Specific Course Information

a. Catalog Description: Topics include software planning, specifications, coding, testing and maintenance. Students gain experience in the team approach to large system development.

b. Prerequisites: COP 3530

c. Required, Elective, or Selected Elective: Required

Specific Goals for the Course

a. Specific Outcomes of Instruction: The primary goal of this course is to introduce students to the principles of software engineering. We will cover software planning, specifications, coding, testing, and maintenance. Group projects will enhance team skills and approaches to large system development.

b. Student outcomes addressed by the course: d, g

Brief List of Topics to Be Covered

- Communication
- Introduction to the complete software life cycle
- Requirements Engineering
- XP, Story Cards
- SDP Models
- Presentation skills
- Iteration I Presentation
- Design Models
- Software Development
- Testing
- Management
- Iteration II Presentation
Course Number & Name: CGS 3065 Legal and Social Issues in Computing

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Gerald Haskins

Textbook Title, Author, and Year: published articles in a course pack & on-line references

a. Supplemental Material: none

Specific Course Information

a. Catalog Description: This course explores the history, the myth, the ethics, the law and the risks of computer-based technology in modern society. Emphasis will be placed on critical analysis of hypotheticals and case studies. Published material will be supplemented with on-line Internet references.

b. Prerequisites: previous experience in Unix environment

c. Required, Elective, or Selected Elective: students must take either this course or EGN 4034 Professional Issues in Engineering (1 credit).

Specific Goals for the Course

a. Specific Outcomes of Instruction: Students will understand how legal and social issues impact the domain of computing.

b. Student outcomes addressed by the course: f, h, i, j

Brief List of Topics to Be Covered

- First Amendment Law
- Computer Misuse & Vulnerability
- Ethics in the Computer
- Computer Crime
- Computers and the Media
- The Oz Syndrome
- Information & Economics
- Risks of Overreliance
- Hacking and Viruses
- Blogging and Politics
- Censorship & Free Speech
- Online Indecency
- Electronic Privacy
- Homeland Insecurity
- Games with a Message
- Internet Politics
- Computer Contracts
- Intellectual Property
- Noxious Speech and Hate Groups
Course Number & Name: COT 3100 Applications of Discrete Structures

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Timothy A. Davis

  a. Supplemental Material: None

Specific Course Information
  a. Catalog Description: Covers the mathematics of discrete events; i.e., events that involve distinct elements, finite structures of distinct elements or finite sampled versions of continuous phenomena (such as movement).
  b. Prerequisites: MAC 2233, MAC 2311 or MAC 3472; Coreq: COP 3504 or COP 3503.
  c. Required, Elective, or Selected Elective: Required

Specific Goals for the Course
  a. Specific Outcomes of Instruction: This course teaches the fundamentals of logic, proof techniques, induction/recursion, counting, advanced, relations, and graph theory. These mathematical tools are essential to doing and understanding Computer Science / Computer Engineering. The primary emphasis of the course is mathematical reasoning and problem solving. Equipping the student with specific skills (such as tools for solving recurrence relations) is important, but only a secondary goal of this course.
  b. Student outcomes addressed by the course: a, k

Brief List of Topics to Be Covered
  • Logic and proofs (logic, predicates and quantifiers, rules of inference, proofs, proof methods and strategy)
  • Basic Structures: sets, functions, sequences, sums, cardinality and matrices.
  • Algorithms, asymptotic notation, complexity of algorithms
  • Number theory and cryptography: divisibility, modular arithmetic, primes, GCD
  • Induction and Recursion: induction, recursive definitions, recursive algorithms
  • Counting: basics, pigeonhole principle, permutations and combinations
  • Discrete Probability
  • Advanced Counting Techniques: applications of recurrence relations, divide-and-conquer
  • Relations: properties, representation, equivalence relations
  • Graphs: terminology, types of graphs, graph models, connectivity
  • Trees
Course Number & Name: CDA 3101 Introduction to Computer Organization

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Prabhat Mishra

   a. Supplemental Material: None

Specific Course Information
   b. Prerequisites: COP 3504 or COP 3503; MAC 2233, MAC 2311 or MAC 3472.
   c. Required, Elective, or Selected Elective: Required

Specific Goals for the Course
   a. Specific Outcomes of Instruction: This introductory course emphasizes essential concepts, the logical basis of computer structure, machine representation of instructions and data, flow of control, basic machine instructions, and assembly language programming. Students will be taught these basic concepts, and there will be exercises that help the student to learn the basics of assembly language programming. We also cover pipelined, parallel, and multicore architectures, to acquaint students with these architectures, the basics of how they function, and what practical problems are associated with these architectures. This is not an advanced course in computer architecture.
   b. Student outcomes addressed by the course: a, c, e

Brief List of Topics to Be Covered
   • Introduction and Course Overview
   • MIPS Assembly Language
   • Arithmetic for Computers
   • The Processor: Datapath, Control and Pipelining
   • Memory Hierarchy
   • Storage Systems
   • Multiprocessors and Multicore Architectures
   • Functional Validation of Computer Organization
Course Number & Name: COP 3229 Computer Programming Using C++

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Steve Thebaut


a. Supplemental Material: None

Specific Course Information

a. Catalog Description: In-depth treatment of the C++ programming language and an introduction to Windows programming using Visual C++.

b. Prereq: COP 3504 or COP 3503.

c. Required, Elective, or Selected Elective: Elective. At most 3 credits of any programming language course may count towards the Computer Engineering degree (COP 3229 and COP 3275).

Specific Goals for the Course

a. Specific Outcomes of Instruction: Through this course, students should be able to write, read, and understand computer programs using the C++ programming language. Students should also gain sufficient understanding of computer systems and programming concepts to be prepared to learn what is needed to tackle future programming challenges. Students are equipped with the basics of object-oriented programming.

b. Student outcomes addressed by the course: k.

Brief List of Topics to Be Covered

- Abstract classes as interfaces
- Class hierarchies for object-oriented programming
- Templates as the basis for type-safe generic software
- Exceptions for regular error handling
- Namespaces for modularity in large-scale software
- Run-time type identification for loosely coupled systems
- The C subset of C++ for C compatibility and system-level work
- Standard containers and algorithms
- Standard strings, I/O streams, and numerics
- C compatibility, internationalization, and exception safety
Course Number & Name: COP 3275 Computer Programming Using C

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Taylor Glenn


- **Supplemental Material:** None

Specific Course Information

- **Catalog Description:** Course solves problems related to a variety of disciplines and introduces students to the basic concepts of software and hardware.
- **Prerequisites:** MAC 1147 or equivalent.
- **Required, Elective, or Selected Elective:** Elective. At most 3 credits of any programming language course may count towards the Computer Engineering degree (COP 3229 and COP 3275).

Specific Goals for the Course

- **Specific Outcomes of Instruction:** Through this course, students should be able to write, read, and understand computer programs using the C programming language. Students should also gain sufficient understanding of computer systems and programming concepts to be prepared to learn what is needed to tackle future programming challenges.
- **Student outcomes addressed by the course:** k.

Brief List of Topics to Be Covered

- Compiling and Running Programs
- Variables, Data Types, and Arithmetic Expressions
- Looping
- If statements
- Arrays
- Functions
- Structures
- Strings
- Pointers
- Recursion and the Program Stack
- The Preprocessor
- Program Structure and Multiple C Files
- Bits, Bytes, and Numbering Systems
Course Number & Name: COP 3502 Programming Fundamentals for CIS Majors 1

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Rong Zhang

Textbook Title, Author, and Year: Introduction to Java Programming, Y. Daniel Liang, Prentice Hall; 8th edition, 2010
  a. Supplemental Material: None

Specific Course Information
  a. Catalog Description: First course in a 2-semester introductory sequence for students with no prior programming experience. Explores major concepts of Computer Science and the process of computer programming, including object-oriented programming, procedural and data abstraction and program modularity.
  b. Prerequisites: Corequisite: MAC 2311.
  c. Required, Elective, or Selected Elective: COP 3502 and 3503 are an optional two-semester pair of courses that together fulfill the requirement satisfied by the one-semester COP 3504.

Specific Goals for the Course
  a. Specific Outcomes of Instruction: The students are expected to develop an ability to identify, formulate, and solve the hardware and software Computer Engineering problems, accounting for the interaction between hardware and software.
  b. Student outcomes addressed by the course: c, e, k

Brief List of Topics to Be Covered
  • Understand what programming is and the unique feature of Java
  • Read: Understand programs written in JAVA language
  • Write: Design and implement programs using JAVA language
  • Compile: Use compiler to convert JAVA code into executable file
  • Execute: Run corresponding code to get results
  • Debug: Identify and fix syntax and semantic errors in JAVA code.
Course Number & Name: COP 3503 Programming Fundamentals for CIS Majors 2

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Rong Zhang


a. Supplemental Material: None

Specific Course Information

a. Catalog Description: The second course in the introductory sequence for students with no prior programming experience. Explores major concepts of Computer Science and computer programming processes, including object-oriented programming, procedural and data abstraction and program modularity.

b. Prerequisites: COP 3502 and MAC 2311.

c. Required, Elective, or Selected Elective: COP 3502 and 3503 are an optional two-semester pair of courses that together fulfil the requirement satisfied by the one-semester COP 3504.

Specific Goals for the Course

a. Specific Outcomes of Instruction: COP 3503 is the second course of a two-semester introductory sequence for students without prior programming experience. It builds up on COP 3502 (previously CIS 3022), and further explores Object-Oriented Programming using Java as the programming language. Concepts such as inheritance, abstract classes, interfaces, etc. will be introduced. Furthermore basic algorithms and data structures will be discussed, which are useful in solving problems. By the end of the course students will have a solid foundation for more advanced courses, and they will be able to start producing complex and useful programs.

b. Student outcomes addressed by the course: c, e, k

Brief List of Topics to Be Covered

- Object-Oriented Programming (Objects and Classes)
- Inheritance and Polymorphism
- Abstract Classes and Interfaces
- Exception Handling
- File I/O
- Recursion
- Algorithms: Searching and Sorting
- Data Structures: Lists, Stacks, Queues
- Graphical User Interfaces (GUIs)
Course Number & Name: COP 3504 Adv. Programming Fundamentals for CIS Majors  
(Formerly CIS 3020)

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each; 2 hours lab per week

Course Coordinator’s Name: Dr. Douglas D Dankel II


Specific Course Information
   a. Catalog Description: A fast-paced introduction to Computer Science for students with prior programming experience. Explores major concepts of Computer Science and the process of computer programming, including object-oriented programming, procedural and data abstraction and program modularity.
   b. Prerequisites: MAC 2311 or MAC 3472, programming experience
   c. Required, Elective, or Selected Elective: Required (or students may take the pair of courses COP 3502 and 3503)

Specific Goals for the Course
   a. Specific Outcomes of Instruction: Students obtain an understanding of (1) object-oriented programming, (2) procedural abstraction, (3) data abstraction, and (4) program modularity and state information. They learn proper techniques for developing, testing, and documenting code.
   b. Student outcomes addressed by the course: c, e, k

Brief List of Topics to Be Covered
   • Basic concepts of object-oriented programming (abstraction, polymorphism, inheritance, encapsulation)
   • Abstraction and specification (procedural, data, and control flow)
   • UML and use cases
   • Writing, understanding, and reasoning about code
   • Program design and documentation
   • The software life cycle (waterfall and agile), testing, debugging and defensive programming; recursion (linear, tree, tail and non-tail)
   • Introduction to big O notation
   • Basic search algorithms (linear and binary) and sorting methods (selection, bubble, quick, heap)
   • Importance of user interface design
Course Number & Name: COP 3530 Data Structures and Algorithms

Credits and Contact Hours: 4 crs; 4 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Manuel E. Bermudez

   a. Supplemental Material: None

Specific Course Information
   a. Catalog Description: Algorithm development using pseudo languages, basic program structures, program design techniques, storage and manipulation of basic data structures like arrays, stacks, queues, sorting and searching and string processing. Linked linear lists. Trees and multilinked structures.
   b. Prerequisites: COP 3504 or COP 3503, with C or better grade, COT 3100 and MAC 2234, MAC 2312, MAC 3473 or MAC 3512.
   c. Required, Elective, or Selected Elective: Required

Specific Goals for the Course
   a. Specific Outcomes of Instruction: To learn how the choice of data structures and algorithm design methods impacts the performance of programs. To learn object-oriented design principles. To study specific data structures such as linear lists, stacks, queues, hash tables, binary trees, heaps, tournament trees, binary search trees, and graphs. To study specific algorithm design methods such as the greedy method, divide and conquer, dynamic programming, backtracking, and branch and bound. To gain experience writing programs in C++.
   b. Student outcomes addressed by the course: a, e, k

Brief List of Topics to Be Covered
   - Insertion sort and practical complexities. Run-time measurement.
   - Linear lists. Array representation, array resizing. ArrayLinearList class.
   - Iterators. Linked representation of a linear list.
   - Chain class. Head nodes, circular lists, and doubly linked lists.
   - Simulated pointers and available-space lists.
   - Row-major, column-major indexing, special matrices. Sparse matrices.
   - Stacks--application to parentheses matching, towers-of-hanoi, railroad car rearrangement, and switchbox routing; array stacks.
   - Dictionaries, linear list representation. Hashing and hash table design.
   - LZW compression.
• Trees, binary trees, and properties. Binary tree representation and operations. Binary tree traversal methods—preorder, inorder, postorder, level order. Reconstruction from two orders.
• Online equivalence classes.
• Application of priority queues to heap sort and machine scheduling. Min and max heaps. Initialization of min and max heaps. Height- and weight-biased leftist trees.
• Winner and loser trees and application to k-way merging, run generation, and first-fit bin packing. Binary search trees and indexed binary search trees. AVL trees.
• Graph applications and properties. Graph operations and representation.
• Breadth-first and depth-first search. Application to path finding, connected components, and spanning trees.
• Greedy method. Application to bin packing/loading, knapsack problems.
• Single source all destinations shortest paths algorithm.
• Kruskal's and Prim's minimum-cost spanning tree algorithms.
• Divide and conquer, and application to defective chessboard and min-max problem. Iterative min-max implementation.
• Merge sort, natural merge sort, and quick sort.
• Selection and closest pair of points.
• Dynamic programming, 0/1 knapsack problem, recursive and iterative solutions.
• Matrix multiplication chains, dynamic programming recursive recurrence.
• Iterative solution to matrix multiplication chains.
• All pairs shortest paths. Single source shortest paths, with negative edge weights.
• Solution space trees and backtracking.
• Branch and bound.
Course Number & Name: CNT 4007C Computer Network Fundamentals  
(Formerly CEN 4500C)

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Jonathan Liu

   a. Supplemental Material: None

Specific Course Information
   a. Catalog Description: The fundamental concepts, principles and standards of computer networks. Topics will be introduced in bottom-up approach, starting from physical layer in OSI system architecture with a stronger focus on data link, mac, network and transport layers.
   b. Prerequisites: COP 3530, CDA 3101 and COP 4600
   c. Required, Elective, or Selected Elective: Required for CISE students (CEN), elective for ECE (CEE) students.

Specific Goals for the Course
   a. Specific Outcomes of Instruction: This course covers problems in design and analysis of computer networks. While some effort focuses on the low-level protocols, most of the course is devoted to higher level protocols at the medium access, network and transport layers.
   b. Student outcomes addressed by the course: b, k

Brief List of Topics to Be Covered
   • Introduction
   • The Application Layer
   • The Transport Layer
   • The Network Layer
   • The Data Link Layer
Course Number & Name: COP 4020 Programming Language Concepts

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Joseph N Wilson

   a. Supplemental Material: Programming for the Java™ Virtual Machine, Joshua Engelm Addison-Wesley Professional, 1999 (Recommended)

Specific Course Information
   a. Catalog Description: An introduction to programming language principles, including language constructs, design goals, run-time structures, implementation techniques and exposure to a wide variety of programming paradigms.
   b. Prerequisites: COP 3530
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: Provide students with an thorough understanding of the underlying concepts of programming languages (demonstrated by the ability to answer questions and work problems in class and answer examination questions) as well as a practical knowledge of implementation techniques (demonstrated by implementing a small Turing-complete programming language).
   b. Student outcomes addressed by the course: a, c, k

Brief List of Topics to Be Covered
   - Programming Language Syntax
   - Names, Scopes, Bindings
   - Semantic Analysis
   - Target Machine Architecture
   - Core Issues in Language Design
   - Control Flow
   - Data Types
   - Subroutines and Control Abstraction
   - Data Abstraction and Object Orientation
   - Alternative Programming Models
   - Functional Languages
   - Logic Languages
   - Concurrency
   - Scripting Languages
   - A Closer Look at Implementation
   - Building a Runnable Program
   - Run-time Program Management
   - Code Improvement
Course Number & Name: CAP 4053 Artificial Intelligence for Computer Games

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Douglas D Dankel II

Textbook Title, Author, and Year: Artificial Intelligence for Games, by Ian Millington, Morgan Kaufmann, 2006. AI for Game Developers, David Bourg and Glenn Seemann, O’Reilly, 2004.

a. Supplemental Material: None

Specific Course Information

a. Catalog Description: An examination of the use of A.I. in computer games. Topics include general A.I. knowledge, path finding, movement, tactics and planning, strategy, state machines, learning, dialogue, and emotions.

b. Prerequisites: COP 3530

c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course

a. Specific Outcomes of Instruction: This course presents an examination of the use of artificial intelligence in computer games. Topics to be covered include general AI knowledge, path finding, movement, tactics and planning, strategy, state machines, learning, dialogue, and emotions. The course will be taught as a seminar/project class. All students will make three presentations on the various course topics and everyone will be involved in the group development of a game. Each student will be involved in the group development of a game during the semester. The domain of the project will be discussed in a later handout. The completed project will be due on the last day of class. Periodic deadlines involving the creation of some portion of the project and written reports will be specified in this later handout.

b. Student outcomes addressed by the course: a, c, d, g, h, k

Brief List of Topics to Be Covered

- A*
- Command Hierarchy
- Dead Reckoning
- Emergent Behavior
- Flocking
- Formations
- Influence Mapping
- Level-of-Detail AI
- Manager Task Assignment
- Obstacle Avoidance
- Scripting
- State Machines
- Stack-based State Machines
- Subsumption Architecture
- Terrain Analysis
- Trigger System
- Bayesian Networks
- Blackboard Architecture
- Decision Tree Learning
- Filtered Randomness
- Fuzzy Logic
- Genetic Algorithms
- N-Gram Statistical Prediction
- Neural Nets & Perceptions
- Planning, Player Modeling
- Production Systems
- Reinforcement Learning
- Reputation System
- Smart Terrain
- Weakness Modification Learning
- Emotions
Course Number & Name: CEN 4072 Software Testing and Verification

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Steve Thebaut

Textbook Title, Author, and Year: Software Testing & Analysis, Pezze & Young, 2008.
   a. Supplemental Material: required readings in a course packet

Specific Course Information
   a. Catalog Description: Concepts, principles and techniques of software testing and verification. Strengths and limitations of black-box and white-box testing methods; techniques for proving the correctness of programs.
   b. Prerequisites: CEN 3031
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: Software Testing and Verification is a survey course on concepts, principles, and techniques related to software testing and formal program verification. Students will become acquainted with both the strengths and limitations of various functional and structural testing methods, as well as techniques for proving the functional correctness of sequential programs. Topics include: black-box and white-box test case design strategies, incremental integration testing techniques, inspections and reviews, axiomatic verification, predicate transforms, and function-theoretic verification.
   b. Student outcomes addressed by the course: a, b, c, e, k

Brief List of Topics to Be Covered
   • Intro to V&V Techniques and Principles
   • Requirements and Specifications
   • Black-Box Test Case Design Strategies: Partition Testing, Combinatorial Approaches
   • White-Box Test Case Design Strategies: Logic & Dataflow Coverage, Path Conditions and Symbolic Evaluation
   • Integration and Higher Level Testing
   • Testing Object-Oriented Software
   • Reviews and Inspections
   • Testing Tools
   • Formal Program Specification
   • Axiomatic Verification: Weak Correctness, Rules of Inference, Strong correctness
   • Predicate Transforms: Proving Strong Correctness, Computing Weakest Pre-conditions
   • Functional Verification: Complete and Sufficient Correctness, Axiom of Replacement, Correctness Conditions, Iteration Recursion Lemma, Revisiting Loop Invariants
   • Cleanroom Software Engineering
Course Number & Name: CDA 4102 Computer Architecture

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Jih-Kwon Peir


Specific Course Information

a. Catalog Description: Introduction to computer architecture and system organization including virtual memory, supports cache, pipeline, vector processing, multiprocessor and RISC architecture.

b. Prerequisites: CDA 3101 and COP 3530.

c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course

a. Specific Outcomes of Instruction: This course teaches students fundamental knowledge in Computer Architecture and Microarchitecture. The course covers the basic organizations of computer systems including instruction-set architecture, execution pipeline, memory hierarchy, and I/O subsystem. It also addresses advanced processor microarchitecture issues such as dynamic instruction scheduling, branch prediction, lock-up free caches, instruction-level parallelism, multiple instruction fetch/issuing, speculative execution, etc. to improve computer processor performance. Shared-memory multiprocessor systems with coherent caches to reduce memory access latency, the virtual machine concept and virtual machine monitor are also covered. Term projects will be assigned to study issues of high-performance processors.

b. Student outcomes addressed by the course: c, k

Brief List of Topics to Be Covered

- ISA Basics
- Pipeline basics
- ILP exploitation
- Limit ILP
- Cache basics
- Multiprocessor and TLP
- Memory
- Storage
Course Number & Name: CIS 4301 Information and Database Systems 1

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. ‘Daisy’ Zhe Wang

   a. Supplemental Material: Oracle 10g Programming: A Primer, by R. Sunderraman, 2007 (Recommended)

Specific Course Information
   a. Catalog Description: The first part of a two-course sequence that studies the essential concepts, principles & techniques of modern database systems. Topics include modeling and querying of data using conceptual data models as well as the development of a database application.
   b. Prerequisites: COP 3504 or COP 3503 and COT 3100
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: This course is the first in a two-course sequence that serves as an introduction to modern database systems. This first course provides an in-depth view of databases from a sophisticated user’s perspective. Essentially, when you are done with this class you will know the right way to design and implement software that uses a database as a back end to store and manage data. Even if you think that you know the right way to do this because you have played with MySQL previously, the simple fact is that you probably do not know the right way. We will not cover database internals or how to tweak them in order to get them to run fast; that is the topic of the next course in the sequence.

   From a theoretical point of view, this course covers the essential principles for the design, analysis, and use of computerized database systems. The design and techniques of conceptual modeling, database modeling, database system architecture, and user/program interfaces are presented in a unified way.

   From a practical point of view, students learn to deal with a commercial database system (Oracle). They learn how to apply theoretically understood concepts in a professional database environment. For example, they practice how to formulate ad hoc queries or how to write application programs in a database context. They also learn the complete process from devising a conceptual database design up to its transformation into the database schema of a concrete database management system.

   By providing a balanced view of “theory” and “practice”, the material covered should allow the student to understand and use practical database systems. The student is provided with a basic understanding of the issues and problems involved in database system development, a knowledge of currently practical techniques for satisfying the
needs of a database system, and an indication of the current research approaches that are likely to provide a basis for tomorrow's systems.

b. **Student outcomes addressed by the course:** a, b, c, e, k

**Brief List of Topics to Be Covered**
- The ER model
- Conceptual database design and modeling
- The relational model
- Relational query languages (relational algebra, relational calculus, and SQL)
- Logical database design (functional dependencies and normal forms)
- Object-relational databases and SQL-3
Course Number & Name: COP 4331 Object-oriented Programming

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Joseph N Wilson


a. Supplemental Material: Design Patterns: Elements of Reusable Object-Oriented Software, Erich Gamma, Richard Helm, Ralph Johnson, & John Vlissides (Addison-Wesley, 1994) (optional)

Specific Course Information

a. Catalog Description: Fundamental conceptual models for programming languages illustrated with specific programming languages and application problems. Specific topics include class and object models, inheritance among classes, objects and static and dynamic systems and implementations.

b. Prerequisites: COP 3530.

c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course

a. Specific Outcomes of Instruction: This course discusses fundamental conceptual models for object-oriented programming and illustrates these with Java language and application problems. We will be covering object-oriented terminology extensively while emphasising the practical importance of the concepts. Students will be responsible for reading the required textbook, creating presentations for the challenges, and working on a significant and non-trivial team-programming project to gain familiarity with object-oriented analysis, design, and implementation.

b. Student outcomes addressed by the course: c, e, k

Brief List of Topics to Be Covered

- Learning fundamentals of object-oriented design
- Building a working knowledge of object-oriented programming
- Getting familiar with UML terminology: learn how to incorporate UML in design
- Understanding how to design and implement well built class hierarchies
- Being able to evaluate classes and hierarchies
- Differentiating between object oriented programming and other paradigms
Course Number & Name: COP 4343 UNIX System Administration

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Joseph N Wilson

   a. Supplemental Material: None

Specific Course Information
   a. Catalog Description: A study of the underlying concepts and techniques employed in the installation, administration and tuning of UNIX operating systems. Topics covered include operating system installation, simple network configuration, file backup and restore, account administration, device management, scheduling, file systems, network management, and basic system and network security.
   b. Prerequisites: COP 4600
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: The Unix® System Administration course addresses the goal of teaching engineering science and design. The student is required to employ basic knowledge gained in other parts of the program to the task of effectively administering an operating system. Knowledge of specific data structures and programming techniques employed in the implementation of operating systems and utilities is exploited in solving problems assigned during the term.
   The Unix® System Administration course addresses several of the CISE Department's program objectives. The course provides a variety of information demonstrating interactions between hardware and software. It provides ample opportunities to learn to analyze, design, and implement solutions to Computer Engineering problems. It also addresses a number of the ethical, legal, and social issues associated with the administration of computer systems and networks.
   b. Student outcomes addressed by the course: e, k

Brief List of Topics to Be Covered
   • Operating system installation
   • Simple network configuration
   • File backup and restore
   • Account administration
   • Device management, scheduling
   • File systems
   • Network management
   • Basic system and network security
Course Number & Name: CAP 4403 Introduction to Aesthetic Computing

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Paul Fishwick

Textbook Title, Author, and Year: extensive class notes provided online
   a. Supplemental Material: None

Specific Course Information
   a. Catalog Description: Basic concepts of applying aesthetics to the representation of formal discrete structures found in computing, as well as to their operational behaviors.
   b. Prerequisites: COP 3530.
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: This is a seminar class where we will investigate new ways of representing three types of formal language for a variety of end goals and users. We will also have invited lectures and media presentations related to the class topic.
   b. Student outcomes addressed by the course: a, c, e

Brief List of Topics to Be Covered
   ● Introduction to class: goals and objectives
   ● History of the class, past project examples
   ● Encyclopedia Chapter
   ● Team Talk
   ● Video
   ● Basic Method, Guide and Tutorial
   ● Technologies
   ● Futuristic interfaces: World Builder Microsoft Corning Heavy Rain
   ● Game Concepts
   ● Ethics and legal issues Terms of Service
Course Number & Name: CAP 4410 Digital Image Processing

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Baba Vemuri


Specific Course Information
  a. Catalog Description: Survey of techniques used to replicate the human vision process in computer systems. Topics include image formation, image algebra, filtering, range extraction, edge and boundary detection, region growing, and model based vision.
  b. Prerequisites: COP 3530 and MAC 2312, MAC 3473, or MAC 3512.
  c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
  a. Specific Outcomes of Instruction: Students learn the mathematics behind computer image processing and image recognition, and become familiar with a wide range of techniques in this domain.
  b. Student outcomes addressed by the course: a, c, e, k

Brief List of Topics to Be Covered
  • Image Formation: Monocular imaging system; Orthographic & Perspective Projections; Camera model and Camera calibration; Binocular imaging systems; 3D image sensing (range sensing with laser ranging, grid coding etc.)
  • Low-level Vision: Basic image processing (continuous and discrete images), Edges and edge finding, stereo vision, Regularization, Shape from X, Optic flow and its computation, Motion analysis (computation of motion parameters and structure).
  • Shape Segmentation and Representation: Simple segmentation techniques in 2D and 3D: De-formable curves and surfaces a.k.a. “snakes” and associated numerical methods. Snakes for tracking and Kalman snakes. 2D (implicit and explicit functions, boundaries: Fourier/Wavelet descriptors; regions: Texture description using co-occurrence matrices, Medial axis, quadtrees etc.) and 3D shape representation (surface based: implicit and explicit functions, Gauss map and its differential; and volume based: Octrees, deformable solids etc.) techniques, Multi-resolution representations (Laplacian pyramid and wavelet basis).
  • High-level Vision: Simple object recognition methods in 2D and 3D: Various criteria for image- image or shape-shape matching. Principal Component Analysis (PCA) and building priors for recognition.
Course Number & Name: COT 4501 Numerical Analysis-A Computational Approach

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Timothy A. Davis


Specific Course Information
   b. Prerequisites: COP 3504 or COP 3503 and MAS 3114.
   c. Required, Elective, or Selected Elective: Required for CISE (CEN) students; elective for ECE (CEE) students.

Specific Goals for the Course
   a. Specific Outcomes of Instruction: The objectives of this course is to enable students to master the theory and practice of numerical integration, nonlinear equations, linear and nonlinear systems of equations, differential equations, and interpolation, so that they can be used to solve real-world problems. Computational Science has become the third branch of science, along with theory and experimentation. One can't do an experiment on, say, the sun, and building a nuclear fusion experiment is rather costly and hard to do. Theory can only take you so far. Computational Science bridges the gap: it can simulate complex scenarios that rely on theory and can be validated by experiments, but are beyond the reach of both.
   b. Student outcomes addressed by the course: a, b, e

Brief List of Topics to Be Covered
   • Scientific Computing: approximations, computer arithmetic, mathematical software
   • Systems of Linear Equations: existence, uniqueness, sensitivity, direct & iterative solvers
   • Linear Least Squares: existence, uniqueness, sensitivity, orthogonalization, SVD
   • Eigenvalue Problems
   • Nonlinear Equations: existence, uniqueness, sensitivity, convergence rates, nonlinear systems
   • Optimization: existence, uniqueness, sensitivity, optimization in one direction
   • Interpolation: existence, uniqueness, sensitivity, polynomial interpolation, splines
   • Numerical Integration and Differentiation
   • Initial Value Problems for Ordinary Differential Equations
Course Number & Name: COP 4600 Operating Systems

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Richard Newman


Specific Course Information
  a. Catalog Description: The design and implementation of various components of a modern operating system, including I/O programming, interrupt handling, process and resource management, computer networks and distributed systems.
  b. Prerequisites: CDA 3101, COP 3530; knowledge of C or C++ recommended.
  c. Required, Elective, or Selected Elective: Required

Specific Goals for the Course
  a. Specific Outcomes of Instruction: Students will study the design & implementation of various components of a modern operating system. Topics include processor multiplexing, process and resource management, network and distributed operating system concepts. This is a course on the theory, design, and implementation of operating systems, not a course on how to merely use an operating system. Successful students will be able to discuss the four main components of operating system, design each of these, evaluate a given design vis-à-vis a particular purpose, and apply their knowledge of data structures, algorithms, performance analysis, and protocols to real-life problems in multi-threaded systems.
  b. Student outcomes addressed by the course: c, k

Brief List of Topics to Be Covered
- Processor multiplexing
- Process and resource management
- Network and distributed operating system concepts
Course Number & Name: COP 4620 Translators and Translator Writing Systems

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Manuel Bermudez


Specific Course Information
   a. Catalog Description: Translation of languages, scanning and parsing techniques. Translator writing systems. The implementation of a compiler.
   b. Prerequisites: COP 3530
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: This course covers the theory and practice of compiler-writing. The class discussions will parallel the project, in which students will extend (significantly) the implementation of a compiler for a subset of C.
   b. Student outcomes addressed by the course: c, e, k

Brief List of Topics to Be Covered
   • Language theory
   • Finite-state machines
   • Lexical analysis
   • Parsing
   • Syntax-directed translation
   • Contextual constraint management
   • Code generation.
Course Number & Name: CAP 4621 Artificial Intelligence and Heuristics

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Douglas D. Dankel II

Textbook Title, Author, and Year: Artificial Intelligence Illuminated, Ben Coppin, Jones and Bartlett, 2004

a. Supplemental Material: none

Specific Course Information

a. Catalog Description: Introduction to artificial intelligence concepts. Heuristic search, clause form logic, knowledge representation, reasoning and inference, overview of computer vision, planning, natural language, Lisp and Prolog.

b. Prerequisites: COP 3530.

c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course

a. Specific Outcomes of Instruction: This course presents an overview of Artificial Intelligence concentrating on the core concepts of problem solving, heuristic search, and knowledge representation. It is intended to give students an appreciation of how difficult it is to develop “intelligent” systems, what makes a system intelligent, and new problem solving techniques and methods.

b. Student outcomes addressed by the course: a, c, g, j, k

Brief List of Topics to Be Covered

- What is intelligence, what is artificial intelligence?
- The Turing Test, strong vs. weak AI
- Searle’s Chinese Room
- Problem solving and the classes of problems
- Knowledge representation (types of knowledge, using knowledge, semantic networks, frames, scripts, rules, state-space vs. problem reduction representations, OR and AND/OR trees)
- Forward vs. backward reasoning
- Blind search (British Museum Algorithm, generate and test, depth, breadth, depth-first iterative deeping)
- Heuristic search (ordered/best-first, admissibility, monotonicity, hill climbing, best-first, beam, uniform-cost, A*, greedy, constraint satisfaction, bi-directional, island), game trees and search (minimax, alpha-beta)
- Various games (checkers, chess, Go, Gomaku, Othello, Backgammon)
- Logic (propositional, predicate, modal/non-monotonic, unification, deduction, abduction)
- Expert systems, learning (inductive, version spaces, inductive bias, Occam’s Razor, overfitting, classical learning by Winston and Samuel)
- Computer vision (noise removal, enhancing, Guzman, Huffman-Clowes, Waltz)
- Natural language (production, understanding, grammars, SHRDLU, planner language)
- Inexact reasoning (Bayes, certainty factors, Denster-Shaffer)
Course Number & Name: CAP 4680 Knowledge-Based System: Theory and Practice

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Douglas D. Dankel II

  a. Supplemental Material: None

Specific Course Information
  a. Catalog Description: Concepts, theory and various applications for knowledge-based (expert) systems, reasoning schemes, knowledge representation, knowledge-based system tools, building knowledge bases, knowledge acquisition, reasoning under certainty and inexact reasoning.
  b. Prerequisites: CAP 4621
  c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
  a. Specific Outcomes of Instruction: This course provides students with an in-depth examination of expert or knowledge-based systems. The intent is for students to learn a unique and different problem solving approach – solving problems from a rule/event-driven perspective, to see how this technique can be applied to various problems, and to gain an understanding of problem solving with imprecise and inexact data.
  b. Student outcomes addressed by the course: a, b, c, h, k

Brief List of Topics to Be Covered
  • Structure of knowledge-based systems
  • Different user perspectives (end-user, developer, tool builder)
  • CLIPS
  • Prolog
  • Logic (propositional, predicate)
  • Rules representations and reasoning (forward, backward, bi-directional)
  • Associate networks
  • Frames
  • Objects
  • Blackboard architectures
  • Inexact reasoning (Bayes, certainty factors, Denster-Shaffer)
  • Knowledge-based systems lifecycle and how it differs from traditional software development
Course Number & Name: CAP 4730 Computational Structures in Computer Graphics

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Alireza Entezari


Specific Course Information

a. Catalog Description: A study of the major topics in computer graphics: display and output technology, two and three-dimensional manipulations; space curves and surfaces, hidden surface removal and shading models.

b. Prerequisites: COP 3530.

c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course

a. Specific Outcomes of Instruction: An advanced course on the concepts and principles underlying interactive gaming and graphics environments. The goal is to build, not just use such environments, including the lighting and modeling of geometry. In particular, students are expected to work with, understand and modify OpenGL example programs. Students will also benefit from refreshing their knowledge of linear algebra and calculus to understand structures rather than just examples. Through several hands-on exercises students develop a thorough understanding of the graphics pipeline and more elaborate techniques in computer graphics. The emphasis is on basic concepts, mathematical principles, algorithms and data structures used in computer graphics.

b. Student outcomes addressed by the course: a, c, e, k

Brief List of Topics to Be Covered

- Graphics Hardware
- Basic Linear Algebra
- Geometric Transformations
- Scene composition
- View Transformations
- Shading and Illumination
- Rendering
- Sampling and Reconstruction
- Modeling Curves and Surfaces
- Ray tracing
- Anti-aliasing and composition
- Color spaces
- Image Based Methods
Course Number & Name: CAP 4800 Systems Simulation

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Paul Fishwick

Textbook Title, Author, and Year: online materials
  a. Supplemental Material: none

Specific Course Information
  a. Catalog Description: Simulation methodology and practice. Covers basic concepts in modeling and analysis for both continuous and discrete systems. Combined simulation methods including integrated qualitative/quantitative system modeling. Will use in-house simulation software.
  b. Prerequisites: COP 3530
  c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
  a. Specific Outcomes of Instruction: The student will be able to explain how complex systems function through reference to dynamic systems models and simulation. The student will learn a modern language related to simulation. The current language is Ptolemy II, which is an open source agent-based package hosted at UC Berkeley.
  b. Student outcomes addressed by the course: a, b, c, e, k

Brief List of Topics to Be Covered
  • Management of data and time
  • System definition
  • Finite state machine
  • Markov models, Petri nets, Turing machines
  • Rule-Based models
  • Event-Based models
  • Cellular automata
  • Compartmental modeling, Functional block modeling
  • System Dynamics method
  • Digital logic circuits
  • Queuing models
  • Lindenmeyer systems
  • Kinetic graphs
  • Difference equations
  • Ordinary and partial differential equations
  • Delay-differential equations
Course Number & Name: CIS 4905 Individual Study in CISE

Credits and Contact Hours: 1 to 4 crs; can be repeated with change in content up to 8 credits.

Course Coordinator’s Name: Dr. Steve Thebaut

Textbook Title, Author, and Year: varies according to the topic
   a. Supplemental Material: varies

Specific Course Information
   a. Catalog Description: Problems in different areas of Computer Science.
   b. Prerequisites: Instructor permission
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: One-on-one interaction between a faculty member and a student. Topics vary.
   b. Student outcomes addressed by the course: varies

Brief List of Topics to Be Covered
   • Varies
Course Number & Name: CIS 4912C Integrated Product and Process Design 1

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Keith Stanfill (ISE Department)

Textbook Title, Author, and Year: None
   a. Supplemental Material: None

Specific Course Information
   a. Catalog Description: The first part of a two-course sequence where teams of engineering and business students partner with industry sponsors to design and build authentic products and processes. Working closely with an industry liaison engineer and a faculty coach, students gain practical experience in teamwork and communication, problem solving and engineering design, and develop leadership, management and people skills. Weekly workshop activities adapt lecture topics to individual projects. Students learn firsthand how to develop products and processes that meet customer requirements on time and within budget.
   b. Prerequisites: CDA 3101, COP 3530, COT 3100 and instructor permission.
   c. Required, Elective, or Selected Elective: CISE students (CEN) must take either CIS 4914 Senior Design, or the pair of IPPD courses (CIS 4912C / 4913C). ECE students in the Computer Engineering program (CEE) take their departmental senior design / IPPD courses.

Specific Goals for the Course
   a. Specific Outcomes of Instruction: The IPPD program provides both classroom and laboratory experience. Working in small multidisciplinary project teams, students get important practical experience in teamwork and communication and in developing their leadership, management and people skills. Advantages of integrating product and process design are well recognized by industry. Concurrent design of products and processes improves product costs and quality and reduces time-to-market. Students who have worked on real-life projects and know how to work in teams are more valuable as employees. They also recognize the importance of communication among different engineering and business disciplines.
   b. Student outcomes addressed by the course: all outcomes a through k

Brief List of Topics to Be Covered
   • How fundamental engineering science is relevant to effective product and process design
   • That design involves not just product function but also producibility, cost, schedule, reliability, quality, customer preferences and life cycle issues
   • How to complete projects on time and within a budget
   • That engineering is a multidisciplinary effort
Course Number & Name: CIS 4913C Integrated Product and Process Design 2

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Dr. Keith Stanfill (ISE Department)

Textbook Title, Author, and Year: None
   a. Supplemental Material: None

Specific Course Information
   a. Catalog Description: The second part of the CIS 4912-4913 sequence.
   b. Prerequisites: CIS 4912C
   c. Required, Elective, or Selected Elective: CISE students (CEN) must take either CIS 4914 Senior Design, or the pair of IPPD courses (CIS 4912C / 4913C). ECE students in the Computer Engineering program (CEE) take their departmental senior design / IPPD courses.

Specific Goals for the Course
   a. Specific Outcomes of Instruction: See CIS 4912C on previous page.
   b. Student outcomes addressed by the course: all outcomes a through k

Brief List of Topics to Be Covered
   ● (See CIS 4912C on previous page for details)
Course Number & Name: CIS 4914 Senior Project (also listed as CEN 4914)

Credits and Contact Hours: 3 crs; 3 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Tuba Yavuz-Kahveci

Textbook Title, Author, and Year: none
  a. Supplemental Material: none

Specific Course Information
  a. Catalog Description: Involves completing a significant CISE-related project. Student must coordinate with the instructor and a project adviser, prepare a detailed technical report and deliver an oral presentation.
  b. Prerequisites: senior CISE standing and approved project proposal
  c. Required, Elective, or Selected Elective: CISE students must take either CIS 4914 Senior Design, or the pair of IPPD courses (CIS 4912C / 4913C). ECE students in the Computer Engineering program take their departmental senior design / IPPD courses.

Specific Goals for the Course
  a. Specific Outcomes of Instruction: Students will apply their knowledge of Computer Science and/or Engineering to a semester long project and provide both written and oral forms of presentation of their project. Students will improve their presentation and communication skills to be effective and competent in a professional setting. Students will be prepared to take roles in engineering projects as successful engineers by applying the engineering techniques and methods that they have learned.
  b. Student outcomes addressed by the course: all outcomes a through k

Brief List of Topics to Be Covered
  ● Time management
  ● Project management
  ● Presentation skills
Course Number & Name: CIS 4930 Special Topics in CISE

Credits and Contact Hours: 1 to 4 credits; 1 to 4 classes per week of 50 minutes each

Course Coordinator’s Name: Dr. Steve Theabaut

Textbook Title, Author, and Year: varies
   a. Supplemental Material: varies

Specific Course Information
   a. Catalog Description: Topics vary. Current topics of interest in area of Computer and Information Sciences.
   b. Prerequisites:
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: varies
   b. Student outcomes addressed by the course: varies

Brief List of Topics to Be Covered
   • Varies
Course Number & Name: CIS 4940 Practical Work

Credits and Contact Hours: 1 credit; can be repeated for credit.

Course Coordinator’s Name: Dr. Steve Thebaut

Textbook Title, Author, and Year: none
   a. Supplemental Material: none

Specific Course Information
   a. Catalog Description: One term practical software engineering work under industrial supervision as set forth in the College of Engineering regulations.
   b. Prerequisites: none
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: Students gain practical experience in putting their knowledge to work in an industrial environment.
   b. Student outcomes addressed by the course: h, i, j

Brief List of Topics to Be Covered
   • Varies according to the industrial supervisor
Course Number & Name: CIS 4949 Co-Op Work in CISE

Credits and Contact Hours: 1 credit.

Course Coordinator’s Name: Dr. Steve Thebaut

Textbook Title, Author, and Year: none
   a. Supplemental Material: none

Specific Course Information
   a. Catalog Description: Practical engineering work under industrial supervision, as set forth in the College of Engineering regulations.
   b. Prerequisites: none
   c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
   a. Specific Outcomes of Instruction: Students gain practical experience in putting their knowledge to work in an industrial environment.
   b. Student outcomes addressed by the course: h, i, j

Brief List of Topics to Be Covered
   • Varies according to the industrial supervisor
Course Number & Name: CIS 4956 Overseas Studies 1

Credits and Contact Hours: 1 to 15; can be repeated with change in topic up to 15 credits.

Course Coordinator’s Name: Dr. Steve Thebaut

Textbook Title, Author, and Year: none
  a. Supplemental Material: none

Specific Course Information
  a. Catalog Description: A mechanism for course work taken at a foreign university as part of an approved study abroad program. Credits taken under this will be transferred to UF and count toward graduation.
  b. Prerequisites: Permission of undergraduate adviser.
  c. Required, Elective, or Selected Elective: Elective

Specific Goals for the Course
  a. Specific Outcomes of Instruction: varies
  b. Student outcomes addressed by the course: h, i, j

Brief List of Topics to Be Covered
  • Varies
Appendix A2 – Course Syllabi (ECE courses)
1. **Course number and name:** EEE 3308C - Electronic Circuits I  
   (Formerly EEL 3304C)

2. **Credits and contact hours:** 3 crs; 3 classes of 50 minutes each and 1 laboratory section of 3 hours per week

3. **Instructor’s or course coordinator’s name:** Dr. Robert Fox

   a. other supplemental materials: None

5. **Specific course information**  
   a. catalog description: Fundamentals of electronic circuits and systems. Laboratory  
   b. prerequisites: EEL 3111 - Circuits I  
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required for CEE students. CEN students may take EEE 3308C or EEL 3135.

6. **Specific goals for the course**  
   a. specific outcomes of instruction: The student will be able to explain the basics of electronic components, circuits, and systems and will be able to design electronic circuits and systems to meet desired needs and specifications  
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, e, g, i, k

7. **Brief list of topics to be covered**  
   a. Operational Amplifiers – Analysis, Circuits, Nonidealities  
   b. Diode – PN junction operation, Model, Circuits  
   c. FET – Operation, DC models, Biasing, AC model, Amplifiers, SPICE  
   d. BJT – Operation, models, Biasing, AC model, Amplifiers, SPICE  
   e. IC Biasing - Current sources, mirrors, steering, Single-Stage Amplifiers, Active loads, Cascade Amplifiers, SPICE  
   f. Differential Amplifiers, Active Load, Frequency Response, Multistage Amplifiers, SPICE
1. **Course number and name:** EEE 3396 - Solid-State Electronic Device

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Jing Guo

   a. other supplemental materials:

5. **Specific course information**
   a. catalog description: Introduction to the principles of semiconductor electron device operation.
   b. prerequisites: EEL 3111 – Circuits I
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required for CEE students, elective for CEN.

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will understand the background on material physics of semiconductors and will be able to develop the fundamental semiconductor equations at equilibrium and non-equilibrium, and to apply these fundamental concepts to basic semiconductor devices in order to explain the device operation, electrical characteristics, and design semiconductor devices with specified operating parameters
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, c, e, k

7. **Brief list of topics to be covered:**
   a. Semiconductor Materials; Crystal lattices and periodic structures, Wave motion of electrons in materials, Fermi energy level
   b. Carrier Transport; Drift and electron and hole mobilities, Diffusion in a concentration gradient, Generation, recombination, trapping, and tunneling
   c. MOS Capacitor; Characteristics of metal-oxide-semiconductor capacitor, Charge control model
   d. PN Junctions; Energy band diagram, Physics of the Shockley diode equation, Space-charge-layer current, Small-signal characteristics
   e. Characteristics MOS field-effect transistor, Conductivity modulation model, Small-signal equivalent circuit model, Switching memory devices
   f. Characteristics of bipolar junction transistor, Derivation of the Shockley equations, Small-signal characteristics, Switching properties
   g. Characteristics of photonic devices
1. **Course number and name**: EEE 4306C - Electronic Circuits II

2. **Credits and contact hours**: 3 crs; 2 classes of 50 minutes each and 1 laboratory section of 3 hours per week

3. **Instructor’s or course coordinator’s name**: Dr. William Eisenstadt

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Design-oriented continuation of EEE 3308; feedback, op amp circuits and applications, digital electronics. Laboratory
   b. prerequisites: EEL 3111 - Circuits I, EEE 3308 - Electronic Circuits I
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will know the analysis, design, and testing of analog electronic circuits, their applications of these circuits, and some advanced topics in RF and Communications
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, i, k

7. **Brief list of topics to be covered:**
   a. Review of transistors and their DC and small signal models - BJT and MOSFET
   c. Review of differential amplifier
   d. Review of current mirror
   e. Feedback theory
   f. Feedback circuit analysis, Shunt, Series, Open loop gain, Closed loop gain, Node resistances
   g. Feedback circuit Frequency Compensation, and Stability
   h. Operational Amplifier Design CMOS and BJT
   i. Power Amplifier Design
   j. Op-amp application circuits.
   k. Filter Design and Bode Plots
   l. Passive Element Filters
   m. Active Element Filters with Op-amps
   n. RF components and parasitics, inductors, transformers
   o. Tuned RF circuits and RF Amplifiers
   p. RF Oscillators and Mixers
1. Course number and name: EEE 4310C - Digital Integrated Circuits

2. Credits and contact hours: 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: Dr. Toshi Nishida

   a. other supplemental materials: None

5. Specific course information
   a. catalog description: Analysis and design of digital circuits using MOS and bipolar devices
   b. prerequisites: EEE 3308 - Electronic Circuits I, EEL 3396 - Solid State Devices, EEL 3701 - Digital Logic and Computer Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: This course focuses on analysis and design of modern digital circuits. Transistors are introduced and described from a digital point of view, and the performance of various circuits is derived and estimated. CMOS digital circuits will be introduced and analyzed. Students will investigate and design digital circuits using L_EDIT and SPICE.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, c, e, i, k

7. Brief list of topics to be covered:
   a. MOS device physics, substrate bias effects, scaling, and SPICE
   b. MOS capacitances
   c. CMOS fabrication, process flow, and design rules
   d. Static CMOS inverter
   e. CMOS inverter dynamic response
   f. Load capacitance estimation
   g. Short channel effects on CMOS inverter
   h. Static CMOS combinational logic
   i. Ratioed logic and pass transistor logic
   j. Dynamic logic, sequential logic, and memory circuits
1. Course number and name: EEE 4329 - Future of Microelectronics Technology

2. Credits and contact hours: 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: Dr. Scott Thompson

   a. other supplemental materials: approximately 15 journal papers and handouts

5. Specific course information
   a. catalog description: Survey of state-of-the-art microelectronics technology and prospects for future technologies. Nanoscale MOSFETs, strained Si, high-k gate dielectrics, carbon nanotubes, molecular electronics, single-electron devices
   b. prerequisites or co-requisites: EEL 3396 - Solid State Devices or consent of instructor
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will be able to select the most appropriate architecture for a given design and explain if and how improved semiconductor technologies would improve overall performance in a way that makes a redesign warranted.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, c, e, i, k

7. Brief list of topics to be covered:
   a. Moore’s Law and microelectronic industry trends
   b. Logic device: State of the Art for a Si MOSFET
   c. Requirements for a logic device replacement
   d. CMOS devices limits: quantum-statistical
   e. Post CMOS logic device
   f. Multi-Gate CMOS
   g. Carbon nanotubes
   h. High level overview of Quantum Transport Devices
   i. Single electron devices for Logic applications
   j. Spintronics
   k. Memory devices
   l. DRAM, ferroelectric, magneto resistive, and phase change RAM
1. Course number and name: EEE 4331 - Microelectronic Fabrication Technologies

2. Credits and contact hours: 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: Dr. David Arnold

   a. other supplemental materials: None

5. Specific course information
   a. brief description of the content of the course (catalog description):
   b. prerequisites: EEL 3396 - Solid State Electronic Devices
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course
      in the program: Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will have an understanding of
      microelectronics fabrication processing technologies which will prepare the
      student for careers in the semiconductor industry, hands-on research relating to
      microelectronics processing, follow-on classes in devices and circuits (e.g. solid-
      state devices, VLSI, MEMS, optoelectronics, nanotechnology, etc.)
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other
      outcomes are addressed by the course.: a, e, k

7. Brief list of topics to be covered:
   a. Intro to Microelectronic Fabrication
   b. Semiconductor Substrates
   c. Thermal Oxidation
   d. Diffusion
   e. Ion Implantation
   f. Optical Lithography
   g. Photoresists
   h. Vacuum Systems
   i. Etching
   j. Physical Deposition
   k. Chemical Vapor Deposition
   l. Device Isolation, Contacts, and Metallization
   m. CMOS Technologies
   n. Bipolar Technologies
   o. MEMS
1. **Course number and name:** EEE 4373 - Radio Frequency Electronics

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor's or course coordinator's name:** Dr. Jane Gu

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: This course will cover fundamental RF theory (such as resonant circuits, matching, noise and transmission lines), radio operation, and the design of key RF circuit blocks (such as amplifiers, mixers and oscillators).
   b. prerequisites: EEL 3396 - Solid State Electronic Devices
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction Students will know the basics of electronic components, circuits, and systems and will be able to design electronic circuits and systems to meet desired needs and specifications. Students will be encouraged to engage in life-long learning of electronics and related technologies.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e, k

7. **Brief list of topics to be covered:**
   a. Introduction and Review
   b. Resonant Circuits, Impedance Matching
   c. Transmission Lines
   d. Smith Charts, Impedance Matching w/ Smith Charts
   e. Network Representations, 2 port, S-parameters
   f. LNA; Output Matching, Input Matching, Power Gain
   g. Noise Factors
   h. Stability, Linearity
   i. RF Systems
   j. Oscillators, VCO, Phase Noise
1. **Course number and name:** EEE 4420 – Introductory Quantum Mechanics for Nanodevices

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Jing Guo

   a. other supplemental materials: None

5. **Specific course information**  
   a. catalog description: Physical principles of modern solid-state devices and their applications; quantum mechanics; fundamentals of nanoelectronics  
   b. prerequisites: EEE 3396-Solid State Electronic Devices  
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**  
   a. specific outcomes of instruction: The student will be able to explain the principles underlying the analysis and design of solid-state and nanoscale electronic devices not treated in prerequisite courses and the methods of approximation and physical reasoning basic to that analysis  
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: e, i, k

7. **Brief list of topics to be covered:**  
   a. Wave Behavior of Particles  
   b. Particles in Periodic Potentials  
   c. Particles in Attractive Potentials  
   d. Tunneling of Particles  
   e. Introduction to Nanoelectronics  
   f. Nanoscale silicon transistors  
   g. Low-dimensional electronic and photonic devices
1. **Course number and name:** EEL 2000 - Introduction to Electrical and Computer Engineering

2. **Credits and contact hours:** 2 crs, 2 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Herman Lam

4. **Text book, title, author, and year:** None

5. **Specific course information**
   a. catalog description: Introduction to Electrical and Computer Engineering tools -- hardware and software. Professional ethics, career development. To provide hands-on experience, students assemble and test hardware projects
   b. prerequisites or co-requisites: None
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: not part of the Computer Engineering program (in the EE program only; included here for reference since some Computer Engineering students do take it).

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will know about the Electrical and Computer Engineering profession, particularly the differences in sub-fields. The student will know about career opportunities and how to prepare for them as well as understanding professional issues such as ethics and continuing education
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: not applicable (not in the program)

7. **Brief list of topics to be covered:**
   a. Learning the ropes
      i. Representatives from ECE and College of Engineering Student Services
      ii. Representative from ECE Technical Support
   b. Overview of ECE curriculum and ECE programs
   c. For ECE area in department (guest speakers)
      i. Undergraduate curriculum
      ii. Grad school and research in that area
      iii. What are the career opportunities?
   d. Students’ perspectives
      i. Panel - ECE student leaders, Q&A
   e. Ethical issues for engineers
      i. Student honor code, case examples
      ii. IEEE code of ethics, case examples
      iii. Ethical Issues - patents, copyrights
   f. Other ECE topic
      i. Finding a job, career choices, outsourcing trends, data
   g. Documentation, patents, copyrights
1. **Course number and name:** EEL 3003 - Elements of Electrical Engineering

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50-minutes each

3. **Instructor’s or course coordinator’s name:** Ms. Wenhsing Wu

   - other supplemental materials: None

5. **Specific course information**
   a. **catalog description:** An introduction to the theory and practice of Electrical Engineering for students no majoring in Electrical Engineering; circuits, machines, electronics and systems
   b. **prerequisites:** MAC 2313 - Analytical Geometry & Calculus 3, PHY 2049 - Physics with Calculus 2
   c. **indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program:** Service course – required by other engineering departments. Not in the Computer Engineering program (included here for reference).

6. **Specific goals for the course**
   a. **specific outcomes of instruction:** The non-Electrical Engineering student will know the concepts, vocabulary, and problem-solving methods used in the field of Electrical Engineering
   b. **explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.** not applicable (not in the Computer Engineering program)

7. **Brief list of topics to be covered:**
   a. Circuit Variables
   b. Circuit Elements
   c. Simple Resistive Circuits
   d. Circuit Analysis Techniques
   e. Op Amp circuits
   f. Inductors and Capacitors
   g. First-order Transients
   h. Diodes
   i. AC Circuit Analysis
   j. AC Power
1. **Course number and name:** EEL 3105 - Analytical Methods in Electrical Engineering

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Pramod Khargonekar

   a. other supplemental materials: MATLAB

5. **Specific course information**
   a. catalog description: Application of calculus to develop the analytical tools used in Electrical Engineering. Real and complex functions; linear spaces, eigenvalue problem; linear differential operators; approximation, curve-fitting, interpolation
   b. prerequisites: MAC 2313 - Analytical Geometry & Calculus 3; Programming language
   c. co-requisites: MAP 2302 - Differential Equations; EEL 3135 - Introduction to Signals & Systems
   d. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required for CEE students, elective for CEN students

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will have a basic understanding of the analytical and mathematical fundamentals of the Electrical and Computer Engineering curriculum. The student will be prepared for junior and senior level courses in the areas of control systems, communications, and signal processing
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e

7. **Brief list of topics to be covered:**
   a. Vectors
   b. Introduction to MATLAB
   c. Complex numbers
   d. Linear equations
   e. Matrices
   f. Eigenvalues and eigenvectors
   g. Least squares
   h. Approximation, interpolation, curve-fitting
   i. Differential equations
   j. Difference equations
   k. Introduction to Laplace and Fourier transforms
1. **Course number and name:** EEL 3111C - Circuits I

2. **Credits and contact hours:** 4 crs; 3 classes of 50 minutes each and 1 laboratory section of 3 hours per week

3. **Instructor’s or course coordinator’s name:** Dr. Ramakant Srivastava

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Basic analysis of DC and AC electric circuits. Laboratory
   b. prerequisites: MAC 2313 – Analytical Geometry & Calculus 3; PHY 2049 – Physics with Calculus 2
   c. co-requisites: MAP 2302 – Differential Equations
   d. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will learn how to analyze circuits of arbitrary complexity and be able to specify voltages, currents, and impedance levels at any point(s) in a circuit.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, e, k

7. **Brief list of topics to be covered:**
   a. Definitions and units of basic electrical quantities
   b. Ohm's law and Kirchhoff's laws and series and parallel dc circuit analysis
   c. Dependent sources, input and output resistances, and operational amplifiers
   d. Mesh, loop and nodal analyses of general dc resistive and op-amp circuits
   e. DC network theorems and bridge circuits
   f. Use of PSpice for dc circuit analysis
   g. Capacitors and inductors
   h. First-order transient analysis of RL and RC circuits
   i. Use of PSpice for transient analysis
   j. Sinusoids, phasors, phasor circuits, impedance and admittance
   k. Nodal, mesh and loop analyses of general ac circuits
   l. Network theorems applied to ac circuits; PSpice applied to ac circuits
   m. Bode plots and use of PSPICE to obtain frequency-response plots
   n. Average power, rms values, apparent power, and complex power
1. Course number and name: EEL 3112 - Circuits II

2. Credits and contact hours: 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: Dr. Fred Taylor

   a. other supplemental materials: None

5. Specific course information
   a. catalog description: Continuation of EEL 3111 with emphasis on circuit applications of convolution, the Fourier series, and Laplace and Fourier transforms
   b. prerequisites: EEL 3111-Circuits 1, EEL 3135-Introduction to Signals & Systems, EEL 3105-Analytical Methods in Electrical Engineering
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required for CEE students, elective for CEN students

6. Specific goals for the course
   a. specific outcomes of instruction: The student will know the fundamental theory of Electrical Engineering through analysis, design and implementation of skills in those areas of Electrical Engineering needed to solve problems in the domain of Electrical Engineering
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course: a, k

7. Brief list of topics to be covered:
   a. Intro, continuous-time signals and systems
   b. Continuous-time linear systems: zero-state and zero-input responses, impulse response and convolution
   c. Lumped-parameter systems and circuits – differential eqs: solution in the time domain, natural modes, impulse response. BIBO stability
   e. Spectra of a periodic signals: Fourier transform. Frequency response of LTI systems and impulse response
   f. Frequency response of systems and circuits modeled by diff eqs; passive one-ports, impedance functions
   g. Laplace transform and its application to circuits and system analysis: system function, poles, zeros, natural modes, stability
   h. More frequency response: Bode plots, analog filter design by pole and zero placement
1. **Course number and name:** EEL 3135 - Introduction to Signals and Systems

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Jose Principe

   
a. other supplemental materials: None

5. **Specific course information**
   
a. catalog description: Continuous-time and discrete-time signal analysis including Fourier series and transforms; sampling; continuous-time and discrete-time linear system analysis with FIR and IIR systems; impulse response, frequency response and system function
   
b. prerequisites: CGS 2425 - Introduction to CIS; MAC 2313 - Analytical Geometry and Calculus 3
   
c. co-requisites: MAP 2302 - Differential Equations; One of: EEL 3105 - Analytical Methods in Electrical Engineering, MAS 3114 – Computational Linear Algebra or MAS 4105 - Linear Algebra 1
   
d. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required for CEE students. CEN students may take this or EEL 3308C.

6. **Specific goals for the course**
   
a. specific outcomes of instruction: The student will know the analytical skills necessary for further study in communications, control, and signal processing
   
b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e, k

7. **Brief list of topics to be covered:**
   
a. Intro, sinusoids, intro to computer
   
b. Spectra of periodic signals: Fourier series
   
c. Sampling theory and finite impulse response filters; linear time invariant systems and impulse response
   
d. Sinusoidal response of LTI systems; frequency response of FIR filters
   
e. The z-transform; application to filters
   
f. Infinite impulse response filters
   
g. Basics of continuous-time signals and systems
   
h. Frequency response of continuous-time systems
1. **Course number and name:** EEL 3211 - Basic Electrical Energy Engineering

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Henry Zmuda

   a. *other supplemental materials:* None

5. **Specific course information**
   a. *catalog description:* Analysis and modeling of power system components. Magnetic circuits, energy conservation, transformers, AC and DC rotating machines
   b. *prerequisites:* EEL 3111 - Circuits I
   c. *co-requisites:* EEL 3112 - Circuits II
   d. *indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program:* Selected Elective

6. **Specific goals for the course**
   a. *specific outcomes of instruction:* The student will know the basics magnetic circuits, transformers, motors, and generators and how to design circuits and systems to meet desired needs.
   b. *explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course:* a, k

7. **Brief list of topics to be covered:**
   e. Preliminary Material/Review
   f. Mechanical and Electromagnetic Fundamentals
   g. Three-Phase Circuits
   h. Transformers
   i. Fundamentals of Electro-Mechanical Energy Conversion
   j. AC Machinery Fundamentals
   k. Synchronous Machines
   l. Induction Motors
   m. DC Motors
   n. Stepper Motors
   o. Single Phase Motors (as time permits)
1. **Course number and name:** EEL 3402 - Remote Sensing in Engineering: Science, Sensors and Applications

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Jasmeet Judge

4. **Text book, title, author, and year:** None
   

5. **Specific course information**
   
a. catalog description: The student will have developed an understanding of remote sensing theory, systems and applications using information obtained from the visible/near infrared, thermal infrared and microwave regions of the EM spectrum
   
b. prerequisites: MAP 2302 - Differential Equations or equivalent
   
c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   
a. specific outcomes of instruction: To develop an understanding of remote sensing theory and systems in visible; infrared; and microwave regions of the EM spectrum
   
b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, c, e, g, i, k

7. **Brief list of topics to be covered:**
   
a. Science and Theory of Remote Sensing
      
i. Introduction: Electromagnetic spectrum and applications of remote sensing
      
ii. Radiative transfer theory in VI, IR, and Microwave
   
b. Sensors in Remote Sensing
      
i. Passive sensors used in the Visible, IR, and Microwave regions
      
ii. Active Sensors in Visible/NIR and Microwave regions
   
c. Remote Sensing Applications to Engineering
      
i. Student presentations (Examples include applications for environment, agriculture, hydrology, wireless communications, defense, archaeology, etc)
1. Course number and name: EEL 3472 - Electromagnetic Fields and Applications I

2. Credits and contact hours: 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: Dr. Vladimir Rakov

   a. other supplemental materials: None

5. Specific course information
   a. catalog description: Transmission line equations, electrostatics, magnetostatics, time-varying fields, plane waves, introduction to antennas and waveguides
   b. prerequisites: EEL 3111 - Circuits I
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will know the basics of electromagnetic fields and how to apply concepts to practical problems
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, k

7. Brief list of topics to be covered:
   a. Transmission Lines
   b. Review of Vector Analysis
   c. Electrostatics
   d. Magnetostatics
   e. Time-Varying Fields
   f. Electromagnetic Waves
1. **Course number and name:** EEL 3473 - Electromagnetic Fields and Applications II

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: Dr. Robert Moore

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Maxwell’s equations, electromagnetic wave propagation in different media, antennas, waveguides, numerical methods, electromagnetic coupling
   b. prerequisites or co-requisites: EEL 3472 - Electromagnetics and Field Applications I
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will know Maxwell’s equations and their applications to modern design
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e, k

7. **Brief list of topics to be covered:**
   a. Maxwell’s equations for time-varying fields
   b. Plane-wave propagation
   c. Wave reflection and transmission
   d. Radiation and Antennas
   e. Waveguides
1. Course number and name: EEL 3701C - Digital Logic and Computer Systems

2. Credits and contact hours: 4 crs; 3 classes per week of 50 minutes each, one 3-hour laboratory section per week

3. Instructor’s or course coordinator’s name: Dr. Eric Schwartz

   a. other supplemental materials: None
   b. Required Hardware for Laboratory: UF-designed PCB (with Altera 7064 CPLD) included with lab fee; USB Byte Blaster for programming CPLD

5. Specific course information
   a. catalog description: An overview of logic design, algorithms, computer organization and assembly language programming and Computer Engineering technology. Laboratory.
   b. prerequisites: Programming
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required

6. Specific goals for the course
   a. specific outcomes of instruction: The student will be able to perform elementary manipulations of Boolean algebraic equations; simplify logic expressions; design combinational and sequential circuits; use a digital design and simulation package, use a hardware description language (HDL), analyze binary storage device behavior and applications, and to study the fundamentals of microprocessor architecture, including assembly language programming and the design of basic components of a microprocessor
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: b, c, e, k

7. Brief list of topics to be covered:
   a. Digital Design; Basic Logic; Mixed Logic; ICs
   b. Introduction to Altera’s Quartus for Schematic Entry
   c. Number Systems and Arithmetic; Boolean Algebra, K-Maps
   d. MSI Components: MUX, Demux, Decoder, Encoder, Adder; Tristate Buffer, etc.
   e. Arithmetic Logic Unit (ALU)
   f. Introduction to Sequential Circuits; Flip-Flops; Excitation Tables; Next State Tables
   g. Design with FFs; Counter Design; Debouncing
   h. Intro to VHDL (using Altera’s Quartus)
   i. SRAM and DRAM, ROM; Programmable Logic: PLD, CPLD, Altera MAX7000 CPLD
   j. State Machines: Mealy, Moore; ASM Design and Implementation; ROM-based State Machine Design
k. Intro to Computer Architecture; Addressing Modes; Data Transfer Instructions
l. Instruction Set; Assembly Language Programming; Creating new (Macro) Instructions
m. Computer Operation Cycles and Timing; Memory Maps; G-CPU Processor Architecture
n. Modifying Processor Architecture to Add New Instructions
1. Course number and name: EEL 3834 - C++ Programming for Electrical Engineers

2. Credits and contact hours: 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: 

4. Text book, title, author, and year: None
   a. other supplemental materials: None

5. Specific course information
   a. catalog description: Develop computer skills and art of writing good computer programs using C and C++ languages. Examples and exercises relevant to Electrical Engineering are used
   b. prerequisites or co-requisites: None
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will have a more detailed understanding of data handling, graphics programming, and computer communications in an engineering environment
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, d, e, k

7. Brief list of topics to be covered:
   a. Introduction to the Visual Studio programming environment
   b. Engineering data types / Programming with the console
   c. Saving and storing data to files in text and binary formats
   d. IEEE Floating Point interpretation/ Microcontroller math
   e. Document/View Architecture and the Foundation Classes
   f. Introduction to graphical programming in Windows
   g. Plotting data
   h. Interactive drawing techniques.
   i. Using serialization and advanced object techniques
   j. Serial Communications (RS-232) – ActiveX controls
   k. Packet protocols and error checking
   l. TCP/IP WinSockets programming
   m. Internet programming techniques
1. **Course number and name:** EEL 3923C - Electrical and Computer Engineering Design 1

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Gijs Bosman

4. **Text book, title, author, and year:** None
   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Student teams design, produce and report on a hard-ware prototype, meeting defined specifications and using a structured design methodology. Project management, hardware prototyping, project reporting
   b. prerequisites: EEL 3111 – Circuits I; EEL 3701C - Digital Logic and Computer Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required for CEE students. CEN students take CIS 4912 instead.

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will have the ability to design a hard-ware prototype, meeting defined specifications, using a structured design methodology
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: c, e, g

7. **Brief list of topics to be covered:**
   a. Project Management and Reporting
      i. Gantt chart
      ii. Concept generation matrix
      iii. Concept selection (house of quality) matrix
      iv. Business case, ethics and patent law
      v. Project proposal, system level design, final report
   b. Lab skills
      i. Soldering/desoldering of small components
      ii. Use of logic state analyzer/oscilloscope for trouble shooting
      iii. Protel software for designing PC boards
      iv. Embedded controller (PIC, Atmel, TI, or other) programming
   c. Production of prototype
      i. Product specifications
      ii. Analog and logic circuit design and simulation
      iii. Parts selection and procurement
      iv. PC board design and manufacturing PC board population
      v. Testing, trouble shooting, validation, verification
1. Course number and name: EEL 4201L - Electrical Energy Conversion Lab

2. Credits and contact hours: 1 cr; 1 laboratory section of 3 hours per week

3. Instructor’s or course coordinator’s name: Dr. Henry Zmuda

   a. other supplemental materials: None

5. Specific course information
   a. catalog description: Electric energy conversion, devices and systems
   b. prerequisites: EEL 3211 - Basic Electric Energy Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will assemble and measure three phase power generation. Using an existing lab station, a student will measure performance under loaded conditions of synchronous generators as well as synchronous, induction, and dc-motors. The student will be measure and explain motor torque-load characteristics under various operating conditions explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: b, e, k

7. Brief list of topics to be covered:
   a. Laboratory 1: Three-Phase Power Measurements
   b. Laboratory 2: Single-Phase Transformers
   c. Laboratory 3: Three-Phase Transformers
   d. Laboratory 4: DC Motors
   e. Laboratory 5: DC Generators, Part A: Machine Constant, DC Generator Output Characteristics
   f. Laboratory 6: DC Generators, Part B: Generator Torque Calculations
   g. Laboratory 7: AC Synchronous Generators: {V, I, P}-Relations, Synchronous Impedance Calculations
   h. Laboratory 8: Induction Motors, Part A: Torque-Speed Characteristics
   i. Laboratory 9: Induction Motors, Part B: Equivalent Circuit Parameters
1. Course number and name: EEL 4242C - Power Electronics

2. Credits and contact hours: 3crs; 2 classes per week of 50 minutes each, 1 3-hour laboratory section

3. Instructor’s or course coordinator’s name:

   a. other supplemental materials: None

5. Specific course information
   a. catalog description: Circuit topologies, analysis, design and simulation of electronic circuits such as power supplies and motor drives. Laboratory
   b. prerequisites: EEE 3308 - Electronic Circuits I
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will know the basics of power electronic components, circuits, and systems and will be able to design power electronic circuits and systems to meet desired needs. Engage in life-long learning.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, e, i, k

7. Brief list of topics to be covered:
   a. DC-DC Conversion: PWM Topologies & DC Analysis
   b. Magnetics: Materials, Transformers, & Inductors
   c. Capacitors
   d. Power Semiconductors: Models, Drives, & Protection
   e. Thermal Management
   f. Dynamics of PWM DC-DC Converters
   g. Closed-Loop Control of PWM DC-DC Converters
   h. Integrated Circuits for Power Electronic
   i. Simulation of Power Electronic Circuits
   j. Design Project
1. Course number and name:  EEL 4351 - Electronic Device Fundamentals

2. Credits and contact hours:  3crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name:  Dr. Jing Guo

   a. other supplemental materials: None

5. Specific course information
   a. catalog description: Physical principles of modern solid-state devices and their practical applications
   b. prerequisites:  EEL 3396 - Solid State Devices
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program:  Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will be able to explain the principles underlying the analysis and design of solid-state devices not treated in prerequisite courses and the methods of approximation and physical reasoning basic to that analysis
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: e, i, k

7. Brief list of topics to be covered:
   a. Wave Behavior of Particles
   b. Particles in Periodic Potentials
   c. Particles in Attractive Potentials
   d. Tunneling of Particles
   e. Introduction to Nanoelectronics
   f. Nanoscale silicon transistors
   g. Low-dimensional electronic and photonic device
1. **Course number and name:** EEL 4440 - Optical Communication Systems

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Huikai Xie


5. **Specific course information**
   a. catalog description: Introduction to electromagnetic waves, dielectric waveguides and fibers, propagation characteristics of fibers, characterization methods, LEDs and laser diodes, photodetector optical receivers and communication systems
   b. prerequisites: EEL 3472 - Electromagnetic Fields and Applications I
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will understand the fundamentals of the operation and design of fiber optic systems and components employed in such communication systems
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e, k

7. **Brief list of topics to be covered:**
   a. Overview of fiberoptic communication systems: evolution, nature of light, advantages and applications
   b. Optics Review: Ray theory, lenses, imaging, numerical aperture, diffraction
   c. Lightwave fundamentals: introduction to electromagnetic waves, wave equations, group velocity, dispersion, polarization, resonant cavities, total internal reflection
   d. Integrated optic waveguides: dielectric-slab waveguide, modes, coupling, dispersion, integration
   e. Optic fibers: step-index fibers, graded-index fibers, modes and fields in fibers, pulse broadening and information rate, fiber fabrication and characterization
   f. Optical sources and amplifiers: PN junction, LEDs, laser principles, laser diodes, tunable laser diodes, VCSELs, modulation, optical amplifiers
   g. Optical receivers: photomultipliers, photodiodes, APDs, responsivity, quantum efficiency, noise, heterodyne detection, preamplifiers
   h. WDM concepts and components: WDM principles, N ‘N couplers, star couplers, add/drop multiplexers, fiber grating filters, tunable sources, and tunable filters
   i. All optical-switching: advantages, MEMS introduction, optical MEMS devices for optical switching
1. **Course number and name:** EEL 4445 - Optics for Engineers

2. **Credits and contact hours:** 4 crs;

3. **Instructor’s or course coordinator’s name:** Dr. Ramakant Srivastava

4. **Text book, title, author, and year:** None
   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Nature of light, radiometry, laser basics, interferometry, holography, coherence, polarization, diffraction, fiber optics, Fourier optics
   b. prerequisites: EEL 3472 - Electromagnetics and Field Applications I
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will have a basic knowledge of optics and related applications
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e, k

7. **Brief list of topics to be covered:**
   a. Electromagnetic spectrum, radiometry, photometry, blackbody radiation
   b. Reflection in plane mirrors, refraction through plane surfaces; imaging by anoptical systemReflection in plane mirrors, refraction through plane surfaces; imaging by anoptical system at a spherical surface, refraction at a spherical surface, thin lenses, vergence and refractive power, Newtonian equation for the thin lens
   c. Einstein's quantum theory of radiation, essential elements of a laser, simplified description of laser operation, characteristics of laser light, laser types and parameters
   d. One-dimensional wave equation, plane waves, spherical waves, complex refractive index, electromagnetic waves, superposition principle, superposition of waves of the same frequency, random and coherent sources, standing waves, phase and group velocities
   e. Two beam interference in dielectric films, the Michelson interferometer, Stokes' relations, multiple-beam interference in a parallel plate, Fabry-Perot interferometer, fringe profiles, the Airy function, resolving power, free spectral range
   f. Mathematical representation of polarized light-Jones vectors, mathematical representation of polarizers-Jones matrices, birefringence-polarization with two refractive indices, double refraction
   g. Diffraction from a single slit, beam spreading, rectangular and circular apertures, resolution, double-slit diffraction, diffraction from many slits, the grating equation, free spectral range of a grating, dispersion of a grating, resolution of a grating
h. Transfer matrix, reflectance at normal incidence, two-layer anti-reflecting films, three-layer anti-reflecting films
i. Optics of propagation in fibers, attenuation, distortion of pulses and dispersion
j. Fourier analysis of finite harmonic wave train, temporal coherence, natural line width, optical data processing, Fourier transform spectroscopy
1. **Course number and name:** EEL 4458 - Fundamentals of Photonics

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Huikai Xie

   - a. other supplemental materials: Extensive course notes and journal papers are distributed

5. **Specific course information**
   - a. catalog description: Review of electromagnetic fields and waves, energy bands in semiconductors, p-n junctions and optical properties of semiconductors. Fundamentals of optical modulators and waveguides, and photonic applications:
   - c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   - a. specific outcomes of instruction: The student will learn the unique aspects of the propagation of light in physical media, learn to quantify and exploit where possible the effects of dispersion and nonlinearities, particularly optical amplification, and learn how to design an electrically modulated photonic system to meet specific performance requirements with regard to signal-to-noise ratio, dynamic range, and bandwidth
   - b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: EE2, EE3, a, c, e, i

7. **Brief list of topics to be covered:**
   - a. Review of Electromagnetic Field Theory
   - b. Planar Waveguides
   - c. Dispersion
   - d. Graded Index Waveguide
   - e. Optical Fiber
   - f. Loss and Nonlinear Phenomenon
   - g. Detection and Noise
   - h. Semiconductor Photodectors
   - i. Electrical Modulation of Light
   - j. Rectangular Waveguide
   - k. Optical Couplers
1. Course number and name: EEL 4461 - Antenna Systems

2. Credits and contact hours: 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: Dr. Jenshan Lin

   a. other supplemental materials: None

5. Specific course information
   a. catalog description: Electromagnetic field theory and its application to antenna design
   b. prerequisites: EEL 3472 - Electromagnetic Fields and Applications I
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will understand the fundamental principles of antenna theory and will know how to apply them to the design and analysis of antenna systems.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e, k

7. Brief list of topics to be covered:
   a. Introduction; Review of electromagnetic field theory
   b. Introduction of different types of antennas and their applications
   c. Antenna radiation pattern, power density, and intensity
   d. Antenna beamwidth, directivity, efficiency, gain
   e. Antenna polarization, input impedance, effective aperture
   f. Friis transmission equation and radar range equation
   g. Far-field radiation and vector potential
   h. RF propagation, weather effect, RF safety
   i. Dipole antennas
   j. Ground reflection effect of vertical and horizontal dipoles
   k. Antenna design using CAD
   l. Microstrip patch antennas
   m. Loop antennas
   n. Antennas and wireless system
1. Course number and name: EEL 4514 - Communication Systems and Components

2. Credits and contact hours: 3 crs; 3 classes per week of 50 minutes each

3. Instructor’s or course coordinator’s name: Dr. Tan Wong

   a. other supplemental materials: MATLAB Student Version, MathWorks, Inc

5. Specific course information
   a. catalog description: Theory of communication, and applications to radio, television, telephone, satellite, cellular telephone, spread spectrum and computer communication systems
   b. prerequisites: EEL 3111-Circuits II; EEL 3135-Introduction to Signals & Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. Specific goals for the course
   a. specific outcomes of instruction: The student will learn to: Work with equations for propagation; Translate between signals and their Fourier transforms; Represent analog signals by digital signals; Determine bandwidth; Translate between bandpass and complex baseband representations; Perform filtering in the time and frequency domains; Identify pros and cons of common analog and digital communications; Perform system-level design of receivers for common modulations; Simulate communication systems
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, e, h, i, k

7. Brief list of topics to be covered:
   a. Introduction and history
   b. Channels and propagation
   c. Signals and spectra
   d. Baseband analog communications
   e. Bandpass analog communications
   f. Baseband digital communications
   g. Bandpass digital communications
   h. Applications
1. **Course number and name:** EEL 4514L - Communications Laboratory

2. **Credits and contact hours:** 1 cr; 1 laboratory section of 3 hours per week

3. **Instructor’s or course coordinator’s name:** Dr. Tan Wong

4. **Text book, title, author, and year:** In-house Lab manual provided by instructor
   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Communication circuits and radio frequency instruments, devices and measurements
   b. prerequisites: EEE 3308 – Electronic Circuits I
   c. co-requisites: EEL 4514 - Communication Systems and Components
   d. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will be familiar with RF test equipment; to give student ability to design and verify the performance of communication circuits
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, e, g, h, i, k

7. **Brief list of topics to be covered:**
   a. RF measuring equipment such as digital storage oscilloscope, function generator, and spectrum analyzer
   b. Frequency response of systems and distortion
   c. Sinusoidal oscillators
   d. Amplitude modulation and envelope detection
   e. Phase-Locked Loop and Frequency Modulation
   f. Phase-Locked Loop as FM and FSK demodulator
   g. Sampling and pulse amplitude modulation
   h. Inter-symbol interference and eye patterns
   i. Mini-project: a Morse code transceiver based on On-Off Keying
1. **Course number and name:** EEL 4516 - Noise in Devices and Communication Systems

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. John Shea

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Origin, characterization and measurement of random noise. Calculation of signal-to-noise ratios and probability of errors in communication systems.
   b. prerequisites: EEL 4514 - Communication Systems and Components
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will be able to use probability to model and analyze communications; choose appropriate modulations based on system constraints; design optimal demodulators (in terms of minimizing probability of error) for common signaling formats; evaluate the error probability for common modulations; and evaluate the use of error-control coding with digital modulation
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, e, i, j, k

7. **Brief list of topics to be covered:**
   a. Introduction of Probability in Communications
   b. Random Signals: Probability; Random Variables; Statistical Decision Theory; Random Processes and Filtering of Random Processes
   c. Digital Communications
      i. formatting and baseband modulation; Baseband demodulation and detection;
      ii. Bandpass modulation and demodulation;
      iii. Basics of error control coding;
      iv. Multiplexing and multiple access;
      v. Cellular communications; Spread-spectrum techniques
1. **Course number and name:** EEL 4523 - Audio Engineering

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. John Shea

   a. **other supplemental materials:** None

5. **Specific course information**
   a. **catalog description:** The student will know the underlying theory of acoustics, electronics, and signal processing, and will be able to demonstrate modern audio engineering practice as applied to music, home audio, recording and sound reinforcement.
   b. **Prerequisites:** EEL 3111C Circuits 1 or EEL 3003 Elements of Electrical Engineering or consent of instructor
   c. **indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program:** Selected Elective

6. **Specific goals for the course**
   a. **specific outcomes of instruction:** The student will understand the underlying theory of acoustics, electronics and signal processing and will be able to demonstrate modern engineering practice as applied to music, home audio, recording and sound reinforcement.
   b. **explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course:** EE1, a, k

7. **Brief list of topics to be covered:**
   - Signals
   - Second-order systems and resonance
   - Acoustics of sound generation, musical instruments
   - Microphones
   - Signal chains
   - Signal interfacing
   - Operational amplifiers and their application in audio signal processing
   - Signal processing blocks (mixer, compressor, phaser, equalizer, etc.)
   - Loudspeakers
   - Room acoustics
   - Psychoacoustics
1. **Course number and name:** EEL 4598 - Data and Computer Communications

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Janise McNair

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Intro to the principles and practice of computer networking, emphasizing data communication and the lower layers of the OSI and TCP/IP protocol architectures
   b. prerequisites: EEL 3701 - Digital Logic and Computer Systems; EEL 4514 - Communications Systems and Components Programming
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will understand the principles and practice of data and computer communications
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e, k

7. **Brief list of topics to be covered:**
   a. OSI and TCP/IP protocol architectures
   b. Physical layer: Data transmission (signals, noise, Fourier representation); Physical media (copper wire, fiber, wireless); Analog and digital transmission (encoding, modulation, SNR); Multiplexing and switching (crossbar, TDM, STM, ATM)
   c. Data link layer: Framing and Synchronization (bit, character stuffing); Error detection and correction (bit errors, parity checks); Coding (cyclic redundancy check, shift registers); Error control techniques (ATM, HDLC); Flow control protocols (Stop & Wait, Sliding Window); Automatic Repeat reQuest (ARQ) protocols
   d. Medium access control layer: Local area network topologies; Medium access protocols; Local area networks (Ethernet, IEEE 802.x standards)
   e. Network layer: Network layer switching (packet, circuit, virtual circuit); Internetworking (Internet Protocol, fragmentation, subnets)
1. **Course number and name:** EEL 4610 - State Variables and Control

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Haniph Latchman

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Development of state-variable approach to linear continuous-time and discrete-time systems with emphasis on the design of feedback control systems
   b. prerequisites: EEL 3135 - Signals and Systems; EEL 3112 - Circuits II
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The topics in this course are part of the fundamental theory of Electrical Engineering and provides depth in the analysis, design, and implementation skills in those areas of Electrical Engineering needed to solve problems in the domain of Electrical Engineering.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, c, e, k

7. **Brief list of topics to be covered:**
   a. Introduction; historical perspective; control problem
   b. Review of input-output linear systems; the concept of state
   c. Linear algebra
   d. Solution of state equations
   e. Stability: zero-state stability, zero-input stability, asymptotic stability
   f. Controllability and observability; canonical decompositions
   g. State feedback: regulation, tracking, disturbance rejection, stabilization
   h. State estimation and feedback
   i. Minimal realizations
   j. Pole placement
   k. Model matching
1. **Course number and name:** EEL 4657 - Linear Control Systems

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Jacob Hammer

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Theory and design of linear control systems
   b. prerequisites: EEL 3111 - Circuits II; EEL 3135 - Signals and Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will understand the essentials of mathematical system modeling, be able to assess stability and performance properties of linear systems, and design lead and lag controllers for linear systems using s-domain and frequency domain techniques.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, c, e, k

7. **Brief list of topics to be covered:**
   a. System Models – Differential Equations, State Space, Transfer Functions, Discrete-Time
   b. Block Diagrams and Masons Formula
   c. Stability Analysis – BIBO and Asymptotic Stability, Stability Margins, Routh Hurwitz
   d. Performance Criteria: Steady State Error and Transient Behavior, Percentage Overshoot, Settling Time, Rise Time
   e. Root Locus & Simple Proportional Gain Controller Design
   f. Lead and Lag Design in the s-domain
   g. Nyquist Stability Criterion and Bode Plots
   h. Controller Design in the Frequency Domain
   i. Advanced Topics
1. **Course number and name:** EEL 4657L - Linear Control Systems Laboratory

2. **Credits and contact hours:** 1 cr; 1 3-hour laboratory section

3. **Instructor’s or course coordinator’s name:** Dr. Jacob Hammer

4. **Text book, title, author, and year:** None
   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Practical applications of linear control theory
   b. prerequisites: EEL 4657 - Linear Control Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will know how to investigate some of the practical aspects in applying classical analog and digital control theory to simple dynamic systems; to introduce computer software to help design and analyze the compensators
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, e, k

7. **Brief list of topics to be covered:**
   a. Measuring the characteristics of a DC motor using frequency domain technique
   b. Model verification using DSP board and MATLAB
   c. Introduction to CC
   d. Lag compensation
   e. Introduction to digital control
   f. High level language implementation of a digital controller
   g. Control of an unstable system - Part I
   h. Control of an unstable system - Part II
   i. Digital Control of an unstable system
1. **Course number and name:** EEL 4665 - Intelligent Machine Design Lab

2. **Credits and contact hours:** 4 crs; 2 classes of 50 minutes each and 2 laboratory sections of 3 hours each per week

3. **Instructor’s or course coordinator’s name:** Dr. Antonio Arroyo

4. **Text book, title, author, and year:** None
   
   a. other supplemental materials: None
   
   b. required hardware for laboratory: Students purchase microprocessor board from industry and build a mobile robotic platform using balsa wood

5. **Specific course information**
   
   a. required hardware for laboratory: Students purchase microprocessor board from industry and build a mobile robotic platform using balsa wood
   
   b. Senior standing and EEL 4744-Microprocessor Applications or consent of instructor
   
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   
   a. specific outcomes of instruction: The student will be able to design a robotic mobile platform from scratch and have it perform elementary behaviors in one semester
   
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, e, f, g, k

7. **Brief list of topics to be covered:**
   
   a. Sensors; IC Implementation of Sensors
   
   b. Introduction to Altium Designer for Schematic Entry
   
   c. Introduction to Solidworks & Inventor for CAD Design
   
   d. Introduction to IDE Usage (using Atmel’s AVRStudio)
   
   e. Assembly Language & C Behavior Programming
   
   f. Usage of randomness in robotics
   
   g. Robot Control, Fuzzy Logic, Behavior Programming
   
   h. Navigation, Obstacle Avoidance, Behavior Fusion
1. **Course number and name**: EEL 4712C - Digital Design

2. **Credits and contact hours**: 4 crs; 3 classes of 50 minutes each and 1 laboratory section of 3 hours per week

3. **Instructor’s or course coordinator’s name**: Dr. Herman Lam

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Advanced modular logic design, design languages, “finite” state machines and binary logic. Laboratory
   b. prerequisites: EEL 3701 - Digital Logic and Computer Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will know the fundamentals, methodologies, and techniques for the structured design of digital systems, using the state of the art technologies and design environments and tools
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course: a, c, e, k

7. **Brief list of topics to be covered**:
   a. Digital design technologies
      1. Review, of digital components (MUX, decoders, flip-flops, counters, VHDL, etc.); VHDL Implementation
      2. Carry-look-ahead adders, ALUs.
      3. Programmable logic devices: PAL’s, PLA’s, PROM’s, CPLD’s, and FPGA’s
      4. Memories - RAM, DRAM, and ROM
   b. Digital design methodology and techniques for finite state machines (FSM)
      1. Top-down, modular design
      2. Controller/controlled-component architecture
      3. ASM fundamentals and design methods
      4. Design methods – traditional, MUX, ROM, “one-hot”, CPLDs, FPGA’s
      5. Testing and design for testing, Digital design examples (labs)
   c. Design environments and tools (lab-intensive)
      1. Design life cycle using model digital development environment
      2. Design specification: graphical, VHDL
      3. Logic synthesis
      4. Simulation: functional and timing
      5. Timing analysis, Device program, Testing
1. **Course number and name:** EEL 4713C - Digital Computer Architecture

2. **Credits and contact hours:** 4 crs; 3 classes of 50 minutes each and 1 laboratory section of 3 hours

3. **Instructor’s or course coordinator’s name:** Dr. Renato Figueiredo

   a. other supplemental materials: None

5. **Specific course information**
   b. prerequisites: EEL 3701 - Digital Logic and Computer Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will be able to solve complex scientific and engineering problems using digital electronic computational machines; will be able to show the elements of a development system for solutions to problems in this field; have an introductory knowledge of VHSIC Hardware Description Language (VHDL) as a tool for design and synthesis of complex digital systems; will be able to apply it to the implementation of microprocessors using micro-programmed state machines; will be able to show the interplay between algorithms and the architectures on which they run
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, c, e, k

7. **Brief list of topics to be covered:**
   b. Instruction set architecture design and hardware/software interface.
   c. Organization of single- and multi-cycle RISC microprocessors.
   d. Introduction to the design of key datapath components (ALU, registers, shifters, sign-extenders) using VHDL behavioral and structural descriptions. Introduction to micro-programming.
   e. Performance: measurement, metrics, summarization and interpretation.
   f. Number systems: representation and operations; fixed and floating-point implementations.
   g. Pipelining. Data hazards and forwarding. Superscalar design.
   h. Memory hierarchies, caches: organization, implementation and performance.
   i. Virtual memory: address translation, placement, look-aside buffers.
   j. Input/output. Disk technologies, busses and protocols. I/O system design. Redundant arrays of inexpensive disks (RAID)
1. **Course number and name:** EEL 4720 – Reconfigurable Computing

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor’s or course coordinator’s name:** Dr. Gregory Stitt

4. **Text book, title, author, and year**
   b. other supplemental materials

5. **Specific course information**
   d. catalog description: Fundamental concepts at advanced undergraduate level in reconfigurable computing based upon advanced technologies in field-programmable logic devices. Topics include general concepts, device architectures, design tools, metrics and kernels, system architectures, and application case studies.
   e. Prerequisites: EEL4712C or EEL5764 or consent of instructor
   f. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   c. specific outcomes of instruction: The student will have a fundamental knowledge and understanding of principles and practice in reconfigurable architecture and computing
   d. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course: a, c, e, k

7. **Brief list of topics to be covered:**
   - General overview (< 1 week)
     - Goals and motivations
     - History, state of the art, future trends
     - Basic concepts and related fields of study
     - Performance, power, and other metrics
     - Algorithm analysis and speedup projections
     - VHDL tutorial
   - RC Architectures (~1 week)
     - Device characteristics
     - Fine-grained architectures
     - Coarse-grained architectures
   - FPGA Physical Design Tools (~1 week)
     - Technology mapping
     - Placement & routing
   - Register Transfer (RT)/Logic Synthesis (1-2 weeks)
     - Controller/Datapath synthesis
     - Logic minimization
   - RC Application Design (1-2 weeks)
- Parallelism
- Systolic arrays
- Pipelining
- Optimizations
- Bottlenecks
- High-level Design (~3 weeks)
  - High-level synthesis
  - High-level languages
  - Design tools
- Hybrid architectures (~1 week)
  - Hybrid architectures
  - Communication
  - Hw/sw partitioning
  - Soft-core microprocessors
- System architectures (2-3 weeks)
  - System design strategies
  - System services
  - Small-scale architectures
  - HPC architectures
  - HPEC architectures
  - System synthesis
  - Architectural design space explorations
- Case Studies (~1 week)
  - Signal and image processing
  - Bioinformatics
  - Security
- Special Topics (~2 weeks)
  - Partial Reconfiguration
  - Numerical Analysis
  - Performance Analysis/Prediction
  - Fault Tolerance
1. **Course number and name:** EEL 4744C - Microprocessor Applications

2. **Credits and contact hours:** 4 crs; 3 classes of 50 minutes each and 1 laboratory section of 3 hours per week

3. **Instructor's or course coordinator’s name:** Dr. Tao Li

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Elements of microprocessor-based systems; hardware interfacing and software design for their application. Laboratory
   b. prerequisites: EEL 3701 - Digital Logic and Computer Systems
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will understand the functional and technological characteristics of microprocessor structures, memory components, peripheral support devices, and interface logic.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, c, e, g, k

7. **Brief list of topics to be covered:**
   a. Introduction
   b. Programming Model and Addressing Models
   c. 68HC12 Instruction Set, Design, Programming
   d. Computer Buses and Parallel I/O
   e. Interrupts and Real-Time Events
   f. Memory concepts and interfacing
   g. Timers
   h. Serial I/O
   i. Analog I/O
1. **Course number and name:** EEL 4750C - Introduction to Digital Signal Processing

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each

3. **Instructor's or course coordinator's name:** Dr. Fred Taylor

   a. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Fundamentals of filter design and Fourier transforms. Hardware implementation of filters. Simulation of signal processing systems using MATLAB
   b. prerequisites: EEL 3135 - Signals and Systems, EEL 4744 - Microprocessor Applications
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Selected Elective

6. **Specific goals for the course**
   a. specific outcomes of instruction: The student will understand the fundamentals of digital signal processing (DSP) theory and practice as required of an entry-level DSP engineer.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, e, k

7. **Brief list of topics to be covered:**
   a. Signals and signal taxonomy
   b. Sampling theorem and aliasing, MATLAB studies
   c. Analog to digital and digital to analog conversion, and signal generation, DSK sampling project
   d. z-transform signal and system representation, MATLAB studies
   e. Discrete Fourier Transform, MATLAB studies, and DSK Fourier project
   f. Linear system properties and behavior, MATLAB studies
   g. Finite impulse response filters (FIR), MATLAB studies, and DSK FIR project
   h. Infinite impulse response filters (IIR), MATLAB studies
   i. IIR architectures, state variable representation, MATLAB studies, and DSP IIR project.
   j. Survey or advanced DSP applications, concepts and technology (adaptive filters, multi-rate, technology)
1. **Course number and name:** EEL 4924 - Electrical & Computer Engineering Design

2. **Credits and contact hours:** 3 crs; 3 classes per week of 50 minutes each, open laboratory

3. **Instructor’s or course coordinator’s name:** Dr. Karl Gugel

4. **Text book, title, author, and year:** None
   b. other supplemental materials: None

5. **Specific course information**
   a. catalog description: Selected design projects involving engineering applications in the various areas of Electrical Engineering. Must be taken prior to the semester of graduation. Laboratory
   b. prerequisites or co-requisites:
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: Required for CEE students. CEN students take CIS 4912.

6. **Specific goals for the course**
   c. specific outcomes of instruction: The student will have an understanding of how a design project goes from written proposal through implementation, technical documentation, and oral presentation of the completed design project all while meeting various deadlines.
   d. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: a, b, c, d, e, f, g, h, i, j, k

7. **Brief list of topics to be covered:**
   a. IEEE Code of Ethics
   b. Design project abstract
   c. Design proposal
   d. Project progress meetings presented weekly
   e. Design patent checked weekly
   f. Project technical report
   g. Project marketing presentation
   h. Project demonstration
1. Course number and name: EGN 1935 - ECE Adventures

2. Credits and contact hours: 2 crs; 1 class per week of 2 hours

3. Instructor’s or course coordinator’s name: Dr. Eric M. Schwartz


5. Specific course information
   a. If you are unsure of your major or would like to learn about potential experiences of an Electrical and Computer Engineering student, this course is for you! In EGN 1935 you’ll learn about robots and experiment with sensors and actuators. This process will help you discover many basic ECE concepts. Grades are based on attendance and class participation. No exams
   b. prerequisites: Freshman or Sophomore Standing
   c. indicate whether a required, elective, or selected elective (as per Table 5-1) course in the program: not in the program (included for reference; Computer Engineering students sometimes take it as a exploratory course)

6. Specific goals for the course
   a. specific outcomes of instruction: The student will have a basic knowledge of the fundamentals components and tools used by Electrical and Computer Engineers.
   b. explicitly indicate which of the student outcomes listed in Criterion 3 or any other outcomes are addressed by the course.: not applicable (not in the CE program)

7. Brief list of topics to be covered:
   a. Power supplies, batteries, multi-meters, oscilloscopes, waveform generators, soldering, hand tools, rudimentary robot programming.
   b. Proto-boards, switches, LEDs, resistors, bump sensors, pull-up/down resistors.
   c. Voltage divider, CdS cells, potentiometers.
   d. IR, modulated IR emitter/detectors, PWM signals, DC motors, servos.
   e. Other sensors, actuators, simple behavior software
   f. Intermediate robot programming, behavior creation and arbitration.
   g. Piezo speakers, sound generation, digital signals, sampled data, sampling frequency, sonar.
   h. Cameras, magnetic sensors, laser bump, modern sensors and actuators, radio frequency, ICs
Appendix B1 – Faculty Vitae (CISE Faculty)

All academic and non-academic experience is full-time unless indicated otherwise.
Name: ARUNAVA BANERJE

Education: Ph.D, Computer Science, Rutgers, The State University of New Jersey, 2001

Academic experience:
- University of Florida, CISE, Associate Professor (2009- Present)

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, Society for Neuroscience

Honors and awards:
- 2000-2001 Rutgers Graduate School Research Award

Service activities:
- Faculty Advisor for Indian Students Association, University of Florida

Most important publications and presentations from the past five years:

Most recent professional development activities: see service activities above
Name: MANUEL BERMUDEZ

Education: Ph.D., Computer and Information Science, Univ. of California, Santa Cruz, 1984

Academic experience:
- University of California Santa Cruz, Visiting Assistant Professor, 1984-85
- University of Florida, Assistant Professor, 1985-1990
- University of Florida, Associate Professor, 1990-present

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: none

Honors and awards:
- UF Teaching Improvement Award, 1995.
- Fulbright Scholar, visiting University of Costa Rica, 1996-97.
- Fulbright Scholar, visiting Universidad de los Andes, Venezuela, 2003-04.
- Honorary Doctorate, Universidad Particular de Chiclayo, Chiclayo, Perú, April 21, 2008
- Invited Speaker, “El Futuro de la Acreditación: La Perspectiva de ABET” at Jornada Informáticos de Calidad; Generando Competitividad Internacional, a seminar organized by SINAES (Sistema Nacional de Acreditación de la Educación Superior), Nov. 28, 2008, San José, Costa Rica.
- Keynote Speaker, V COREIS (Congreso Regional de Estudiantes de Ingeniería de Sistemas), Universidad de Chiclayo, Chiclayo, Perú, April 21, 2008.
- Keynote Speaker, Congreso Regional de Estudiantes de Ingeniería de Sistemas, Chiclayo, Peru, April 28-30, 2010.

Service activities:
- Member, Admissions Committee, CISE Dept.
- Member, ABET Committee, CISE Dept.
- Systems Area Coordinator, Ph.D. Qualifying Exam Committee, CISE Dept.
- Guest Editor, CLEI Electronic Journal, for special edition following co-Chairman position for CLEI (Centro Latinoamericano de Educación en Informática) conference in 2007.
- Program Committee co-Chair, CLEI (Centro Latinoamericano de Educacion en Informatica), Costa Rica, 2007.
• Member, International Accreditation visit team, to evaluate the Systems Engineering program at the Universidad Interamericana de Costa Rica, sponsored by SINAES (Sistema Nacional de Acreditación de la Educación Superior de Costa Rica), Nov. 24-28, 2008. Chair, International Accreditation visit team, 2009.
• Panel member, National Science Foundation CSR Small Panel (Operating Systems and Compilers), Washington DC, March 11-12, 2010.

Most important publications and presentations from the past five years:
• Carl Crane and Manuel E. Bermudez, “The Autonomous Vehicle Program at the University of Florida”, proceedings of the Fifth Latin American and Caribbean Conference for Engineering and Technology, May 29, June 1, 2007, Tampico, Mexico

Most recent professional development activities:
• Taught many short courses on Software Engineering, Compiler Construction in Latin America,
• Have given over 40 invited talks in Latin America.
• Have served as faculty coach on eight IPPD projects.
• see also service activities above
Name: SHIGANG CHEN

Education: University of Illinois at Urbana-Champaign, Ph.D. in CS, May 1999

Academic experience:
- Associate Professor, CISE, U. of Florida, 2008 - present
- Assistant Professor, CISE, U. of Florida, 2002 – 2008

Non-academic experience:
- Senior Software Engineer, Cisco Systems, Network Security, 1999 - 2002

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, ACM

Honors and awards:
- NSF CAREER Award, 2007
- IEEE Communications Society Best Tutorial Paper Award, 1999

Service activities:
- Member of the Steering Committee for IEEE IWQoS
- Area co-chair for Network and Data Communications Track of the 10th International Symposium on Pervasive Systems, Algorithms and Networks (I-SPAN), 2009
- TPC co-chair for the 17th IEEE International Workshop on Quality of Service (IWQoS), 2009
- Guest editor for Journal of Advances in Multimedia, Special Issue on Towards the Next Generation Peer-to-Peer Services, 2007
- Guest editor for IEEE Transactions on Vehicle Technologies, Special Section on Cross-layer Design in Mobile Ad Hoc Networks and Wireless Sensor Networks, 2005
- Guest editor for ACM/Baltzer Journal of Wireless Networks (WINET), Special Issue on Wireless Quality-of-Service Support, 2005
- Track co-chair for Wireless and Mobile Network Architecture of 16th International Conference on Computer Communications and Networks, 2007
- Vice general chair for Second International Conference on Quality of Service in Heterogeneous Wired/Wireless Networks, 2005
- TPC vice chair for the Second IEEE International Conference on Mobile Adhoc and Sensor Systems (MASS’05), 2005
- TPC co-chair for First International Conference on Quality of Service in Heterogeneous Wired/Wireless Networks, 2004
- TPC member for INFOCOM’11, ICDCS’11, QShine’11, INFOCOM’10, ICCCN’10,
MASS’10, ICC’10, GLOBECOM’10, RTSS’09, ICCCN’09, INFOCOM’09, ChinaCom’08, SecureCom’09, INFOCOM’08, ACST’08, GLOBECOM’07, SAWN’07, WCNC’07, ICNP’06, MTUC’06, MultiSec’05, INFOCOM’05, WiQoS’05, SANS’05, ISCC’05, ACST’04, COQODS’04, GLECOM’04, ISCC’04, CNIS’03, CST’03, ICNP’01

- Keynote speaker for the 3rd China Wireless Sensor Networks, SuZhou, China, 2009
- NSF review panelist for five times
- Session chair in INFOCOM’09, ICDCS’08, INFOCOM’05 and INFOCOM’04

Most important publications and presentations from the past five years:

- Tao Li, Shigang Chen, Yibei Ling, Fast and Compact Per-Flow Traffic Measurement through Randomized Counter Sharing, in Proc. of IEEE INFOCOM’10, Shanghai, China, April, 2011.
- Tao Li, Shigang Chen, Wen Luo, Ming Zhang, Scan Detection in High-Speed Networks Based on Optimal Dynamic Bit Sharing, in Proc. of IEEE INFOCOM’10, Shanghai, China, April, 2011.
- Zhuo Huang, Jih-Kwon, Shigang Chen, Approximately-Perfect Hashing: Improving Network Throughput through Efficient Off-chip Routing Table Lookup, in Proc. of IEEE INFOCOM’10, mini-conference, Shanghai, China, April, 2011.
- Zhuo Huang, David Lin, Jih-kwon Peir, Shigang Chen, S. M. Iftekharul Alam, Fast Routing Table Lookup Based on Deterministic Multi-hashing, in Proc. of IEEE International Conference on Network Protocols (ICNP’10), Kyoto, Japan, October 2010.
- Myungkeun Yoon, Tao Li, Shigang Chen, Jih-kwon Peir, Fit A Spread Estimator in A Small Memory, in Proc. of IEEE INFOCOM’09, Rio de Janeiro, Brazil, April 2009. (The journal version is directly accepted by IEEE/ACM Transactions on Networking after the first round of reviews.)
- Tao Li, Shigang Chen, Yibei Ling, Identifying the Missing Tags in a Large RFID System, in Proc. of ACM Mobihoc, Chicago, IL, USA, September, 2010.

Most recent professional development activities: see service activities above
Name: RANDY Y. C. CHOW

Education: PhD, Computer Science, Univ. of Massachusetts, 1977.

Academic experience:
- University of Florida, CISE, Professor, 1988 – present, full time
- University of Florida, CISE, Associate Professor, 1981 – 1988, full time
- Wright State University, Assistant Professor, 1977 -1981, full time

Non-academic experience:
- NSF, Program Director, 2001 – 2003, full time
- IBM, Faculty Associate, Summer 1983, full time

Certifications or professional registrations: none

Current membership in professional organizations: IEEE Computer Society, ACM

Honors and awards:

Service activities:
- Program Committee 2011 Int. Conf. On Network and Parallel Computing
- Program Committee 2010 Int. Conf. On Network and Parallel Computing
- Editor 2002-9 Journal of Information Science and Engineering
- Program Committee 2008 International Conference on GDC
- Program Committee 2007-8 IASTED/Advanced Computer Science and Technology
- Committee of Visitors 2006 CNS/CISE/NSF
- Keynote Speaker 2006 39th Annual Simulation Symposium
- Program Committee 2006 ASTED/Advanced Computer Science and Technology
- Program Committee 2004 International Symposium on ADS
- Program Chairman 2003 International Workshop on FTDCS
- Program Director 2001-3 NSF

Most important publications and presentations from the past five years:


**Most recent professional development activities:**

• Faculty Enhancement Opportunity, Summer 2009; Sabbatical, Spring 2005

• See also service activities above
Name: DOUGLAS DANKEL II

Education: Ph.D., Computer Science, University of Illinois Urbana-Champaign, 1980

Academic experience:
- Assistant Professor, University of Florida, 1979, present, full time
- Lektor, University of Akureyri, Iceland, 2003-2005, full time

Non-academic experience:
- Graduate Engineer, 5/73-8/73, IBM, Raleigh, NC, full time
- Programmer, 5/74-7/74, Northern European University Computing Center, Lyngby, Denmark, full time
- Visiting Researcher, 5/90-12/90, 5/91-8/91, 5/92-8/92, BNR, Research Triangle Park, NC, full time
- Assistant for Internal Courses, 12/80, 1/81, IBM, part time

Certifications or professional registrations: none

Current membership in professional organizations: AAAI, ACM: IEEE, FLAIRS (Florida Artificial Intelligence Research Society)

Honors and awards:
- Finalist in “The Campus Award for Excellence in Undergraduate Teaching”, Univ. of Illinois, Urbana, Illinois, 1976
- Co-Winner of “Teacher of the Year in the College of Engineering”, Univ. of Florida, Gainesville, FL, 1985

Service activities:
- Departmental committees: Curriculum Committee
- Program Chairman: The 10th International Florida Artificial Intelligence Research Symposium, May 12-14, 1997, Daytona Beach, FL
- General Co-Chairman: The 9th Florida Artificial Intelligence Research Symposium, May 20-22, 1996, Key West, FL
- General Chairman: The 14th International Artificial Intelligence Research Symposium, 2001, Key West, FL
- Vice President, Florida Artificial Intelligence Research Society, 1996 - 1997
- President, Florida Artificial Intelligence Research Society, 1997 - 2012
Most important publications and presentations from the past five years:


Most recent professional development activities:

- Workshop on Incorporating Machine Learning in an Introductory AI Course, FLAIRS-2009
- See also service activities above
Name: TIMOTHY DAVIS

Education: PhD, Electrical Engineering, University of Illinois at Urbana-Champaign, 1989

Academic experience:
- University of Florida, CISE, Professor, Aug. 2007 to date, full time
- Stanford University, Visiting Associate Professor, Aug. 2002 to May 2003, full time
- University of Florida, CISE, Associate Professor, Aug. 1996 to Aug. 2007, full time
- University of Florida, CISE Assistant Professor, Dec. 1990 to Aug. 1996, full time

Non-academic experience:
- Lawrence Berkeley National Lab, Visiting Staff Member, Aug. 2002 to May 2003
- General Electric Corporate Res. & Dev., Summer Intern, 1984 and 1985
- RCA, Cooperative Education Student Intern, 1980-1983 (2 calendar years total)
  (all the above are full-time)

Certifications or professional registrations: Engineering in Training, 1983

Current membership in professional organizations: SIAM, ACM

Honors and awards:
- Google Open Source Software Award ($5000), May 1, 2012
- American Electronics Associate Fellowship, 1984 to 1989
- CISE Department Teacher of the Year Award, 1994
- NVIDIA Academic Partner, 2011 ($25,000 gift, plus $4,000 GPU card)
- Google Open Source Software Award (Google Summer of Code), 2011

Service activities:
- Associate Editor, ACM Trans. on Mathematical Software, Sept. 2008 to date
- Associate Editor, Computational Optimization and Appl., Sept. 2005 to date
- Chair, University of Florida College of Engineering Faculty Council, 2007 – 2008
- University of Florida Faculty Senate, 2002-2008
- Elected Member of SIAM Council, 2007 to date
- SIAM Council representative to the SIAM Board of Trustees, 2010 to date
- College of Engineering Awards Committee
- NSF Panelist (multiple occasions)

Most important publications and presentations from the past five years:
- The University of Florida Sparse Matrix Collection T. A. Davis and Y. F. Hu, in ACM


Most recent professional development activities:

- SCEEE 2010 (Supercomputing in Electrical Engineering), Toulouse, France
- Invited talk at University of Michigan, Aug. 2010
- SIAM Annual Meetings
- Invited talk at Florida State University, May 2010
- Master Consultant to The MathWorks (I am the author of x=A\b in MATLAB when A is sparse), including a 500-hour assignment in the Summer of 2007.
- Consultant to Accelogic, Inc.
- Consultant to Mentor Graphics, Inc.
- Faculty Advisor for Cornerstone Robotics Team (high-school), 2010. The team won 1st place in an international competition (Technology Student Association), 2010, in Baltimore, Maryland
- Results of research (mathematical software) incorporated into dozens of commercial software packages and distributed commercially, including MATLAB, Mathematica, MSC Nastran, Cadence, ANSYS, IBM, Berkeley Design Automation (circuit simulation), Geomodeling Solutions, Orcina, ATopTech, Tandent Vision Science, SIMetrix, Mentor Graphics, and countless open-source packages such as the Xyce circuit simulation package (Sandia National Labs).
- See also service activities above
Name: ALIN DOBRA

Education: PhD, Cornell University, 2003

Academic experience:
- Department of CISE, University of Florida, Associate Professor, 2009 – present
- Department of CISE, University of Florida, Assistant Professor, 2003-2009

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: ACM

Honors and awards:
- Best paper award, SIGMOD 2007 conference
- NSF CAREER Award: New Technologies for Approximate Query Processing, 2005-2010

Service activities:
- Organizer of First Bertinoro Workshop on Data-mining (October 2005)
- Internal: chair of facilities committee (2008-now)

Most important publications and presentations from the past five years:

Most recent professional development activities:
- Developed DBO, a database system with statistical guarantees (2006-2009)
- Developed DataPath, a high-performance database system (2009-now)
Name: ALIREZA ENTEZARI

Education: PhD, Computing Science, Simon Fraser University, 2007

Academic experience:
- University of Florida, CISE, Assistant Professor, 2007 – present

Non-academic experience:
- Simon Fraser University, Research Assistant, 2001-2007
- McKesson (ALI Technologies) Medical Imaging, Research and Development, 1999-2001
- Eyeball networks, Research and Development, 1998-1999

Certifications or professional registrations: none

Current membership in professional organizations: IEEE

Honors and awards:
- Natural Sciences and Engineering Research Council (NSERC) of Canada Fellowship, ranked 6th nation-wide

Service activities:
- Facility, Graduate admission committees in the CISE department.
- Served on National Science Foundation and Department of Energy review panels
- Organized workshop on sampling and reconstruction in Banff Intl. Research Station.

Most important publications and presentations from the past five years:

Most recent professional development activities: see service activities above
Name: PAUL FISHWICK

Education: University of Pennsylvania, Computer and Information Science, PhD 1986

Academic experience:
- University of Florida, CISE, Professor, 1998 to date
- University of Florida, CISE, Associate Professor, 1991 – 1998 (tenured)
- University of Florida, CISE, Assistant Professor, 1986 – 1991 (tenure accruing)
- University of Pennsylvania, Research Assistant, 1984 – 1986 (non-tenure accruing)
- University of Pennsylvania, Teaching Assistant, 1983 – 1984 (non-tenure accruing)

Non-academic experience:

Certifications or professional registrations: none

Current membership in professional organizations: ACM, IEEE, SCS

Honors and awards:
- Best Paper Award, Annual meeting of the Broadcast Education Association. Las Vegas, Nevada.
- Florida Blue Key Society Distinguished Professor (one of four in the University), October 2008.
- SCS Distinguished Lectureship Program (one of seven), April 2006
- Association for Computing Machinery Recognition of Service Award In Appreciation for Contributions to ACM, General Chair WSC ’00, December 10-13, 2000.
- WSC Distinguished Service Award, December 2000, "For service as General Chair, Winter Simulation Conference 2000, Orlando, Florida"
- SCS Outstanding Service Award, December 2000, "For outstanding leadership effort in several conference organizations including the 2000 Winter Simulation Conference and the WEBSIM conference series"
- ACM SIGSIM Distinguished Lecturer (one of four), Dec 1998, Multimedia Presentation
- Received a Office of Naval Research Certificate of Recognition for Research Contributions made through the 1994 NAVY-ASEE Summer Faculty Research Program, June-August 1994.
- CIS Department Teacher of the Year Award, 1991.
- Awarded Senior Membership in the Institute for Electrical and Electronics Engineers (IEEE) and in the Society for Computer Simulation (SCS), 1991.
• Received a National Science Foundation Engineering Research Initiation Award, 1989.
• Received a NASA Certificate of Recognition for data base machine research, 1983

Service activities:
• Member Numerous international Conference Program Committees
• Chair of 10 workshops or conferences related to computer simulation
• Served on 12 editorial boards of archival journals

Most important publications and presentations from the past five years:
• Park, H. and P. Fishwick, An Analysis of Queuing Network Simulation Using GPU-Based Hardware Acceleration. ACM Transactions on Modeling and Computer Simulation (TOMACS). Accepted (9/14/10).
• Fishwick, P. 2010. A Decade of Digital Arts and Sciences at the University of Florida, Leonardo, MIT Press, accepted (Nov. 2010).

Most recent professional development activities: see service activities above
Name: PAUL GADER

Education: Ph.D., Applied Mathematics, University of Florida, 1986

Academic experience:
- Professor of CISE, University of Florida, 2003-present
- Associate Professor of CISE, University of Florida, 2001-2003
- Professor of Computer Eng and Computer Sci., Univ of Missouri-Columbia, 2001
- Summer Research Fellow, Image Processing Laboratory, Eglin AFB FL, 1994
- Summer Research Fellow, Institute for Mathematics and Its Applications, Summer Program on Signal Processing, University of Minnesota, 1988
- Assistant Professor of Mathematics, University of Wisconsin-Oshkosh, 1987-1988
- Visiting Assistant Professor of Mathematics, University of Wisconsin-Oshkosh, 1986

Non-academic experience:
- Consultant, Environmental Research Institute of Michigan (ERIM), 1991-1992
- Section Head and Research Eng., Image and Pattern Analysis Section, ERIM, 1989-1991

Certifications or professional registrations: none

Current membership in professional organizations: Fellow IEEE, member SPIE

Honors and awards:
- 2012 UF Research Foundation (UFRF) Professorship.
- Outstanding Junior Faculty Research Award, Presented Annually by University of Missouri-Columbia College of Engineering, March 1996.

Service activities:
- Assoc Editor, Journal of Mathematical Imaging and Vision

Most important publications and presentations from the past five years:
• Heo, P. D. Gader, and H. Frigui, “RKF-PCA: Robust kernel fuzzy PCA”, Neural Networks, Vol. 22, No. 5-6, July 2009, Page(s): 642-650.

Most recent professional development activities: see service activities above
Name: ADELSALAM ‘SUMI’ HELAL

Education: Ph.D. in Computer Sciences, Purdue University, 1991

Academic experience:
- June 2004 – present: Professor, CISE, University of Florida
- Oct 2001 - 2007: Director of Technology Development, NIDRR RERC Center on Successful Aging
- April 2000 – present: Director of the Mobile & Pervasive Computing Laboratory, University of Florida
- Teaching: Assistant Professor at the University of Texas at Arlington, 1991 – 1994.
- Teaching: Visiting Assistant Professor at Purdue University, 1994-1995.

Non-academic experience:
- Consultant to several companies including Motorola, IBM Watson Research Center, Sarnoff Corporation, DoCoMo USA, among others.

Certifications or professional registrations: none

Current membership in professional organizations: IEEE (senior member), ACM, USENIX, IEEE Computer Society

Honors and awards:
- IEEE Certificate of Appreciation for recognition of achievements in the organization of several conferences and workshops.
- Sole author of the most downloaded single article from the IEEE Pervasive Computing Digital library for the month of April 2005.

Service activities:
- Organized or organizing 22 conferences and workshops mainly sponsored by IEEE or the National Science Foundation.
- On Editorial Board of 10 major journal and magazines all in the area of mobile and pervasive computing

Most important publications and presentations from the past five years:
• E. Kim and A. Helal, “Knowledge-Assisted Activity Modeling and Recognition,” Accepted for publication (February 2012) in the International Journal of E-Health and Medical Communications (IJEHMC), to appear 2013.


• M. Thai, R. Bose, R. Tiwari, A. Helal, "Detection and Tracking of Phenomena Clouds," In revision, the ACM transactions on Sensor Networks.


Most recent professional development activities: see service activities above
Name: AHMED HELMY

Education: Ph.D., University of Southern California, 1999

Academic experience:
- Associate Professor, CISE Department, University of Florida, 2006-present
- Assistant Professor, CISE Department, University of Florida, 1999-2006
- co-Founder and co-Director, Wireless and Sensor Networks Lab, USC, 2005-2006
- Founder/Director, Computer Networks Protocol Design & Testing Lab, USC 2000-2006
- Director, Mobile Networking Laboratory, Univ of Florida

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, ACM

Honors and awards:
- National Science Foundation (NSF) CAREER Award, June 2002 – May 2008
- National Science Foundation (NSF) NETS ANET Grant/Award, Oct 2008 – Sept 2012
- National Science Foundation (NSF) NETS NOSS Grant/Award, Sept 2004 – Sept 2008.
- NSF/NASA STRESS Grant/Award, October 2002 – October 2007
- Best Paper Award, IEEE Int’l Conf on Mgmt of Multimedia Networks & Services, 2002
- Zumberge Award for Individual Research, USC, June 2000
- Award winner in ACM MobiCom WiNTECH demo competition (2nd place prize), 2010.
- Award winner in the ACM MobiCom SRC (3rd place) Sept 2007, and (4th place) 2008

Service activities:
- Communications and Networks
- Area Editor of Networking for the IEEE Computer, starting Jan 2010.
- Member of the International Advisory Board for the IEEE/ACM IWCMC (International Wireless Communications and Mobile Computing Conference), since 2010.
- Chair of the industrial advisory board (IAB) committee, CISE Dept, since Fall 2011.
- NSF panels and workshops (12 since 2001)
- Conference Chair or co-Chair:
  - ACM MobiSys Hop Planet Workshop: Co-Chair, June 2012.
  - ACM MobiCom CHANTS (Workshop on Challenged Networks): Technical program
co-Chair, Las Vegas, NV, September 2011.

- IEEE GlobeCom (Global Communications Conference): Chair and coordinator of keynote plenary panel on Sensor Networks, Miami, FL, December 2010.
- IEEE SECON (Comm. Society Conf. on Sensor, Mesh and Ad Hoc Networks)

Most important publications and presentations from the past five years:

Most recent professional development activities: see service activities above
Name: JEFFREY HO

Education: Ph.D., 1999 Mathematics, University of Illinois at Urbana-Champaign

Academic experience:
- Assistant Professor CISE, University of Florida, 2004-Present.

Non-academic experience:
- Research Associate Honda Research Institute, 2002-2003.

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, ACM.

Honors and awards:
- National Science Foundation Post-Doctoral Research Associate, 2000-2002.

Service activities:
- Reviewers for major international academic and research journals and conferences.

Most important publications and presentations from the past five years:

Most recent professional development activities: see service activities above
Name: TAMER KAHVECI

Education: Ph.D., University of California, Santa Barbara, 2004

Academic experience:
- Associate Professor, CISE Department, University of Florida, 2010-present
- Assistant Professor, CISE Department, University of Florida, 2004-2010

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: ACM, IEEE, ICSB

Honors and awards:
- NSF CAREER Award, 2009-2014
- University of Florida, Research Initiative Award, 2008
- Best paper award at the Intl Conf on Computational Systems Biology, 2008
- ORAU Powe Junior Faculty Enhancement Award, 2006
- Silver medal at International Mathematics Olympiads, 1993

Service activities:
- Editorial review board, Intl J. of Knowledge Discovery in Bioinformatics.
- Editorial Board, ISRN Bioinformatics.
- Editorial Board, Frontiers in Genomic Assay Technology
- Editorial Board, Network Modeling & Analysis in Health Informatics and Bioinformatics

Most important publications and presentations from the past five years:
- Ferhat Ay, Tamer Kahveci, Valerie de Crecy-Lagard, Consistent alignment of metabolic pathways without any abstraction in modeling, International Conference on Computational Systems Biology (CSB), 2008. (Best paper award)
- Daniel Marbach, Sushmita Roy, Ferhat Ay, Patrick E. Meyer, Rogerio Candeias, Tamer Kahveci, Christopher A. Bristow, and Manolis Kellis, Predictive regulatory models in

- Nirmalya Bandyopadhyay, Sanjay Ranka, Y. Sun, Steve Goodison, Tamer Kahveci, Pathway based Feature Selection for Cancer Microarray Data, Journal of Advances in Bioinformatics, accepted for publication.
- Jayendra Venkateswaran, Deepak Lachwani, Tamer Kahveci, Christopher Jermaine, Reference-Based Indexing for Metric Spaces with Costly Distance Measures, the VLDB Journal, 17:5, pages 1231 - 1251, 2008.
Most recent professional development activities: see service activities above
Name: JONATHAN C. L. LIU

Education: Ph.D., Computer Science and Engineering, University of Minnesota, 1996

Academic experience:
- Assoc. Professor, CISE Department, Univ of Florida, 2002 to date
- Asst. Professor, CISE Department, Univ of Florida, 1999 to 2002
- Asst. Professor, Computer Science, School of EECS, Washington State Univ, 1996-1999

Non-academic experience:
- Consultant for Honeywell Technology Center, Minnesota, 1993-1996.

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, ACM

Honors and awards:
- National Science Foundation CAREER Award, 1999-2005.
- Keynote address at The NDSU Workshop on ATM Networking, North Dakota, 1996.

Service activities:
- Area Editor, Networks and Multimedia, IEEE COMPUTER, 2001-2007ZZ

Most important publications and presentations from the past five years:
- S. Chung, E. Kim and J. Liu, "A Storage Saving Schemes to Share HD-Quality Content in Community Networks" IEEE Transactions on Consumer Electronics Vol. 54, No. 4,


Most recent professional development activities: See also service activities above

- V. Kulkarni and J. Liu, "Software-Only M-JPEG Performance for Multimedia Applications", Intl Conf on Distributed Multimedia Systems,. 2010, Oak Brook, IL
- "An Efficient MST-Based Content-Sharing Architecture for P2P/PVR Networks", IEEE Intl Symp. on Multimedia, Dec 2009, San Diego, California, USA.
``A FC/AL-Based P2P Network for Personal Archive and Sharing'', IS&T International Conference on Archiving, Bern, Switzerland, June 24-27, 2008.
Name: BENJAMIN LOK

Education: Ph.D., Computer Science, University of North Carolina at Chapel Hill, 2002

Academic experience:
- University of Florida, Associate Professor, 2009-2011, full-time
- University of Florida, Assistant Professor, 2003-2009, full-time

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, ACM

Honors and awards:
- Speaker at the National Academy of Science Kavli France-US Frontiers of Science Symposium in Roscoff, France (2008)
- IEEE Virtual Reality 2008 Best Paper
- NSF Career Award (2007)
- University of Florida Student ACM Chapter’s CISE Teacher of the Year (2005)

Service activities:
- Department of Computer and Information Sciences and Engineering Graduate Affairs Committee
- Department of Computer and Information Sciences and Engineering Curriculum Committee
- College of Engineering Technology Innovation Advisory Committee
- College of Engineering Faculty Innovation Council
- College of Engineering IT Steering Committee
- Steering Committee: IEEE Virtual Reality Conference (2009-present)
- General Chair: IEEE Virtual Reality 2013

Most important publications and presentations from the past five years:


Most recent professional development activities: see service activities above
Name: PRABHAT MISHRA

Education: Ph.D. Information and Computer Sc. University of California, Irvine, 2004

Academic experience:
- Since Aug 16, 2010: Associate Professor, CISE Department, University of Florida
- Jul 2004 – Aug 2010 Assistant Professor, CISE Department, University of Florida
- May – Dec 2006: Assistant Professor, Indian Institute of Science, Bangalore
- Apr – Jul 2004: Asst. Project Scientist, CECS, University of California, Irvine
- Jul 2000 – Mar 2004: Research Assistant, University of California, Irvine

Non-academic experience:
- Summer 2001, 2002: Research Intern, PowerPC Design Center, Motorola, Austin
- Summer 2000: Research Intern, IA-64 Perf. Group, Intel, Santa Clara

Certifications or professional registrations: none

Current membership in professional organizations: ACM, IEEE

Honors and awards:
- NSF CAREER Award, National Science Foundation, 2008-2013
- VLSI Design Best Paper Award, International Conference on VLSI Design, 2011
- International Educator of the Year, College of Engineering, Univ. of Florida, 2007
- Outstanding Dissertation Award, European Design Automation Association, 2004
- CODES+ISSS Best Paper Award, ACM/IEEE CODES+ISSS Conference, 2003

Service activities:
- Member, Shared Infrastructure Advisory Committee, UF Info. Technology, 2011–12.
- Steering Committee Member, Interdisciplinary Strategic Planning (Energy), 2009 – 2010
- Member, Graduate Admissions Committee, 2005-2011.
- Member, Scholarships and Awards Committee, 2005-2011.
- Member, Colloquium Committee, 2005-2011.

Most important publications and presentations from the past five years:


**Most recent professional development activities:** see service activities above
Name: RICHARD NEWMAN


Academic experience:
- University of Florida, CISE, Assistant Professor, 1986-present, full time

Non-academic experience:
- Intellon Corp./Atheros/QualComm, contractor, consult in powerline network protocols and security, 1999-present, part time
- Epic Tide/Fair Warning, board of directors, oversee and advise security company, 2004-present, part time
- Unistry Associates, senior scientist, consult in intrusion and anomaly detection, 1995-1999, part time

Certifications or professional registrations: none

Current membership in professional organizations: ACM, IEEE

Honors and awards:
- University Superior Accomplishment Award for Faculty Service, University of Florida, 1995-1996
- ACM Teacher of the Year, CISE Department, 1994-1995
- Distinguished Service Key, Alpha Phi Omega National Service Fraternity, 1991

Service activities:
- Reviewer, several conferences and journals
- Various departmental committees (comprehensive exams, facilities, petitions, etc.)
- Editorial board, International Journal of Network Management
- Co-chair, IEEE ISPLC 2006

Most important publications and presentations from the past five years:

**Most recent professional development activities:** see service activities above
Name: JIH-KWON PEIR

Education: Ph.D. (Computer Science), University of Illinois at Urbana-Champaign, 1986.

Academic experience:
- Associate Professor, Computer & Information Sciences Department, University of Florida, Gainesville, Florida, Jan. 1994 – Present
- Visiting Professor, Tsing-Hua University, Taiwan, 1992 – 1993.

Non-academic experience:
- Visiting Researcher, IBM Almaden Research Center, summer 1995.
- Division Director, Computer and Communication Lab, Industry Technology Research Institute, Taiwan, 1992 – 1993.

Certifications or professional registrations: none

Current membership in professional organizations:

Honors and awards:
- Outstanding Alumni Award, College of Engineering, University of Wisconsin – Milwaukee, 2010.
- NSF CAREER Award, 1996.
- IBM Invention Achievement Award, 1992.

Service activities:
- Graduate Program Coordinator, Department of Computer Information Science and Engineering, University of Florida, 2001 – present.
- Keynote Speaker, 8th Workshop on Compiler Techniques for High-Performance Computing (CTHPC2002), 2002.
Most important publications and presentations from the past five years:


Most recent professional development activities: see service activities above
Name:  JORG PETERS

Education:  1990 PhD, advisor Carl de Boor, Univ. of Wisconsin, Madison, Computer Sciences

Academic experience:
- 2004: Professor, CISE, University of Florida, Gainesville, FL
- 1997–98: Assoc. Professor, CS, tenured, Purdue University, West Lafayette, IN
- 1992–97: Asst. Professor, CS, Purdue University, West Lafayette, IN

Non-academic experience:  none

Certifications or professional registrations:  none

Current membership in professional organizations:  SIAM, ACM, IEEE

Honors and awards:
- NSF National Young Investigator 1994

Service activities:
- Associate Editor of Computer Aided Geometric Design
- Associate Editor of ACM Transactions on Graphics
- Associate Editor of APNUM (Applied Numerical Mathematics)
- Organizer Dagstuhl Seminar 2008, 2011
- Chair SIAM activity group on Geometric Design
- co-Chair SIAM GD/ACM SPM conference 2011

Most important publications and presentations from the past five years:
- Myles and J. Peters. Bi-3 C 2 Polar Subdivision ACM Transactions on Graphics, Siggraph 2009, 28(3); 2009. Generalizes subdivision of bi-3 splines to irregular configurations

Most recent professional development activities:  see service activities above
Name: ANAND RANGARAJAN

Education: Ph.D., Electrical Engineering, University of Southern California, 1991

Academic experience:
- 1984 to 1986, Teaching Assistant and Discussion Leader, University of Southern California, Los Angeles, CA.
- 1984 to 1990, Graduate Research Assistant, University of Southern California, Los Angeles, CA.
- 1990 to 1992, Postdoctoral Associate, Departments of Computer Science and Diagnostic Radiology, Yale University, New Haven, CT.
- 1992 to 1996, Associate Research Scientist, Departments of Computer Science and Diagnostic Radiology, Yale University, New Haven, CT.
- 1996 to 2000, Assistant Professor, Departments of Diagnostic Radiology and Electrical Engineering, Yale University, New Haven, CT.
- 2000 to present, Associate Professor, Department of Computer and Information Science and Engineering, University of Florida, Gainesville, FL (tenured, 2002).

Non-academic experience:
- ConCADnation, LLC, 1995-1998

Certifications or professional registrations: none

Current membership in professional organizations: IEEE

Honors and awards:

Service activities:
- Program committees IPMI, EMMCVPR
- Area chair ICCV 2007
- Associate Editor, IEEE T-PAMI 2004-2008
- Associate Editor CVIU 2007-2010.

Most important publications and presentations from the past five years:
- Rangarajan and K.S. Gurumoorthy, A Schrödinger wave equation approach to the eikonal...


**Most recent professional development activities:**

- NSF proposal, Complex data visualization using interactive multiscale machine learning (funded).
- NSF proposal, Complex wave formulations for shape analysis (funded).
- DARPA proposal, Quantum information geometry for integrated sensor representation and analytics (not funded).
- See also service activities above
Name: SANJAY RANKA

Education: 1988m Ph.D, Computer and Information Science, University of Minnesota, MN

Academic experience:
- 2002-- Professor, CISE, University of Florida, Gainesville, FL
- 1995-99 Associate Professor, CISE, University of Florida, Gainesville, FL
- 1993-95 Associate Professor, Computer Science, Syracuse University, Syracuse, NY
- 1988-93 Assistant Professor, Computer Science, Syracuse University, Syracuse, NY

Non-academic experience:
- 1999-2002 Chief Scientist and CTO, Paramark Inc., Sunnyvale, CA

Certifications or professional registrations: none

Current membership in professional organizations: ACM, IEEE, AAAS

Honors and awards:
- AAAS Fellow for “distinguished contributions to theory and practice of parallel and distributed computing”, 2006.

Service activities:
- Member, IFIP Committee on System Modeling and Optimization, 1999-present.
- Associate Editor, IEEE Transactions on Parallel and Distributed Computing, 2010-present.
- Associate Editor, Sustainable Computing: Systems and Informatics, 2010-present.
- Associate Editor, Knowledge and Information Systems, 2010-present.
- Associate Editor, International Journal of computing, 2010-present.
- Co-General Chair, International Conference on Green Computing, 2011
- Co-General Chair, International Conference on Green Computing, 2010
- Co-General Chair, International Conference on Data Mining, 2009
- Program Chair, International Conference on Contemporary Computing, 2009
- Program Chair, International Conference on Contemporary Computing, 2010
- Track Chair, ACM Conference on Information and Knowledge Management 2010

Most important publications and presentations from the past five years:
• Nirmalya Bandyopadhyay, Tamer Kahveci, Sanjay Ranka, Yijun Sun and Steve Goodison, Pathway based Feature Selection Algorithm for Cancer Microarray Data" Advances in Bioinformatics, to appear.
• Bin Song, Esra Buyuktahtakin, Tamer Kahveci and Sanjay Ranka, Manipulating the steady state of metabolic pathways, IEEE/ACM Transactions on Computational Biology and Bioinformatics, to appear.
• Manas Somaiya, Chris Jermaine and Sanjay Ranka, Learning Correlations Using the Mixture-Of-Subsets Model. ACM Transactions on Knowledge Discovery from Data, Volume 1, Number 4, 2008, pp 1-42.

Most recent professional development activities: see service activities above
Name: GERHARD RITTER

Education: PhD, Mathematics, University of Wisconsin-Madison, 1971

Academic experience:
- UF Florida Blue Key Distinguished Professor, 2004-present
- UF Interim Chair of CISE, July 2011-present
- UF Professor Emeritus, 2007-present
- UF Director, Center for Computer Vision and Visualization, 1986-2006
- UF Professor, CISE, 1986-2007
- UF Professor, Mathematics, 1986-2007
- UF Associate Professor, Mathematics, 1981-2006
- UF Assistant Professor, Mathematics, 1971-1981

Non-academic experience:

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, INNS, SPIE, MAA

Honors and awards:
- European Academy of Sciences Member (Citation for fundamental work in computer vision and pattern recognition) 2002-present.
- Fellow SPIE
- IEEE Senior Member
- General Ronald W. Yates Award for Excellence in Technology Transfer (1998)
- International Federation for Information Processing (IFIP) Silver Core Award (1989).

Service activities:
- Member, Advisory Board for Migrant Education, Putnam County, FL. 2002-present.
- Chief Editor, Computation y Sistemas, CONACYT journal, 2001-2009.
- Member Advanced Sensors Committee, Key Technologies for the 1990s, Deputy Under Secretary of Defense for Research and Advanced Technology (1989).
- Member, Advanced technology research emerging technologies advisory panel to the Deputy Under Secretary of Defense for Research and Advanced Technology (1988).
- Panel Member, Desert Storm Technology: Conversion to Peacetime Applications led by Senator Graham, 1991.
- Founding Member and first Chair, SIAM Activity Group on Imaging Science (SIAG-IS), 1998-2003.
- Chair, Session on Morphological and Fuzzy Neural Networks, FUZZ-IEEE, 2003.
• Chair, SIAM Symposia on Imaging Science, 2000.
• Co-chair, Special Session, IEEE World Congress on Computational Intelligence (WCCI), 2006.
• Program Chair, SPIE Program on Mathematical Imaging Science, 1990-present.
• Editorial Board, Journal of Pattern Analysis and Applications, 1997-present

Most important publications and presentations from the past five years:
• G.X. Ritter and G. Urcid, Perfect Recovery from Noisy Input Patterns with a Dendritic Lattice Associative Memory, Proceedings of the International Joint Conference on Neural Networks (IEEE/INNS), San Jose, CA, 2011, pp. 503-510.
• Urcid, J.C. Valdiviezo, G.X. Ritter, Lattice Associative Memories for Segmenting Color Images in Different Color Spaces, IEEE Conference on Hybrid Artificial Intelligence Systems (HAIS), San Sebastian, Spain, 2010

Most recent professional development activities: see service activities above
Name: SARTAJ SAHNI


Academic experience:
- 2001 - 2011 Chair, CISE, University of Florida
- 1998 - Present Distinguished Professor, CISE, University of Florida
- 1990 - 1998 Professor, CISE, University of Florida
- 1981 - 1990 Professor, Computer Science, University of Minnesota
- 1977 - 1981 Associate Professor, Computer Science, University of Minnesota
- 1973 - 1977 Assistant Professor, Computer Science, University of Minnesota

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: European Academy of Sciences, Fellow of ACM, IEEE, and AAAS

Honors and awards:
- Honorary Professor, Asia University, Taichung, Taiwan, 2009.
- IEEE Computer Society Technical Committee on Parallel Processing (TCPP) Outstanding Service Award, 2010.
- IEEE Computer Society Continuous Service Award, 2010

Service activities:
- Co-Editor-in-Chief, Jr of Parallel & Distributed Computing, 1992-2010.
- Member, Editorial Board, Parallel Processing Letters, 2001-.
- Editor-in-Chief, Computer and Information Science Series, Chapman & Hall/CRC, 2002-.
- Member, Editorial Board, Intl. J. of Computational Science and Engineering, 2003-.
- Member, Editorial Board, International Journal of Distributed Sensor Networks, 2004-.
- Member, Advisory Board, Intl. J. of Pervasive Computing and Communications, 2004-.
- Member, Intl. Advisory Board, Sultan Qaboos University Journal for Science, 2005-.
- Member, Editorial Advisory Board, Enterprise Info. Systems, Taylor and Francis. 2006-.
- Member, Editorial Board, Lecture Notes on ICST Activities (Inst for Computer Sciences, Social-Informatics, and Telecommunications Engineering), Springer Verlag, 2008-.
• Member, Editorial Advisory Board, Intl. J. of Contemporary Computing, 2010-.
• Steering committee member for many international conferences.
• General chair and program chair for many international conferences.

Most important publications and presentations from the past five years:
• W. Lu and S. Sahni, Low power TCAMs for very large forwarding tables. IEEE/ACM Transactions on Networking, 18, 3, 2010, 948-959.

Most recent professional development activities:
• Attended many international conferences
- See also service activities above
Name: BEVERLY SANDERS

Education: Ph.D., Applied Mathematics, Harvard University, 1985

Academic experience:
- Associate Professor, CISE Department, University of Florida, 1995-present
- California Institute of Technology, visiting Associate Professor (1994-1995, on leave from ETHZ).
- Swiss Federal Inst. of Technology (ETH Zurich), Assistant Professor, 1990-1995.
- Swiss Federal Inst. of Technology (ETH Zurich), Senior Research Associate, 1986-1990.
- University of Maryland, Assistant Professor, 1985-1986.

Non-academic experience:

Certifications or professional registrations: none

Current membership in professional organizations: ACM, IEEE, IFIP WG10.3 on Concurrent Programming

Honors and awards:

Service activities:
- Program committee for IEEE/ACM Conf on Automated Software Engineering 2010
- Many other program committees and reviewing activities less recently)

Most important publications and presentations from the past five years:
- Berna L. Massingill, Timothy G. Mattson, Beverly A. Sanders, Reengineering for Parallelism: an entry point into PLPP for legacy applications, Concurrency and Computation: Practice and Experience, Volume 19, Issue 4, Pages 369-569 (25 March 2007)


Most recent professional development activities: see service activities above
Name: MARK SCHMALZ

Education: Ph.D. Computer Science (in Image Compression) University of Florida 1996

Academic experience:
- Associate Scientist, CISE, University of Florida, 2003-present
- Assistant Scientist, CISE, University of Florida, 1998-2003
- Visiting Assistant Professor, CISE, University of Florida, 1996-1998

Non-academic experience:
- UltraHiNet, LLC, Gainesville, FL
- Research/Scientist, (parallel high-performance computing), 2008-present
- University of Florida, Gainesville, FL
- Research/Teaching Assistant Department of CISE, 1990-1996
- Research Assistant, Center for Computer Vision and Visualization 1989-1990
- Harbor Branch Oceanographic Institution Ft. Pierce, FL
- Electrical Engineer, Division of Engineering Res. and Development, 1985-1988
- Research Assistant, Department of Physical Oceanography, 1983-1985
- Applications Programmer, Department of Computer Services, 1982-1983

Certifications or professional registrations: none

Current membership in professional organizations: ACM, IEEE, SPIE, Tau Beta Pi

Honors and awards:
- Nominated for University of Florida College of Engineering Teaching/Advisement Awards, 2003
- Best Paper Award, SCI-2002 World Multiconference on Systemics, Man, and Cybernetics, Conference on Medical Imaging
- University of Florida College of Liberal Arts and Sciences Research Award, Laryngeal Imaging (Co-PI with C. Sapienza as PI), 2001

Service activities:
- Member, Electronic Data Subcommittee of the Traffic Records Coordinating Committee (TRCC), State of Florida, 2011-present
- Member, Traffic Records Electronic Dataset Subcommittee (TREDS) of the Traffic Records Coordinating Committee (TRCC), State of Florida, 2009-2010
- Member, Technical Organizing Committee, Optical Engineering and Applications Program, SPIE Optics and Photonics Symposium, San Diego, CA, August 2009
- Member, Executive Committee, Optical Engineering and Applications Program, SPIE Optics and Photonics Symposium, San Diego, CA, August 2007
• Member, Technical Organizing Committee, Optical Engineering and Applications Program, SPIE Optics and Photonics Symposium, San Diego, CA, August 2007
• Conference Co-Chair, Mathematics of Data/Image Pattern Recognition, Compression, and Encryption XIV, with Applications, to be held in San Diego, CA, August 2011
• Conference Co-Chair, Mathematics of Data/Image Pattern Recognition, Compression, and Encryption IX – XIII, with Applications, San Diego, CA, August 2006 – 2010

Most important publications and presentations from the past five years:

Most recent professional development activities:
• Numerous invited talks, conferences, over $5.5 million in grants or contracts (PI or Co-PI) over past 10 years
• See also service activities above
Name: MARKUS SCHNEIDER

Education: Doctoral degree (Dr. rer. nat., Ph.D.) in Computer Science, Fern Universität Hagen, Hagen, Germany, 1995

Academic experience:
- Department of Computer and Information Science and Engineering (CISE), University of Florida, Gainesville, Florida, USA; Associate Professor; since August 2008; full time
- Department of Computer and Information Science and Engineering (CISE), University of Florida, Gainesville, Florida, USA; Assistant Professor; January 2002–August 2008; full time
- Department of Computer Science, FernUniversität Hagen, Hagen, Germany; University assistant (“Hochschulassistent (C1)“); June 1996–December 2001; full time
- Department of Computer Science, FernUniversität Hagen, Hagen, Germany; Scientific associate (“wissenschaftlicher Mitarbeiter”); July 1991–June 1996; full time

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: ACM, ACM SIGSPATIAL

Honors and awards:
- NSF CAREER award

Service activities:
- Curriculum committee
- Graduate admissions committee
- Chair and member of PhD committees
- Chair and member of Master thesis committees
- Co-founder of ACM SIGSPATIAL
- Treasurer of ACM SIGSPATIAL from 2008 to 2011
- Reviewer for many journals, conferences, and workshops
- Workshop Chair of the 19th ACM SIGSPATIAL Int. Conf. on Advances in Geographic Information Systems (ACM SIGSPATIAL GIS) 2011
- Dagstuhl Seminar titled Data Warehousing: from Occasional OLAP to Real-time Business Intelligence

Most important publications and presentations from the past five years:

**Most recent professional development activities:** see service activities above
Name: MEERA SITHARAM

Education: 1990 PhD in CS, University of Wisconsin-Madison

Academic experience:
- 1998– Associate Professor of CISE, University of Florida (UF Tenure: 2002)
- 1997-1998 Visiting Associate Professor of CS, Purdue University
- 1996-1999 Assoc Professor of Math and CS, Kent State University(KSU Tenure: 1996)
- 1991-1995 Assistant Professor of Math and CS, Kent State University
- 1990–1991 A. v. Humboldt postdoc fellowship, University of Bonn, Germany

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations:

Honors and awards:

Service activities:
- International Conference Program Committees:
- ACM SAC, Geometric constraints and reasoning, on PC annually since 2006.
- ADG Automated deduction in geometry, every two years, on PC since 2004.
- Association for Symbolic Annual meeting, 2007, Gainesville (Organizing committee)
- IASTED-NANA (Nanoscience and technology), 2009.

Most important publications and presentations from the past five years:
• M. B’ona, M. Sitharam “Icosahedral symmetry analysis of virus assembly pathways,” Computational and Mathematical methods in medicine, special issue on Mathematical Virology (Stockley and Twarock special Ed.s) 2009.

Most recent professional development activities:
• Running a local elementary school math circle for 4 years with help from Tau Beta Pi and other UF student organizations; Member of UF-Gainesville committee on mathematical circles in local schools.
• As faculty advisor of Gators for Asha, exploring P2P social entrepreneurship: partnering with grassroots nonprofits locally and in India applying information and computation principles to engineer social movements by designing protocols for collaborative, distributed, participatory decision making; building transparent and efficient P2P economies (including manufacturing and finance) and sustainable community resource base.
• As faculty advisor of Spicmacay, exploring global P2P musicmaking. Honors and Awards in the past 5 years (1) 1 NSF grant, 1 joint NSF-NIGMS grant, one gift from SolidWorks Corp., and 1 UF seed grant. (2) 1 Keynote talk at (International conference on applied computing) ICAC in 2009;
• At least 2 invited addresses talks every year.
• See also service activities above
Name: MY THAI

Education: Ph.D. in Computer Science, University of Minnesota, Sep 2001 – Dec 2005

Academic experience:
- Associate Professor, CISE Department, University of Florida, 2011-present
- Assistant Professor, CISE Department, University of Florida, Aug 2006-2011

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, ACM

Honors and awards:
- UF Provost’s Excellence Awards for Assistant Professor, 2011
- National Science Foundation (NSF) CAREER Award, 2010-2015
- DTRA Young Investigator Award, 2009-2011
- Outstanding Service Award, Int Conf on Bioinformatics and Computational Biology (BIOCOMP), 2007

Service activities:
- Book Series Editor: Springer Briefs in Optimization.
- Associate Editor: Journal of Combinatorial Optimization, Optimization Letters, Ad Hoc and Wireless Sensor Networks
- Guest Editor: Algorithmica, Ad Hoc Networks, Discrete Mathematics, Algorithms and Applications
- Conference and PC co-Chairs: SIMPLEX 11, DIS 11, COCOON 10
- Scholarship and Awards Committee, 2007-2011
- CISE Ambassador, 2006-2008
- Judge for the UF Engineering and Science Fair, 2006-2007

Most important publications and presentations from the past five years:
- Y. Xuan, Y. Shen, N. P. Nguyen, and M. T. Thai, A Trigger Identification Service of


**Most recent professional development activities:** see service activities above
Name: STEVE THEBAUT

Education: Ph.D., Computer Science, Purdue University, 1983

Academic experience:
- University of Florida, CISE Dept., Associate Chairman, 2000 – Present, full time
- University of Florida, CISE Dept., Undergraduate Coordinator, 2001 – Present
- University of Florida, CISE Dept., Site Director, Software Engineering Research Center (SERC), an NSF Industry/University Cooperative Research Center, 1994 – 2000
- Hong Kong University of Science and Technology, Department of Computer Science, Visiting Scholar, 1991-1993
- University of Florida, CISE Dept., Assistant Professor, 1983 – Present

Non-academic experience:

Certifications or professional registrations: none

Current membership in professional organizations: ASEE

Honors and awards:
- Nominated as Outstanding UF EDGE Faculty for the Academic year 2005-2006
- IBM Post-Doctoral Research Fellowship, University of Florida, 1983-84.
- Maurice H. Halstead Award for Research Excellence in Software Engineering, Purdue University, 1982

Service activities:
- Chair, CISE Undergraduate Petitions Committee
- Member, Systems Facilities Committee and Commencement Representatives
- Faculty Adviser to Association of Graduate Students in CISE (ASCIE)
- College of Engineering EDGE Class Scheduling Committee
- Academic Advisor, National Technological University (NTU), Software Engineering Masters Program, 1995-2004
- Advisory Board Member, Sino Software Research Center (SSRC), Hong Kong University of Science and Technology, 1992-1995
- Program Comm Member, IASTED International Conference on Advances in Computer Science and Technology (ACST), 2004, 2005, 2006
- Program Comm Member, Fourth International Conference on Computer Science, Software Engineering, Information Tech., e-Business and Applications (CSITeA’04), 2004
- Program Comm Member, 9th Conference on Software Engineering Education, 1995
• Technical Advisor: DoD Software Technology for Adaptable, Reliable Systems (STARS) program, 1988-89
• Invited Lecturer: Software Engineering Institute (SEI), Carnegie Mellon University, ‘‘Software Project Management,’’ nationally distributed video continuing ed. course, 1989
• Referee: IEEE Transactions on Software Engineering, International Journal of Computer and Information Sciences, Software-Practice & Experience, and several others

Most important publications and presentations from the past five years:
• ‘‘An Intelligent Plan Selection Model for BDI Frameworks,’’ (with Umut Sargut), European Workshop on Multi-Agent Systems (EUMAS'07), Hammamet, Tunisia, December 2007.
• ‘‘Software Engineering Foundations,’’ a series of technical lectures delivered to the faculty and staff of the Infosys Education and Research Department, Mysore, India, July-August, 2009.
• Invited seminar: ‘‘Three Talks for the Price of One: Making Meetings Work, Reviews and Inspections, Test Plans and Planning,’’ IPPD Guest Lecture, Fall 2010, UF College of Engineering, University of Florida.

Most recent professional development activities:
• Participated in 2010 NSF ADVANCE Workshop on Recruitment and Mentoring in Engineering and Physics
• See also service activities above
Name: ALPER UNGOR

Education: Ph.D., University of Illinois at Urbana-Champaign, 2002

Academic experience:
- Associate Professor, CISE Department, University of Florida, 2010-present
- Assistant Professor, CISE Department, University of Florida, 2004-2010

Non-academic experience:
- Sandia National Labs, Visiting Researcher, Summer 1999

Certifications or professional registrations: none

Current membership in professional organizations: ACM, SIAM

Honors and awards:
- CAREER Award, National Science Foundation, 2009-2014
- David J. Kuck Best Ph.D. Thesis Award, UIUC
- Best Paper award (among over 360 papers submitted to) ISCIS, 2003
- Computational Science and Engineering Fellowship, UIUC, 2002
- C.L. Dave and Jane W. -S. Liu Award, UIUC, 2001
- Excellence in Teaching Award, University of Illinois at Urbana-Champaign, 1998.

Service activities:
- Program Committee Member for International Sym. on Voronoi Diagrams, June 2009.
- Program Committee Member for Sym. on Geometric Processing, July 2007
- Program Committee Member for ACM Symp on Computational Geometry, June 2006.
- Chair of the International Meshing Roundtable (IMR), Oct 2004
- Organizer and Chair of the Workshop on Meshing for Computational Biology, IMR 2004
- Program Committee Member for Intl Symp on Computer and Information Sciences (ISCIS), 2004
- Program Committee Member for the International Meshing Roundtable (IMR), Oct 2003.
- Program Committee Member for Intl Symp on Computer and Information Sciences (ISCIS), 2003.

Most important publications and presentations from the past five years:
- H. Erten and A. Ungor, Computing Triangulations with No Small No Large Angles,
International Symposium on Voronoi Diagrams (ISVD) 2009.


- H. Erten and A. Ungor, Triangulations with locally optimal Steiner points, Symposium on Geometry Processing (SGP) 2007: 143-152.


**Most recent professional development activities:** attended and spoke at multiple international conferences; see also service activities above.
Name: BABA VEMURI

Education: PhD, Electrical & Computer Engineering, University of Texas at Austin, 1987

Academic experience:
- 2010- To date: Professor and Director, Laboratory for Computer Vision, Graphics & Medical Imaging, affiliate appointment in ECE and BME Departments.
- 2003-2010: Professor and Director of the Center for Vision, Graphics and Medical Imaging (CVGMI), Dept of CISE and BME, affiliate appt. Dept of ECE, Univ Florida
- 2003-2006: University Research Foundation (UFRF) Prof., Dept of CISE, BME, ECE.
- 1999-2003: Professor, Department of CISE, BME and ECE.
- 1992-1999: Associate Professor, Department of CISE and ECE.
- 1987-1992: Assistant Professor, Department of CISE.

Non-academic experience:
- Summer 1989: IBM TJ Watson Research Center, NY.
- Summer 1992: IBM TJ Watson Research Center, NY.

Certifications or professional registrations: none

Current membership in professional organizations: IEEE, ACM, MICCAI

Honors and awards:
- ACM Fellow 2009
- IEEE Fellow 2001
- Plenary speaker at several conferences
- Chartered member of the Neuro-technology study section at NIH
- Member external advisory board Neuro-imaging Center, Brigham and Womens Hospital, Harvard Medical School
- UFRF Professorship 2003-06
- UF SPP award 2007.

Service activities:
- Chair, faculty search and screening 2001-2010.
- Member, Departmental steering committee 2001- to date.
- Member, Departmental Tenure and Promotion committee – 2008-2010
- Chair, Departmental Tenure and Promotion committee 2011.
- Chair, Departmental Tenure and Promotion committee 2011.
- Associate Editor, Journal of Medical Image Analysis, Elsevier Publishing, 2000- to date.
- Associate Editor, J of Computer Vision and Image Understanding,, 2000- to date.
- Area Chair, IEEE Int. Conference on Computer Vision (ICCV) 2011, Barcelona, Spain.
- Area Chair, Intl. Conf. on Info. Processing in Medical Imaging, 2011, Germany.
- Area Chair, IEEE Conf on Computer Vision and Pattern Recognition, 2008, Alaska, USA.
- Area Chair, Intl. Conf. on Medical Image Comput. and CAI, 2008, NYC, USA.
- Program Chair, IEEE Intl. Conf. on Computer Vision 2007, Rio de Janeiro Brazil.
Most important publications and presentations from the past five years:

Most recent professional development activities:
- See service activities above
Name: ‘DAISY’ ZHE WANG

Education: Ph.D. Electrical Eng. and Computer Sciences, Dec., 2011 UC Berkeley

Academic experience:
- Fall 2011 - Assistant Professor, CISE Department, University of Florida
- Fall 2005-Summer 2011 Ph.D. candidate, Research Assistant, Database Group, UC Berkeley
- Summer 2003-Spring 2005 Undergraduate Researcher, University of Toronto

Non-academic experience:
- Summer 2008 Research Internship, Yahoo! Research. Santa Clara
- Spring 2008 – Spring 2010 Research Collaborator, IBM Almaden Research Center
- Summer 2007 Software Engineer, Google Inc.
- Summer 2006 Research Internship, Intel Research Berkeley
- Summer 2003, 2004, 2005 Software Developer, DB2 UDB Compiler Group, IBM Toronto Lab

Certifications or professional registrations: none

Current membership in professional organizations: ACM, IEEE

Honors and awards:
- UC Berkeley, Department of Electrical Engineering and Computer Sciences Stonebraker/Wong Fellowship 2009,
- UCC Berkeley, Dept of EECS, Departmental Fellowship 2005

Service activities:
- Reviewer: ICDE2011, TKDE2011
- External Conference Reviewer: VLDB, SIGMOD, ICDE

Most important publications and presentations from the past five years:
• “Hybrid In-Database Inference for Declarative Information Extraction” VLDB Conference, June 15, 2011
• “Selectivity Estimation for Extraction Operators over Text Data” ICDE Conference, April 14, 2011
• “Querying Probabilistic Information Extraction” EMC/Greenplum Seminar, July 11, 2011
• CSAIL Seminar, MIT, November 17, 2010.
• Database Seminar, University of Toronto, January 5, 2010.

**Most recent professional development activities:** see service activities above
Name: JOSEPH WILSON

Education: Ph.D., Computer Science, University of Virginia, 1985
          M.S., Applied Math and Computer Science, University of Virginia, 1980
          B.S., Applied Math and Computer Science, Florida State University, 1977

Academic experience:
- Assistant Professor, CISE Department, University of Florida, 1985-present
- Associate Chair, CISE, 1994-2001

Non-academic experience:
  Helped develop forms-oriented graphical database user interfaces in Cobol
- Software Engineer Intermetrics 1980
  Maintained DARPA DEC Twenex B-Ada compiler parser and performed compiler
testing using the ACVC

Certifications or professional registrations: none

Current membership in professional organizations:
- Member IEEE
- Member IEEE Data Fusion Technical Committee
- Member ACM

Honors and awards:
- 2001-2002 ACM Professor of the Year (UF Student Chapter award)
- 1994, University of Florida Teaching Incentive Program Award Winner

Service activities:
- IEEE IGARSS TPC Member 2010
- University of Florida Faculty Senator

Most important publications and presentations from the past five years:
- Nicholas I. Rummelt and J.N. Wilson, “Array set addressing: an enabling technology for
  the efficient processing of hexagonally sampled imagery,” Journal of Electronic Imaging,
  20, 03 June 2011.
- R. Mazhar, P.D. Gader, J.N. Wilson, Matching-pursuits dissimilarity measure for shape-
  based comparison and classification of high-dimensional data, IEEE Transactions on
- G. Ramachandran, P.D. Gader, and J.N. Wilson, GRANMA: gradient angle model
  algorithm on wideband EMI data for landmine detection, IEEE Geoscience and Remote
  evaluation of several fusion algorithms for anti-tank landmine detection and


**Most recent professional development activities:**

- *HMDS Historical Perspective*, invited presentation, L-3 Cyterra Corporation, 8 August, 2010.
Name: YE XIA

Education: PhD, University of California, Berkeley, EECS, 2003.

Academic experience:
- Associate professor, University of Florida, Computer and Information Science and Engineering Department. Aug. 2009 – Present

Non-academic experience:

Certifications or professional registrations: none

Current membership in professional organizations: IEEE

Honors and awards:

Service activities:
- Member of the Technical Program Committee for the following conferences.
- Fifth International Workshop on Systems and Networks (SSN 2009). 2009.
- The First IEEE Int. Workshop on Info. and Data Assurance (IPCCC WIDA’08). 2008
- International Workshop on Wireless and Sensor Networks (WSNS 2008)
- The First International Workshop on Wireless Security and Privacy (WiSP 2008)
- Fourth International Workshop on Systems and Networks (SSN 2008)
- IEEE Wireless Communications and Networking Conference (WCNC 2007).
- Third International Workshop on Systems and Networks (SSN 2007)
- Third ACIS Intl. Workshop on Self-Assembling Wireless Networks (SAWN 2007)
- Third International Workshop on Wireless and Sensor Networks (WSNS 2007)

Most important publications and presentations from the past five years:


Most recent professional development activities: see service activities above
Name: TUBA YAVUZ-KAHVECI

Education: Ph.D., Computer Science, University of California, Santa Barbara Summer 2004

Academic experience:
- Lecturer, Dept of Computer and Info. Science and Eng, Univ Florida, 2004 to date.

Non-academic experience:
- Summer Internship, Texas Instruments, Santa Barbara, Summer 2001.

Certifications or professional registrations: none

Current membership in professional organizations: none

Honors and awards:
- NSF Educators Travel Grant
- Microsoft Travel Grant

Service activities:
- Program Committee Member for ICSoft.

Most important publications and presentations from the past five years:

Most recent professional development activities: see service activities above
Name: RONG ZHANG

Education: Ph.D, Computer Science, Rutgers - the State University of New Jersey, 2006

Academic experience:
  • Lecturer, Dept of Computer and Info. Science and Eng, Univ Florida, 2006 to date.

Non-academic experience: none

Certifications or professional registrations: none

Current membership in professional organizations: none

Honors and awards:

Service activities:
  • Assistant Undergraduate Coordinator in the Undergraduate Petitions Committee of CISE dept
  • Commencement Representatives of CISE dept
  • Student Exit Interview Coordinator of CISE dept

Most important publications and presentations from the past five years:

Most recent professional development activities:
1. Name – **DAVID P. ARNOLD**

2. Education – PhD, EE, Georgia Institute of Technology, 2004; MS, EE, University of Florida, 2001; BSEE, University of Florida, 1999; BSCEN, University of Florida, 1999

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2010-date, full time; Material Science & Engineering, University of Florida, 2010-date, courtesy appointment; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2005-2010, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers; Eta Kappa Nu; American Association of Engineering Educators

7. Honors and awards – DARPA Young Faculty Award; Presidential Early Career Award for Scientists and Engineers; Southeastern Center for Electrical Engineering Education Young Investigator Grant; ECE Student-Selected Teacher of the Year, Runner-Up; Best Paper, AIAA Aerodynamic Measurement Technical Committee; UF ECE Electric E Award

8. Service activities (within and outside of the institution) – Advisor, HKN Student Chapter; Numerous conference program committees; Journal reviewer; Local Science Fair Judge; Volunteer, Make-A-Wish Foundation


1. Name – A. ANTONIO ARROYO

2. Education – PhD, EE, University of Florida, 1981; MS, EE, University of Florida, 1973; BSEE, EE, University of Florida, 1970

3. Academic experience – Department of Electrical Engineering, University of Florida, Associate Professor, 1985-date, full time; Department of Electrical Engineering, Auburn University, Assistant Professor, 1982-1985, full time; Department of Electrical Engineering, University of Florida, Visiting Professor, 1981-1982, full time; Department of Computer and Information Sciences, University of Florida, Adjunct Professor, 1977-1979, full and part-time.


5. Certifications or professional registrations – State of Florida Professional Engineer License #19957

6. Current membership in professional organizations – American Association for Artificial Intelligence (AAAI); American Society for Engineering Education (ASEE); Institute of Electrical and Electronic Engineers (IEEE); Association for Computing Machinery (ACM); Sigma Xi; Eta Kappa Nu (HKN); Sigma Tau Engineering Honorary Fraternity


9. Briefly list the most important publications and presentations from the past five years –

10. Briefly list the most recent professional development activities – *Cigr International Conference Of Agricultural Engineering*, Xxxvii Congresso Brasileiro De Engenharia Agrícola – Conbea 2008 Brazil, August 31 to September 4, 2008; *2008 Florida Conference on Recent Advances in Robotics*, FIT, Melbourne, Florida, May 8-9, 2008
1. Name – **RIZWAN BASHIRULLAH**

2. Education – PhD, EE, North Carolina State University, 2004; MS, EE, North Carolina State University, 1999; BSEE, University of Central Florida, 1997

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2010-date, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2005-2010, full time

4. Non-academic experience – Chief Science Officer eTect LLC, Co-founder, Biomedical start-up developing innovative in-body communication technologies for medication adherence monitoring, part time; Post-doctoral Research Associate, Department of Electrical & Computer Eng., North Carolina State University, 2004, full time

5. Certifications or professional registrations – None


7. Honors and awards – 2006 - National Science Foundation Faculty Early Career Development Award; 2005 - Who's Who in Engineering Academia, National Honor Society; Member of Golden Key and Phi Kappa Phi, National Honor Society; Best Session Paper Award: (J. Chen, R. Bashirullah, “A 50mW 2.5GS/s 5-Bit ADC in 0.13μm CMOS with a Novel Background Calibration Scheme,” Techcon, Sept 10, 2010); Inventor Recognition Award, University of Florida 03/2010; The Museum of Modern Art, New York (MoMA) exhibition, Swallow-Signaling Pill, 2011

8. Service activities (within and outside of the institution) – Electronics Division Area Chair, 08/2010-date; Faculty Search Committee, 2009-date; ECE Graduate Admissions and Aid Committee, 2004-2009; ABET Departmental Committee, 2005-date; Associate Editor, Transactions of Biomedical Engineering, 2005-2009; Conference Co-Chair, IEEE Topical Conference on Biomedical Wireless Technologies, Networks & Sensing Systems (BioWireleSS), Jan 2011; Technical Program Committee Member, IEEE International Symposium on Low Power Electronic and Design (ISLPED) - mixed-signal track, 2010; Organizing Session Track Chair, Biomedical Applications of Microwave Systems, IEEE Radio and Wireless Symposium, 2010; Technical Program Committee Member, IEEE International Symposium on Quality Electronic Design, Package - IC Interaction & Co-Design (PDI), 2009; Technical Program Committee Member, IEEE International Symposium on Low Power Electronic and Design (ISLPED) - mixed-signal track, 2009; Technical Program Committee Member, IEEE International Symposium on Quality Electronic Design, Package - IC Interaction & Co-Design (PDI), 2008; Technical Program Committee Member, IEEE International Symposium on Circuits and Systems (ISCAS) 2008; Technical Program Committee Member, IEEE International Symposium on Circuits and Systems - Biomedical Signal & Image Processing, 2007; Technical Program Committee Member, IEEE International Symposium on Quality Electronic

9. Briefly list the most important publications and presentations from the past five years – R. Bashirullah, “Wireless Biomedical Implants,” IEEE Microwave Magazine, Dec 2010

J. Chen, R. Bashirullah, “A 50mW 2.5GS/s 5-Bit ADC in 0.13μm CMOS with a Novel Background Calibration Scheme,” Techcon, Sept 10, 2010. (Best Session Paper Award)


10. Briefly list the most recent professional development activities – Attended various conferences and symposiums
1. Name – GIJS BOSMAN

2. Education – PhD, Physics, University of Utrecht, 1981; MS, Physics, University of Utrecht, 1976; BS, Physics, University of Utrecht, 1971

3. Academic experience – Professor and Graduate Coordinator, Electrical and Computer Engineering, University of Florida, 2005-date, full time; Professor, Electrical and Computer Engineering Department, University of Florida, 2003-2005, full time; Professor and Associate Chair for Student Affairs, Electrical and Computer Engineering Department, University of Florida, 1997-2003, full time; Professor and Graduate Coordinator, Electrical and Computer Engineering, University of Florida, 1990-1997, full time; Associate Professor, Electrical Engineering Department, University of Florida, 1986-1990, full time; Assistant Professor, Electrical Engineering Department, University of Florida, 1984-1986, full time; Visiting Assistant Professor, Electrical Engineering Department, University of Florida, 1981-1984, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers, American Physical Society, Dutch Physical Society

7. Honors and awards – 2006-Eta Kappa Nu, ECE Teacher of the Year; 1999-College of Engineering Teacher of the Year Award for 1998-1999; 1998 - University of Florida TIP Award for Outstanding Teaching; 1994-University of Florida TIP Award for Outstanding Teaching; 1994-Departmental Award for Outstanding Teacher of an EE Core Course; 1989-Departmental Award for Supervising the Outstanding Ph.D. Dissertation of 1988; 1987-Departmental Award for Supervising the Outstanding Ph.D. Dissertation of 1986; 1986-Departmental Award for Outstanding Teacher of an EE Core Course; 1985-Departmental Award for Supervising the Outstanding Ph.D. Dissertation of 1985; 1984-Tau Beta Pi Award for Excellence in Undergraduate Teaching

8. Service activities (within and outside of the institution) – Member of the Senate (1993-2000 and 2009-date); Served as chair and member of the following departmental committees; Device Division, ECE Curriculum Committee, Graduate Committee, Faculty Development Committee, Device and Physical Electronics Area Committee, Peer Review Committee, Faculty Search Committees; Member of the Scientific Organizing Committee of the Unsolved Problems of Noise (UPON) V Conference held in Lyon, France in June 2008; Member of the Scientific Organizing Committee of the Nineteenth International Conference on Noise and Fluctuations to be held in Tokyo, Japan, September 2007; Member of the Scientific Organizing Committee of the Eighteenth International Conference on Noise and Fluctuations held in Salamanca, Spain, September 2005; Leading opponent at the Ph.D. Exam of Jay S. Kolhatkar at the Technical University of Twente, The Netherlands, 2005; Member of the Scientific Organizing Committee of the Unsolved Problems of Noise (UPON) IV Conference held in Lecce, Italy in June 2005;
Member of the Scientific Organizing Committee of Optics East, Boston 2005; State and County Science Fair Judge (2005, 2004, 2003, and 2002); Reviewer for all major journals in the field of Applied and Device Physics; Reviewer of proposals for all major funding agencies

9. Briefly list the most important publications and presentations from the past five years–


10. Briefly list the most recent professional development activities – Attended annual International Conference on Noise and Fluctuations
1. Name – WILLIAM R. EISENSTADT

2. Education – PhD, EE, Stanford University, 1986; MS, EE, Stanford University, 1981; BSEE, EE, Stanford University, 1979


4. Non-academic experience – Engineering Aide, Hewlett-Packard Research Labs, Evaluated the TECAP measurement system for the characterization of GHz bipolar transistors, Summer 1979, full time; Engineering Aide, NASA Ames Research Center, Analog circuit design, Summer 1978, full time

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Automatic RF Test Group, ARFTG; IEEE Senior Member; Round Table Group; Semiconductor Test Consortium, STC; Technology Test Technical Council, TTTC

7. Honors and awards – 2009-Three SRC Inventor Recognition Awards; 2005 - SRC Inventor Recognition Award; 1996-AT&T Paradyne Spot Light Award


10. Briefly list the most recent professional development activities – NSF SBIR Phase I Workshop, Arlington VA. September 20-22, 2010; annual IEEE Wireless and Microwave Technology (WAMI) Conference; IEEE International Symposium on Circuits and Systems; Workshop on Test of Wireless Circuits and Systems
1. **Name** – **YUGUANG “MICHAEL” FANG**

2. **Education** – PhD, EECS, Boston University, 1997; PhD, EECS, Case Western Reserve University, 1994; MS/BS, Math, Qufu Normal University, 1987

3. **Academic experience** – Electrical & Computer Engineering, University of Florida, Professor, 2005-date, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 2003-2005, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2000-2003, full time; Electrical & Computer Engineering, New Jersey Institute of Technology, Assistant Professor, 1998-2000, full time; Teaching and Research Faculty, Department of Mathematics & The Institute of Automation, Qufu Normal University, Shandong, PRC, 1987–1988, full time

4. **Non-academic experience** – None

5. **Certifications or professional registrations** – None

6. **Current membership in professional organizations** – Institute of Electrical and Electronic Engineers Fellow; Association of Computing Machinery

7. **Honors and awards** – 2011 UF Distinguished Faculty; 2009-College of Engineering Faculty Mentoring Award; 2008-IEEE Fellow—“For contributions to wireless networks and mobile computing systems”; 2006 - University of Florida Research Foundation Professor; 2002-Office of Naval Research Young Investigator Award; 2002-IEEE TCGN Best Paper Award; 2001-National Science Foundation Faculty Early Career Development Award; 2001-Chinese Association for Science & Technology Academic Excellence Award; 2000-IEEE Appreciation Award

8. **Service activities (within and outside of the institution)** – Faculty Development Committee, Department of Electrical and Computer Engineering, University of Florida, August 2007–Present; Advisory Committee for the Department Chair, Department of Electrical and Computer Engineering, University of Florida, August 2003–2009; College of Engineering Tenure and Promotion Board, Fall 2005-Summer 2008; Graduate Admission and Aid Committee, Department of Electrical and Computer Engineering, University of Florida, Fall 2003–Spring 2007; Faculty Search Committee, Department of Electrical and Computer Engineering, University of Florida, 2001–2004; Editor-in-Chief, *IEEE Wireless Communications*, January 2009–present; Editor for Wireless Networks, *IEEE Transactions on Communications*, May 2000–present; Editor for *ACM Wireless Networks (WINET)*, April 2001–present; Editor for *Journal of Computer Science and Technology (JCST)*, November 2006–Present; Member of Steering Committee, *IEEE Transactions on Mobile Computing*, January 2008–present; Advisory Board, *Journal of Communications (JCM)*, Academy Publisher, Oulu, Finland, November 2008-date; Served on the Faculty Search Committee, Department of Electrical and Computer Engineering at New Jersey Institute of Technology, 1998–2000
9. Briefly list the most important publications and presentations from the past five years –


1. Name – RENATO FIGUEIREDO

2. Education – PhD, CEN, Purdue University, 2001; MS, EE, University of Campinas, 1995; BSEE, University of Campinas, 1994

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2008-date, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2002-2008, full time; Electrical & Computer Engineering, Northwestern University, Assistant Professor, 2001-2002, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronics Engineers; Association for Computing Machinery


10. Briefly list the most recent professional development activities – Attendance and presentation in recent international conferences: 7th IEEE International Conference on Autonomic Computing (June 2010), 19th ACM International Symposium on High-Performance Distributed Computing (June 2010), 23rd IEEE/ACM International Conference for High Performance Computing, Networking, Storage and Analysis (November 2010).
1. Name – JOSE A. B. FORTES

2. Education – PhD, EE, University of Southern California, 1984; MS, EE, Colorado State University, 1981; BSEE, Universidade de Angola, 1978

3. Academic experience – Electrical & Computer Engineering, University of Florida, Professor and BellSouth Eminent Scholar, 2001-date, full time; Electrical & Computer Engineering, Purdue University, Professor and Assistant Head for Education, 1999-2001, full time; Electrical & Computer Engineering, Professor, 1995-1999, Purdue University, full time; Arquitectura de Computadores, Universitat Politecnica de Catalunya, Visiting Professor, 1993-1995, full time; Electrical Engineering, Purdue University, Associate Professor, 1989-1993, full time; Electrical Engineering, Purdue University, Assistant Professor, 1987-1989, full time; Electrical Engineering, Purdue University, Visiting Assistant Professor, 1984-1987, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – International Electrical and Electronics Engineers; Association for Computing Machinery; American Association for the Advancement of Science: American Association for Artificial Intelligence: Digital Government Society of North America


8. Service activities (within and outside of the institution) – 2006–present, High Performance Computing Committee Member, Department of Electrical and Computer Engineering at the University of Florida; 2008–Present Member of the Editorial Board: Book series on Autonomc Systems, Birkhauser Publishing; 2004-Present Member of the Editorial Board: ACM Journal on Emerging Technologies in Computer Systems; 1998-Present Member of Editorial Board: Cluster Computing: The Journal of Networks, Software Tools and Applications; 1993-Present Member of Editorial Board: International Journal of Parallel Programming; 1990-Present Member of Editorial Board: Journal of VLSI Signal Processing

9. Briefly list the most important publications and presentations from the past five years – “Reliability evaluation of logic circuits using probabilistic gate models,” Jie Han, Hao Chen, Erin Boykin, J. A. B. Fortes, Microelectronics Reliability. September 2010

“Architecting Reliable Multi-core Network-on-Chip for Small Scale Processing Technology,” X. Fu, T. Li, J. A. B. Fortes, The 40th Annual IEEE/IFIP International Conference on Dependable Systems and Networks


“Decoupling Quality of Service, Resource Management and Connectivity in Clouds and Data Centers,” The 8th International Symposium on Parallel and Distributed Computing (ISPDC ‘09), Lisbon, Portugal, July 1-3, 2009


1. Name – **ROBERT M. FOX**

2. Education – PhD, EE, Auburn University, 1986; MS, EE, Auburn University, 1981; BS, Physics, University of Notre Dame, 1972

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Chair and Associate Professor, 2003-date, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 1992-2003, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 1986-1992, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Phi Kappa Phi, Eta Kappa Nu, Institute of Electrical & Electronic Engineers (Senior Member); Electron Devices Society; Circuits and Systems Society; Solid-State Circuits Society

7. Honors and awards – 2005-UF EDGE Outstanding Faculty Award; 2005-SRC Inventor Recognition Award; 1996-HKN Teacher Appreciation Award; 1995-UF Teaching Improvement Program (TIP) Award

8. Service activities (within and outside of the institution) – UF Career Resource Center Advisory Committee Member; College of Engineering (COE) Distance Education Advisory Committee Member, COE Curriculum Committee Member; COE Honors and Awards Committee Member. ECE Dept. Curriculum Committee Member; ECE Petitions Committee Member; IEEE Bipolar/BiCMOS Circuits and Technology chairman 2002-2004, 2002 Short-Course Chairman; IEEE Journal of Solid-State Circuits, Guest Editor, September 2003; IEEE Circuits and Systems Society Chairman, 2001-2002; 2003 Analog Signal Processing Technical Committee Track Chair for the Intl. Symposium on Circuit and Systems


10. Briefly list the most recent professional development activities – Attended the Symposium on VLSI Circuits, 2008 and Symposium on VLSI Circuits, 2008
1. Name – ALAN D. GEORGE

2. Education – PhD, CSC, Florida State University, 1991; MS, ECE, University of Central Florida, 1985; BS, CSC, University of Central Florida, 1982

3. Academic experience – Electrical & Computer Engineering, University of Florida, Professor, 2004-date, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 1997-2004, full time; Electrical Engineering, Florida A&M University-Florida State University, Associate Professor, 1993-1997, full time; Electrical Engineering, Florida A&M University-Florida State University, Assistant Professor, 1993-1997, full time; Electrical Engineering, Florida A&M University-Florida State University, Visiting Assistant Professor, 1987-1989, full time

4. Non-academic experience – Task/Group Leader and Senior Computer Systems Engineer, Martin Marietta Missile and Electronics Systems, Orlando, Florida, responsible for technical development and administration of majority of all scientific superminicomputers, minicomputers, and workstations in entire plant, consisting of facilities servicing over 1000 engineers and technicians, responsible for the supervision of a staff of 20 engineers and operators, 1985-1987, full time; System Manager and Computer Systems Engineer, General Electric, Simulation and Control Systems Department, Daytona Beach, Florida, responsible for technical development and administration of all computers in Consolidated Engineering Computer Facility, consisting of facilities servicing over 400 engineers and technicians, 1983-1985, full time; Programmer/Analyst, University of Central Florida and Naval Training Systems Center, Orlando, Florida, Responsible for design, development, and generation of computer animation software for Computer Simulation Laboratory, and design and implementation of tracking and training software systems for Training Analysis and Evaluation Group, 1980-1983, part time

5. Certifications or professional registrations – None


7. Honors and awards – 2010-College Advising Award for Faculty Mentoring (UF); 2009-College Advising Award for Doctoral Student Mentoring (UF); 2005-University Service Award, OIT Volunteer of Year (UF); 1999-University Productivity Award (UF); 1995-State of Florida Teaching Incentive Program (TIP) Award (FAMU-FSU); 1994-Tau Beta Pi, Florida Eta Chapter, EE Professor of the Year Award (FAMU-FSU); 1993-University Teacher of the Year Award (FAMU-FSU)

8. Service activities (within and outside of the institution) – Chair, University High-Performance Computing Committee (2001-2009); Member, Faculty Search Committee, (1997-2004, 2006-2008); Member, ABET Course Curriculum Subcommittees, (2005-date); Member, Ph.D. Exam Committee, (2005-2007); Member, Graduate/Undergrad
Curriculum Committee, (2005-2006); Chair, Graduate/Undergrad Curriculum Committee, (2003-2005); served on editorial boards at three scholarly journals including *IEEE Transactions on Computers*, *Cluster Computing*, and *Microprocessors and Microsystems*. He has served as program chair, program co-chair, general chair, and general vice-chair at several IEEE conferences and workshops, keynote speaker at RSSI'07 and MRSC'08 conferences and HPRCTA workshop at SC’09, and on program committees at numerous other conferences


10. Briefly list the most recent professional development activities – Attended the Reconfigurable Architectures Workshop, 2010 and the High-Performance Reconfigurable Computing Technology and Applications Workshop, 2009
1. Name – JAMES P. GOETTEN

2. Education – MS, EE, University of Florida, 1993; BSEE, University of Florida, 1991

3. Academic experience – Associate in Engineering, Electrical & Computer Engineering, University of Florida, 1997-date, full time;


5. Certifications or professional registrations – State of Florida Registered Professional Engineer; State of Florida Licensed Electrician

6. Current membership in professional organizations – Institute of Electrical and Electronics Engineers, National Fire Protection Association

7. Honors and awards – None

8. Service activities (within and outside of the institution) – None

9. Briefly list the most important publications and presentations from the past five years – None

10. Briefly list the most recent professional development activities – None
1. **Name** – **ANN GORDON-ROSS**

2. **Education** – PhD, CSE, University of California-Riverside, 2007; BS, CSE, University of California-Riverside, 2000

3. **Academic experience** – Electrical & Computer Engineering, University of Florida, Assistant Professor, 2007-date, full time; Department of Computer Science and Engineering, University of California at Riverside, Lecturer/Instructor, 2004-2006, part time

4. **Non-academic experience** – None

5. **Certifications or professional registrations** – None

6. **Current membership in professional organizations** – 2009-date, Women in Engineering ProActive Network; 2007-date Association of Computing Machinery; 2001-date Institute of Electrical and Electronic Engineers


9. Briefly list the most important publications and presentations from the past five years –


10. Briefly list the most recent professional development activities – Attended various conferences and workshops including the National Association for Multicultural Engineering Program Advocates and the Women in Engineering ProActive Network Conference
1. Name – **QUN 'JANE' GU**

2. Education – PhD, EE, University of California-Los Angeles, 2007; MS, ECE, University of Iowa, 2002; MS, EE, Huazhong University of Science & Technology, 2000; BS, EE, Huazhong University of Science & Technology, 1997

3. Academic experience – Electrical & Computer Engineering, University of Florida, Assistant Professor, 2010-date, full time; University of California, Los Angeles, Post-Doctoral Researcher, 2009-2010, full time

4. Non-academic experience – Staff Design Engineer, AMCC, San Diego, CA, 2008-2009, full time, Develop 40Gbps ultra-high throughput and high-quality data communication systems over twisted wires; design wide bandwidth, low noise synthesizer for low jitter clock generation; Senior Design Engineer, Wionics Research, Realtek Group, Irvine, CA, 2007-2008, full time, Led the team to design a complete 60GHz transceiver front end with a full band synthesizer in 65nm CMOS technology. Invented several energy efficient circuit design techniques and created sophisticated design methodology for CMOS V-band link; System Verification Intern, SST Communication Inc., Los Angeles, CA, 2004, full time, WLAN 802.11 a/b/g transceiver/system testing and debugging; Circuit Design Intern, SST Communication Inc., Los Angeles, CA, 2003, full time, WLAN 802.11 a/b/g transceiver design and layout

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineers

7. Honors and awards – None

8. Service activities (within and outside of the institution) – Serve as faculty searching committee member; Reviewer for various technical journals, including IEEE Journal of Solid-State Circuits, IEEE Microwave and Wireless Components Letters, IEEE Transactions on Circuits and Systems-I, IEEE Communications Magazine

9. Briefly list the most important publications and presentations from the past five years –


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1. Name – KARL GUGEL

2. Education – PhD, ECE, University of Florida, 1993; MSE, EE, Florida Atlantic University, 1987; BSEE, Michigan Technological University, 1981

3. Academic experience – Electrical & Computer Engineering, University of Florida, Lecturer/Researcher, 2000-date, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – None

7. Honors and awards – 2010 IEEE ECE Teacher of the Year

8. Service activities (within and outside of the institution) – Updated course EEL 4744-Microprocessor Applications and EEL 3923-Junior Design

9. Briefly list the most important publications and presentations from the past five years – “Optimizing Mill Control Through the Use of Vibration Analysis”, Karl Gugel and Rodney Moon, IEEE-IAS/PCA Cement Industry Technical Conference, Charleston, South Carolina, April 2007


10. Briefly list the most recent professional development activities – IEEE International Conference of the Engineering in Medicine and Biology Society, IEEE-IAS/PCA Cement Industry Technical Conference
1. Name – Jing Guo

2. Education – PhD, EE, Purdue University, 2004; MS, EE, Shanghai Jiao Tong University, 2000; BSEE, Shanghai Jiao Tong University, 1998

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2010-date, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2004-2010, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers

7. Honors and awards – NSF Faculty Early CAREER Award, 2009


9. Briefly list the most important publications and presentations from the past five years –


1. Name – JACOB HAMMER

2. Education – DSc, EE, Technion-Israel Institute of Technology, 1980; MS, EE, Technion-Israel Institute of Technology, 1977; BSEE, Technion-Israel Institute of Technology, 1974


4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Fellow, Institute of Electrical & Electronic Engineers


8. Service activities (within and outside of the institution) – Director of Graduate Programs/Graduate Coordinator and Chairman of Graduate Studies Committee, Department of Electrical and Computer Engineering, University of Florida (August 1997-August 2005); Area Chairman for Systems and Control, Department of Electrical Engineering, University of Florida (January 1988-December 1989, 1996-2005); Member, Curriculum Committee, Department of Electrical Engineering, University of Florida, (1991- 1993); Member, Graduate Committee, Department of Electrical Engineering, University of Florida (1988-1990, 1996 - 2005); Associate Editor of the IEEE Transactions on Automatic Control (Jan. 1990 - Dec. 1992); Designated Reviewer for the IEEE Control Systems Society (nonlinear control area); Panelist for Fellowship Programs, National Research Council (nominated); Nominator for 1990 Japan Prize. Served as referee for the following Journals and Funding agencies; IEEE Transactions on Automatic Control; IEEE Transactions on Systems, Man, and Cybernetics; IEEE Transactions on Circuits and Systems; IEEE Transactions on Biomedical Engineering; International Journal of Control; SIAM Journal on Control and Optimization; Systems and Control Letters; Journal of Mathematical Systems, Estimation, and Control; Automatica; National Science Foundation; National Security Agency
9. Briefly list the most important publications and presentations from the past five years –


10. Briefly list the most recent professional development activities – Attended European and Asian Control Conferences, Mediterranean Conference on Control and Automation, IEEE Conference on Systems Man, and Cybernetics, International Conference of Applied Mathematics and Computing as well as other conferences.
1. **Name** – **JOHN G. HARRIS**

2. **Education** – PhD, EE, California Institute of Technology, 1991; MS, EE, Massachusetts Institute of Technology, 1986; BSEE, Massachusetts Institute of Technology, 1983

3. **Academic experience** – Electrical & Computer Engineering, University of Florida, Chair and Professor, 2011-date, full time; Electrical & Computer Engineering, University of Florida, Interim Chair and Professor, 2009-2011, full time; Electrical & Computer Engineering, University of Florida, Professor, 2006-2009, full time; Electrical & Computer Engineering, University of Florida, Associate Chair, 1997-2006, full time; Electrical & Computer Engineering, University of Florida, Assistant Chair, 1993-1997, full time; Artificial Intelligence Lab, Massachusetts Institute of Technology, Postdoctoral Fellow, 1991-1997, full time

4. **Non-academic experience** – Bolt Beranek and Newman Inc., Summer 1984 Intern, Worked on the Monarch multiprocessor, specifically, designed prototype dynamic memory registers for the Monarch processor chip, full time; International Business Machines Corp., Summer 1983 Intern, Designed a CMOS VLSI standard cell library. Layouts were automatically generated based upon electrical specifications, full time; Software Arts Inc. (Creators of VisiCalc), Summer 1982 Intern, Designed the Electrical Engineering Applications, Package for the TK!Solver program, full time

5. **Certifications or professional registrations** – None

6. **Current membership in professional organizations** – Institute of Electrical & Electronic Engineers; International Society for Optical Engineering; International Neural Network Society; Speech Communication Association

7. **Honors and awards** – Member of the Motorola Visionary Board 2002-2005; 1998-99 University of Florida Teaching Improvement Program Winner; NSF CAREER Award, 1995; NSF Postdoctoral Research Fellowship 1991-93, (NSF Division of Advanced Scientific Computing); Hughes Aircraft Graduate Doctoral Fellowship 1987-91; Tau Beta Pi (Engineering Honor Society); Eta Kappa Nu (Electrical Engineering Honor Society)

8. **Service activities (within and outside of the institution)** – Founding member of the UF Biomedical Engineering Program; Member of the University of Florida Brain Institute, 1997-present; Member of the University of Florida Center for Smell and Taste, 1998-present; Member of the University of Florida Center for Research at the Bio/Nano Interface, 2000-present; ECE Graduate Admissions and Recruiting Coordinator, 2005-2009; Founder and organizer of ECE Graduate Seminar Course; UF MOSIS VLSI Fabrication Liaison, 1996-present; Member of many past ECE and BME department search committees; Associate Editor, Frontiers in Neuromorphic Engineering, 2008-present; President of IEEE Technical Committee on Neural Systems and Applications (NSA TC), Circuits & Systems Society; Co-organizer of yearly Telluride Neuromorphic Cognitive Workshop, 2009-2011; Member of the Program Committee for International...
Briefly list the most important publications and presentations from the past five years –


1. Name – **DOUGLAS M. JORDAN**

2. Education – PhD, EE, University of Florida, 1990; MS, EE, University of Florida, 1981; BSEE, University of Florida, 1980

3. Academic experience – Electrical and Computer Engineering, University of Florida, Research Assistant Scientist, 2010-present, full time; Electrical & Computer Engineering, University of Florida, Senior Lecturer, 2004-2010, full time; Electrical & Computer Engineering, University of Florida, Lecturer, 2000-2004, full time; Electrical Engineering, University of West Florida, Associate Professor, 1994-2000, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – American Geophysical Union


8. Service activities (within and outside of the institution) – College of Engineering Tenure and Promotion Committee; Undergraduate Coordinator, Electrical and Computer Engineering

9. Briefly list the most important publications and presentations from the past five years –


10. Briefly list the most recent professional development activities – None
1. Name – **JASMEET JUDGE**

2. Education – PhD, EE & AOSS, University of Michigan 1999; MS, EE, University of Michigan, 1994; BS, PHY, Stillman College, 1992

3. Academic experience – Associate Professor, Agricultural and Biological Engineering, University of Florida, 2008-present, full time; Director, Center for Remote Sensing, Agricultural and Biological Engineering, University of Florida, 2001-present, full time; Assistant Professor, Agricultural and Biological Engineering, University of Florida, 2001-2008, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – American Society of Agricultural & Biological Engineers; Institute of Electrical & Electronic Engineers, Society of Women Engineers; American Geophysical Union

7. Honors and awards – Gamma Sigma Delta Honor Society of Agriculture, 2011; Outstanding Young Researcher, Florida Section ASABE, 2008; Outstanding Faculty Advisor in Region D, Society of Women Engineers, 2006

8. Service activities (within and outside of the institution) – Member, Plant Science Research and Education Unit Advisory Council, UF/IFAS; Faculty Advisor, UF Chapter for the Society of Women Engineers; Member, Search and Screen Committee for Faculty and Research Scientist positions; Member, Department Computer Committee; Member, Department Graduate Curriculum Committee; Member, Department Agrisystems Major Committee; Member, Department Social Committee; Associate Editor, Transactions of ASABE, Applied Engineering in Agriculture, 2004-2007; Reviewer and Panel Review Member for NSF, USDA, USGS, NOAA, and NASA proposals; Reviewer of Journals: IEEE Transactions in Geoscience & Remote Sensing, Journal of Hydrometeorology, Geophysical Research Letters, International Journal of Remote Sensing, Advances in Water Resources, Vadose Zone Hydrology, Water Resources Research; Technical Program Committee for IEEE-IGARSS; Chair, Remote Sensing Technical Committee, American Geophysical Union


10. Briefly list the most recent professional development activities – LEAD-IFAS, 2010-2011, College of Agricultural and Life Sciences annual Teaching Enhancement Symposia
1. Name – YOUNG JUNG

2. Education – PhD, Computer Science, Ajou University, 2007; MS, Computer Science, Ajou University, 2002; BS, Information and Communication, Ajou University, 2000

3. Academic experience – None


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Collaborative Computing: Newtorking, Applications and Worksharing


8. Service activities (within and outside of the institution) – Publicity Chair, Conference on Collaborative Computing: Newtorking, Applications and Worksharing, 2008; Program Committee Member, Conference on Collaborative Computing: Newtorking, Applications and Worksharing 2009 and 2010


10. Briefly list the most recent professional development activities – None
1. Name – PRAMOD P. KHARGONEKAR


3. Academic experience – Electrical & Computer Engineering, University of Florida, Professor and Eckis Chair, 2009-date, full time; Electrical & Computer Engineering, University of Florida, Dean, College of Engineering; Associate Vice President, Engineering & Industrial, Experiment Station; Eckis Professor, 2001-2009, full time; Electrical Engineering & Computer Science, University of Michigan, Chair, 1997-2001, full time; Electrical Engineering & Computer Science, University of Michigan, Associate Chair, 1995-1997, full time; Electrical Engineering & Computer Science, University of Michigan, Professor, 1989-1995, full time; Electrical Engineering, University of Michigan, Professor, 1988-1989, full time; Electrical Engineering, University of Minnesota, Associate Professor, 1984-1988, full time; Electrical Engineering, University of Florida, Assistant Professor, 1981-1984, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations –


8. Service activities (within and outside of the institution) – Member, Budget Allocation Committee, September 2006-April 2007; Member, Board of Directors, University of Florida Foundation, May 2003-June 2009; Chair, Search Committee, Director of Institutional Research, January 2006-April 2006; Member, Board of Directors, Center for Entrepreneurship and Innovation, College of Business, August 2002-date; Member, Advisory Board, International Center for Automated Information Research, Levin College of Law, August 2002-date; Member, Board of Directors, University of Florida Research Foundation, July 2001-June 2008; Member, Advisory Board, School of Natural Resources and Environment, July 2003-date; Member, Research Policy Council, University of Florida, July 2001- June 2009; Member, Advisory Board, Digital Worlds Institute, January 2002- June 2009; Member, Deans Advisory Group, University of Florida Foundation, August 2004- June 2008; Chair, Search Committee, Dean of College of Liberal Arts and Sciences, University of Florida, September 2007- April 2008;
Member, Executive Committee, Howard Hughes Medical Institute Science for Life Program, University of Florida, August 2009-date; Member, Advisory Board, Computational Biology Program, University of Florida, August 2009-date; Member, Strategic Planning Task Force for Energy Research, College of Engineering, University of Florida, September 2009-date; Member, Strategic Planning Task Force for Leadership Initiative, College of Engineering, University of Florida, October 2009-date; Associate Editor, Mathematical Problems in Engineering, November 1994-date; Member, External Review Team, Faculty of Engineering, Yale University, November 2006; Member, Advisory Board, Indian Institute of Technology, Mumbai, India, 2006-date; Member, Executive Committee, Board of Directors, IIT Bombay Heritage Fund, 2005-date; Member, Public Policy Committee, Engineering Deans Council, September 2005-date; Chair, Awards Committee, American Automatic Control Council, 2005-date.

9. Briefly list the most important publications and presentations from the past five years –


10. Briefly list the most recent professional development activities – Sabbatical at University of California, 2010-2011; attended conferences including International Conference on Distributed Computing Systems and IEEE Conference on Decision and Control.
1. Name – **GLORIA J. KIM**

2. Education – PhD, BME, Georgia Institute of Technology and Emory University, 2007; MSE, BME, Johns Hopkins University, 2001, BS, Chm, Seoul National University, 1997

3. Academic experience – Research Assistant Professor, Electrical and Computer Engineering, University of Florida, 2011-date; Chemistry, University of Florida, 2010-2011, full time; SUNY-Buffalo, Research Assistant Professor, Biomedical Engineering, 2009-2010, full time, SUNY-Buffalo, Research Assistant Professor, Electrical Engineering, 2009, full time,

4. Non-academic experience – company or entity, title, brief description of position, when (ex. 1993-1999), full time or part time

5. Certifications or professional registrations – None

6. Current membership in professional organizations – American Chemical Society, Biomedical Engineering Society, Controlled Release Society

7. Honors and awards – None

8. Service activities (within and outside of the institution) – Manuscript reviewer for IOP Biomedical Materials and Nanotechnology; Faculty Facilitator, Southeast Regional Conference of Asian American Leaders Workshop, UF

9. Briefly list the most important publications and presentations from the past five years – “Multimodal Biological Imaging and Therapeutics, Kim, Gloria J., Weibo, Cai, Drummen, Gregor, Nano Today, 2011


“Spatially Controlled Electrospun Solid Gradient Nanofibers for Guided Spiral Ganglion Neuron Culture,” Pitfee Jao, Wei Sun, Yong-Kyu Yoon, and Gloria J. Kim, Biomedical Engineering Society Annual Fall Meeting, Austin, TX, October 6-9, 2010.
10. Briefly list the most recent professional development activities – attended various conferences.
1. Name – HERMAN LAM

2. Education – PhD, EE, University of Florida, 1979; ME, EE, University of Florida, 1974; BSEE, Georgia Institute of Technology, 1972

3. Academic experience – Electrical & Computer Engr., University of Florida, Associate Professor, 1985-date, full time; Electrical & Computer Engr., University of Florida, Assistant Professor, 1980-1985, full time; Computer and Information Science, University of Florida, Adjunct Assistant Professor, 1979-1980, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers, Association for Computing Machinery

7. Honors and awards – 1993-UF Teaching Improvement Program (TIP); 1991-College of Engineering Undergraduate Teaching Award; 1991-ECE Supervisor of the Outstanding Senior Design Project; 1991-Outstanding Paper Award (coauthor), 7th Int. IEEE Conference on Data Engineering, Japan; 1990-ECE Award for Outstanding Supervisor of an EE Core Laboratory; 1990-ECE Award for Best PhD Dissertation (co-chair); 1989-ECE Award for Outstanding Supervisor of an EE Core Laboratory; 1988-ECE Award for Outstanding Supervisor of an EE Core Laboratory; 1986-Tau Beta Pi Undergraduate Teaching Award

8. Service activities (within and outside of the institution) – Chair, Department of ECE Curriculum Committee; Chair, EEL 4712 course committee; Member, EEL 3701 course committee; Department of ECE ABET Monitoring Committee; CE ABET subcommittee for Program Outcomes; Reviewer for journals: IEEE Transactions on Software Engineering; International Journal of Computer and Information Science; IEEE Transactions on Computers; ACM Computing Surveys; Data and Knowledge Engineering Journal; IEEE Transactions on Knowledge and Data Engineering; IEEE MICRO, Journal of Computer Systems Science and Engineering; IEEE Internet Computing; IEEE Journal of Transactions on Parallel and Distributed Systems; IBM Systems Journal; IEEE Transaction on Service Computing; Editorial Board Member of the International Journal of Business Process Integration and Management, 2004-2009; Program committee, workshop for ICEBE 2008 (International Conference on ebusiness Engineering), Xi’an, China, October 22-24, 2008; Program committee, ReConFig’08, 2008 International Conference on ReConFigurable Computing and FPGAs, Cancun, Mexico, December 3-5, 2008; Program committee, IEEE International Conference on Web Services (ICWS08), Beijing, China, September 23-26, 2008; Program committee, International Workshop on High-Performance Reconfigurable Computing Technology and Applications (HPRCTA’09), Portland, Oregon November 15, 2009; Program committee, 2009 International Conference on ReConFigurable
Computing and FPGAs (ReConFig09), Cancun, Mexico, December 9-11, 2009; Program committee, 2009 Symposium on Application Accelerators in High-Performance Computing (SAAHPC09), Urbana, Illinois, July 27-31, 2009; Program committee, WoRMES'2009 International Workshop on Reconfigurable and Multi-core Embedded Systems, Vancouver, Canada, Aug 29-31, 2009.

9. Briefly list the most important publications and presentations from the past five years – “Reconfigurable Supercomputing with Scalable Systolic Arrays and In-Stream Control for Wavefront Genomics Processing,” C. Pascoe, A. Lawande, H. Lam, A. George, Y. Sun, W. Farmerie, and M. Herbordt, Proc. of Symposium on Application Accelerators in High-Performance Computing (SAAHPC), Knoxville, TN, July 13-15, 2010


1. Name – **HANIPH A. LATCHMAN**

2. Education – PhD, EE, Oxford University, 1986; BS, EE, University of the West Indies

3. Academic experience – Electrical & Computer Engineering, University of Florida, Professor, 2003-date, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 1993-2003, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 1986-1993, full time

4. Non-academic experience – Computer Networking and Forensics Consultant, 1995-date, part time; Co-founder and Technical Director, Jamaica Online Information Systems & Qualitech Computer Services, 1994-date, part time

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers, American Society of Engineering Education


8. Service activities (within and outside of the institution) – Member of Department Faculty Search Committee, September 2006-date; Member of Department Curriculum Committee, 2005-date; Member of the Board, University Center for Excellence in Teaching, 1998-date; Member, Rhodes/Marshal Scholarship Selection Committee, 1991-date; Associate Editor for IEEE Transactions on Education, 1998-date; Member IEEE Education Society Administration Committee, December 2002-date; Reviewer for International Journal of Control, 1990-date; Reviewer for IEEE Transactions on Control, 1989-date; Reviewer for IEEE Transactions on DSP, 1995-date; Reviewer for IEEE Transactions on Education, 1995-date; Reviewer for Interactive Multimedia Electronic Journal of Computer Enhanced Learning, 1999-date; Reviewer for John Wiley, 1990-date; Reviewer for Prentice Hall 1990-date; Reviewer for McGraw Hill 1990-date; Reviewer for Oxford University Press 1995-date; Reviewer for IEEE Signal Processing Letters 1995-date; Review for the National Science Foundation 1997

9. Briefly list the most important publications and presentations from the past five years – “Multimedia Powerline Communication Systems”, Haniph A. Latchman and Larry


10. Briefly list the most recent professional development activities – Attend various conferences including IEEE International Conference on Control and Automation, World Multi-Conference on Systems, Cybernetics and Informatics, IEEE International Conference on Power Line Communications and Its Applications
1. Name – **MARK E. LAW**

2. Education – PhD, EE, Stanford University, 1988; MS, EE, Stanford University, 1982; BS, CprE, Iowa State University, 1981

3. Academic experience – College of Engineering, University of Florida, Associate Dean for Academic Affairs, 2009-date, full time; Electrical & Computer Engineering, University of Florida, Professor and Department Chair, 2003-2009, full time; Electrical & Computer Engineering, University of Florida, Professor, 1997-2003, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 1993-1997, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 1988-1993, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Fellow, Institute of Electronics and Electrical Engineers; Member, The Bohmische Physical Society; Materials Research Society; Electrochemical Society; American Physical Society; American Society for Engineering Education; Tau Beta Pi; Phi Kappa Phi; Eta Kappa Nu; Sigma Xi

7. Honors and awards – 2010, IEEE Electron Device Society J. J. Ebers Award, 2007-Professional Achievement Citation in Engineering, Iowa State University; 2006-Semiconductor Research Corporation Aristotle Award; 2004-Semiconductor Research Corporation Faculty Recognition Award for Student Recruiting; 1998-IEEE Fellow; 1997-UF Research Foundation Faculty Award; 1996- College of Engineering Teacher of the Year; 1995-UF Teaching Improvement Program (TIP) Award; 1994-Outstanding Young Alumnus of Iowa State University; 1994- Iowa State University, College of Engineering, Professional Progress Award; 1993-Semiconductor Research Corporation Teaching Excellence Award; 1992- National Science Foundation Presidential Faculty Fellow

8. Service activities (within and outside of the institution) – Member, University Honors Committee Advisory Board, 2010-date; Chair, ECE Undergraduate Curriculum Reform Committee, 2008-2009; Chair, Electrical and Computer Engineering Department, 2003-2009; Chair, ECE Faculty Search Committee, 2003; Chair, College of Engineering Nano-Bio Faculty Search, 2002-2003; Chair, Department Strategic Planning Committee, 2002-2003; Co-Director of the Nanoscience and Technology Institute 2001-2002; Member, Academic Personnel Board, 2001-2003; Chair, Ad-hoc Committee to Review College Core, 1999; Director of Undergraduate Programs 1998-2000; Chair, Departmental Curriculum Committee, 1995-2000; Member, College Curriculum Committee, 1998-2001; Member, College Petitions Committee, 1996-98; Member, College Committee to Examine the Undergraduate Core, 1995; Member, Departmental Faculty Development Committee, 1995, 1998; Chair, Ad-hoc Committee on Curriculum Reform, 1993-1994;
Member, Departmental Student Awards Committee, 1991-1994; Member, Departmental Graduate Committee, 1989-93; Member, Numerous Search Committees; Chair, Iowa State University Honors Advisory Board, 2008-date; Associate Editor, IEEE Circuits and Devices Magazine, 2002-2006; Member, Editorial Board, Journal of Modeling and Simulation of Microsystems, 1998-2008; Editor-in-Chief, IEEE Journal of Technology Computer Aided Design, 1996-2001; Co-Organizer, MRS Symposium on Front-End Processing, 2008; Co-Organizer, AVS Ultra-Shallow Junctions Workshop, 2005; Lead Organizer, MRS Symposium on Front-End Processing, 2002; General Chair, International Electron Devices Meeting, 2000; Co-Organizer, MRS Symposium on Front-End Processing, 1999; Technical Program Vice Chair, International Electron Devices Meeting, 1998; General Chair, Simulation of Semiconductor Processes and Devices, 1997; President, Southeast ECE Department Heads Organization, 2007; Vice-President, Southeast ECE Department Heads Organization, 2006; Secretary, Southeast ECE Department Heads Organization, 2005; Vice President, Electron Device Society Technical Activities, 2003-2006; Chairman, Electron Device Society TCAD Technical Advisory Board, 2000-2004; Member, Electron Device Society VLSI Circuits Technical Advisory Board, 2001-2004

9. Briefly list the most important publications and presentations from the past five years –


10. Briefly list the most recent professional development activities – Technical Conference Attendance – typically IEDM, NSREC, and others during each year
1. Name – XIAOLIN 'ANDY' LI

2. Education – Ph.D., Computer Engineering, Rutgers University, 2005; Ph.D. Communications & Information Engineering, National University of Singapore, 2001; MS, Zhejiang University, 1998; BS, Qingdao University, 1995

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2010-date, full time; Department of Computer Science, Oklahoma State University, Assistant Professor and Director for the Scalable Software Systems Laboratory, 2005-2010, full time

4. Non-academic experience – IBM Austin Lab, Extreme Blue Intern, 2003, full time; Center for Wireless Communications (now I2R), Singapore, Research Staff, 2001, full time

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineers, Association of Computing Machinery

7. Honors and awards – National Science Foundation CAREER Award, 2010; Finalist for the The Second IEEE International Scalable Computing Challenge (SCALE 2009); Outstanding Service Award, The 17th IEEE International Conference on Computer Communications and Networks (ICCCN 2008); Outstanding Service Award, The IEEE 21st International Conference on Advanced Information Networking and Applications (AINA 2007); Best Paper Award, the 2007 IEEE International Symposium on Ubisafe Computing

8. Service activities (within and outside of the institution) – Associate Editor, Security and Communication Networks (SCN), Wiley, 2007-date; Associate Editor, International Journal of Communication Systems (IJCS), Wiley, 2007-date; Executive Committee, IEEE Technical Committee on Scalable Computing (TCSC); Coordinator, Technical Area of Sensor Networks, IEEE TCSC; Panelist, National Science Foundation (NSF), (CSR, STCI, REU, CAREER), 2006, 2007, 2009; Program Chair, The 8th IEEE International Conference on Dependable, Autonomic and Secure Computing (DASC 2009), Chengdu, China December 12-14, 2009; Program Chair, The 6th IEEE Workshop on Challenges of Large Applications in Distributed Environments (CLADE 2008) in conjunction with HPDC 2008; Program Chair, The 5th International Conference on Autonomic and Trusted Computing (ATC 2008); TPC member, INFOCOM 2009-2012, MASS09, CCGrid08 and 07, Globecom08 and 07, ICPADS08, ICC10

9. Briefly list the most important publications and presentations from the past five years – “A Taxonomy of Peer-to-Peer Desktop Grid Paradigms,” H. Zhao, X. Liu, and X. Li, Cluster Computing Journal, Springer, 2010


“Advanced Computational Infrastructures for Parallel and Distributed Adaptive Applications,” M. Parashar and X. Li (Eds.), John Wiley & Sons, 2009


10. Briefly list the most recent professional development activities – Added various conferences including IEEE International Symposium on Cluster Computing and the Grid; the IEEE International Parallel and Distributed Processing Symposium
1. Name – JENSHAN LIN

2. Education – PhD, EE, University of California-Los Angeles, 1994; MS, EE, University of California-Los Angeles, 1991; BS, Electrophysics, National Chiao Tung University, 1987

3. Academic experience – Department of Electrical and Computer Engineering, University of Florida, Professor, 2007-date, full time; Department of Electrical Engineering National Taiwan University, Taipei, Taiwan, Visiting Professor, 2006-2007, full time; Department of Electrical and Computer Engineering, Associate Professor, 2003-2007, University of Florida, full time; Rutgers University, WINLAB, Visiting Professor, 2001-2002, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineers

7. Honors and awards – 2010 IEEE Fellow; 2008 IEEE MTT-S Graduate Fellowship Award with Changzhi Li; 2007 IEEE Microwave Theory and Techniques Society N. Walter Cox Award; 2007 IEEE Radio and Wireless Symposium Best Student Paper Award 2nd place with Changzhi Li; 2004 IEEE MTT-S Undergraduate/Pre-Graduate Scholarship Award with Jerry Jun; 1997 Eta Kappa Nu Outstanding Young Electrical Engineer Award Honorable Mention; 1994 UCLA School of Engineering and Applied Science Outstanding PhD Award

8. Service activities (within and outside of the institution) – Chair, Electronics Division, ECE Department 2008-2010; Member, Faculty Development Committee, ECE Department 2009-2010; Member, Search Committee, College of Engineering Associate Dean for Academic Affairs, 2009; Elected Senator, University of Florida Faculty Senate Aug. 2006-Aug. 2009; Elected member of Faculty Council, College of Engineering Aug. 2006-Aug. 2009; Member, Budget Committee, ECE Department 2008-2009; Member, Intel Chair Search Committee, ECE Department 2004-2007; Member, PhD Exam Committee, ECE Department 2004-2008; Member, Faculty Search Committee, ECE Department 2003-2006; Member, Financial Aid Committee, ECE Department 2004; Chair, Technical Coordinating Committee, IEEE MTT-S AdCom (2010-date); Elected Member, IEEE MTT-S AdCom (2006-date); Technical Program Chair, IEEE Radio and
Wireless Symposium (2009); General Chair, IEEE RFIC Symposium (2008); Technical Program Chair, IEEE RFIC Symposium (2007); Associate Editor, IEEE Transactions on Microwave Theory and Techniques (2006-2010)

9. Briefly list the most important publications and presentations from the past five years –


10. Briefly list the most recent professional development activities – Attended various conferences
1. Name – **NIMA MAGHARI**

2. Education – PhD, EE, Oregon State University, 2010; BS, EE, University of Tehran, 2005

3. Academic experience – Department of Electrical & Computer Engineering, University of Florida, Assistant Professor, 2011-date, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electric and Electronic Engineers


8. Service activities – Reviewer of IEEE-International Conference on Circuit and Systems (ISCAS), Journal of Solid-State Circuits (JSSC), Transactions on Circuits and Systems (TCAS) and Transactions on Very Large Scale Integration Systems (TVLSI)


10. Briefly list the most recent professional development activities – Attended various seminars and conferences.

1. Name – ANDREA MATSUNAGA

2. Education – PhD, ECE, University of Florida, 2010; MS, EE, University of Sao Paulo, 2001; BS, EE, University of Sao Paulo, 1998

3. Academic experience – None

1. Non-academic experience – Advanced Computing & Information System Laboratory, University of Florida, Assistant Research Scientist, Researching new approaches for developing a permanent and powerful cloud computing infrastructure to link biological data from collections across the United States into a single unified web interface, overcoming the “data silos” problem, as part of the NSF funded Integrated Digitized Biocollections (iDigBio) project, 2011-date; Advanced Computing & Information System Laboratory, University of Florida, PostDoctoral Associate, Managed VMware and Xen-based virtual computing systems for the DARPA Reorganization and Plasticity to Accelerate Injury Recovery (REPAIR) project, 2010-2011, full time.

1. Certifications or professional registrations – None

2. Current membership in professional organizations – Institute of Electrical and Electronic Engineers, Women in Electrical & Computer Engineering


6. Briefly list the most recent professional development activities – Attended various seminars and conferences.
1. Name – JANISE McNAIR

2. Education – PhD, ECE, Georgia Institute of Technology, 2000; MS, EE, University of Texas at Austin, 1993; BSEE, University of Texas at Austin, 1991

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2009-date, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2000-2009, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers; IEEE Communications Society; IEEE Computer Society; Association for Computing Machinery; American Society of Engineering Education; National Society of Black Engineers

7. Honors and awards – 2008-Selected to participate in DARPA Computer Science Study Group; April-June 2007 WSN Location Verification paper in ScienceDirect’s Top 25 Hottest Articles

8. Service activities (within and outside of the institution) – Chair, Graduate Recruitment and Admissions Committee, Department of Electrical and Computer Engineering, 2009-present; Chair, ABET Course Subcommittee for Data and Computer Communications; Member, ABET Subcommittee for Computer Engineering Program; Member, College of Engineering Information Technology Strategic Plan Committee; Faculty Advisor, National Society of Black Engineers (NSBE), University of Florida, 2007-present; Organizer, Women in Engineering Seminars, UF Engineers Week, 2003-2006, Mentor, University of Florida Minority Mentor Program, 2001-2004; Participant in National Science Foundation Proposal Review Panels; Associate Editorships: Elsevier Ad Hoc Networks Journal, 2004-date, ACM/Baltzer Wireless Networks Journal 2010-date; IEEE Transactions on Mobile Computing, 2003-2008; Conference Organizing Committees, Workshop Co-Chair, IEEE INFOCOM, March 2010, General Vice Chair, IEEE International Conference on Testbeds and Research Infrastructures for the Development of Networks and Communities, May 2007, Technical Program Committee Chair, IFIP International Conference on Networking, May 2007; Finance Chair, Int’l Symposium on Power Line Communications (ISPLC), March 2006, Publicity Chair, ICST Mobileware 2008 - 2010; Session Chair IEEE MILCOM, 2007-2010; Panel Organizer, Wireless and Mobile Security in Distributed Systems, IEEE Symposium on Reliable Distributed Systems, October 2005, Member, Technical Program Committee for various IEEE and ACM Conferences

9. Briefly list the most important publications and presentations from the past five years – “Residual Energy Aware Channel Assignment in Cognitive Radio Sensor Networks,” X. Li,
D. Wang, J. McNair, and J. Chen, Proc of *IEEE Wireless Communications and Networking Conference (WCNC)*, March 2011


1. Name – **SETH McNEILL**


3. Academic experience – institution, rank, title (chair, coordinator, etc. if appropriate), when (ex. 1990-1995), full time or part time University of Florida, Adjunct Professor, 2011-2012, full time;

4. Non-academic experience – company or entity, title, brief description of position, when (ex. 1993-1999), full time or part time Tetracam, Inc, Director of Engineering Development, 2009-2011, Engineering, project management, product development, grant writing, marketing, sales, and customer service; Tetracam, Inc., Contract Projects Engineer, Matlab to DSP product development path testing, image stabilization

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineers

7. Honors and awards – None

8. Service activities (within and outside of the institution) – Secretary, UF Gator Amateur Radio Club, 2007-2009; Science Fair Judge, Alachua Region Science & Engineering Fair, 2008; UF Small Satellite Design Club


10. Briefly list the most recent professional development activities – None
1. Name – **SEAN MEYN**

2. Education – PhD, Electrical Engineering, McGill University, 1987; ME, Electrical Engineering, McGill University, 1985; BA, Mathematics, University of California, Los Angeles, 1982

3. Academic experience – University of Florida, Department of Electrical & Computer Engineering, Professor & Robert C. Pittman Eminent Scholar Chair, 2012-date, full time; University of Illinois-Urbana Champaign, Professor, 2001-2011, full time; University of Illinois-Urbana Champaign, Assistant/Associate Professor, 1989-2000, full time; Australian National University, Postdoctoral Fellow, 1987-1989, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None


7. Honors and awards – 2010 Outstanding Advisors List, University of Illinois-Urbana Champaign


“Feature selection for composite hypothesis testing with small samples: fundamental limit and algorithms,” D. Huang and S. Meyn, International Conference on Acoustic, Speech and Signal Processing (ICASSP 2012), 2012

10. Briefly list the most recent professional development activities – Attended various conferences.
1. Name – **ROBERT C. MOORE**

2. Education – PhD, EE, Stanford University, 2007; MSE, EE, Stanford University, 2001; BSE, EE, Stanford University, 1999

3. Academic experience – University of Florida, Department of Electrical & Computer Engineering, Assistant Professor, 2007-date, full time

Non-academic experience – Stanford University, Aero/Astro Department, Intern, summer 1998, Developed a data-transfer mechanism for GPS pseudolites in mining car automation, full time; Baan USA, Inc., Quality Assurance Engineer, Intern, Summer 1997, Designed and implemented automated test suites for the Bann V Internet application, full time; Fidelity Investments, Inc., Computer Analyst, 1994-1995, Designed and implemented software solutions for a financial-projections consulting company, full time

4. Certifications or professional registrations – None

5. Current membership in professional organizations – American Geophysical Union; International Union of Radio Science; Institute of Electrical and Electronics Engineers

6. Honors and awards – 2008 Young Scientist Award for XXIXth USI General Assembly; 2006 National Science Foundation Antarctic Service Medal; 1998 Hewlett Packard Project Award for Analog Design, Stanford University

7. Service activities (within and outside of the institution) – Graduate Recruitment and Admissions Committee; Secretary-Elect, American Chapter of the International Union of Radio Science (URSI), Commission H, 2011-present; U.S. Science Coordinator, Arrival Heights Site of Special Scientific Interest, Office of Polar Programs, National Science Foundation, 2010-present; Atmospheric and Space Electricity Executive Committee Member, American Geophysical Union, 2009-present; RF Ionospheric Interactions Workshop Steering Committee Member, 2009-present; Reviewer - *Journal of Geophysical Research, Geophysical Research Letters, Radio Science, IEEE Transactions on Electromagnetic Compatibility*


“D-region modification at HAARP: An overview of recent experimental observations obtained by the University of Florida,” Moore, R. C., (TALK SA31C-04), American Geophysical Union, AGU, San Francisco, CA, 2010

“Ionospheric Effects of Lightning at Camp Blanding, Florida,” Moore, R. C., XXIXth URSI General Assembly, Chicago, IL, August 7-16, 2008


9. Briefly list the most recent professional development activities – American Geophysical Union Annual Conference; RF Ionospheric Interactions Workshop Annual Meeting; International Union of Radio Science Annual Meeting; Polar Aeronomy and Radio Science Summer School
1. Name – KAMRAN MOHSENI

2. Education – PhD, EE, California Institute of Technology, 2000; MS, Aero & Applied Math, Imperial College, 1993; BS, Mechanical Engineering, University of Science and Technology, 1990

3. Academic experience – University of Florida, Department of Electrical & Computer Engineering, Professor, 2011-date, part time; University of Florida, Department of Mechanical & Aerospace Engineering, W. P. Bushnell Endowed Professor, 2011-date, part time; Aerospace Engineering Sciences, University of Colorado at Boulder, Associate Professor, 2007-2011, full time; Aerospace Engineering Sciences, University of Colorado at Boulder, Assistant Professor, 2001-2007, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineers, Society of Industrial and Applied Mathematics, American Institute of Aeronautics and Astronautics, American Society of Mechanical Engineers, American Physical Society

7. Honors and awards – Provost Faculty Achievement Award, University of Colorado at Boulder, 2008; NSF Faculty Early Career Development (CAREER) Program award, 2007; DARPA Young Faculty Award, 2007; Dean’s Award for Professional Progress, University of Colorado at Boulder, 2006

8. Service activities – Reviewer for several professional journals

9. Briefly list the most important publications and presentations from the past five years –


10. Briefly list the most recent professional development activities – Attended various conferences.
1. Name – **TOSHIZAKU NISHIDA**

2. Education – PhD, ECE, University of Illinois-Urbana, 1988; MS, ECE, University of Illinois-Urbana, 1985; BS, Engr Physics, University of Illinois-Urbana, 1983

3. Academic experience – institution, rank, title (chair, coordinator, etc. if appropriate), when (ex. 1990-1995), full time or part time
   - Electrical & Computer Engineering, University of Florida, Associate Chair, 2011-date, full time
   - Electrical & Computer Engineering, University of Florida, Professor, 2009-date, full time
   - University of Florida, Associate Professor, 1993-2009, full time
   - Visiting Associate Professor, University of Tokyo, Japan, 1994, part time
   - Electrical & Computer Engineering, University of Florida, Assistant Professor, 1988-1993, full time
   - Visiting Associate Professor, Osaka University, Japan, 1993, part time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers


10. Briefly list the most recent professional development activities – Sabbatical leave at Interuniversity Microelectronics Centre, Leuven, Belgium investigating strain effects on advanced semiconductor devices, Fall 2008
1. Name – **CHARLES H. “CHAD” OVERMAN IV**

2. Education – MS, ECE, University of Florida, 2001; BSEE, University of Florida, 1995


4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineers, Altera University Program Member

7. Honors and awards – None

8. Service activities (within and outside of the institution) – Volunteer Mission trip for two weeks in remote areas of Amazon River Basin in Brazil, November 2009; Website Administrator, Amazon Vision Ministries, a non-profit organization dedicated to serving the isolated areas of the Amazon Basin, 2008-2009


10. Briefly list the most recent professional development activities – None
1. Name – JOSE C. PRINCIPE

2. Education – Agregado, University of Aveiro, 1985; PhD, EE, University of Florida, 1979, MS, EE, University of Florida, 1974; BS, EE, University of Porto, 1972

3. Academic experience – Electrical & Computer Engineering, University of Florida, Distinguished Professor, 2002-date, full time; Electrical & Computer Engineering, University of Florida, BellSouth Professor, 1995-date, full time; Electrical & Computer Engineering, University of Florida, Professor, 1993-1995, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 1987-1993, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers


9. Briefly list the most important publications and presentations from the past five years – Principe J., “Information Theoretic Learning: Renyi’s Entropy and Kernel Perspectives,” Springer, 2010


10. Briefly list the most recent professional development activities – Attend an average of three conferences per year including IEEE conference on Wireless Communications & Networking, IEEE Workshop Machine Learning for Sig. Processing, IEEE Neural Engineering Conference, and the International Conference on Human Computer Interaction
1. Name – VLADIMIR A. RAKOV

2. Education – PhD, EE, Tomsk Polytechnic, 1983; MS, EE, Tomsk Polytechnic, 1977

3. Academic experience – Electrical & Computer Engineering, University of Florida, Professor, 1998-date, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 1991-1998, full time; High Voltage Research Institute at Tomsk Polytechnic, Russia, Director of Lightning Research Laboratory, 1984-1994, full time; High Voltage Research Institute at Tomsk Polytechnic, Russia, Senior Scientist, 1983-1984, full time; High Voltage Research Institute at Tomsk Polytechnic, Russia, Scientist, 1979-1983, full time; Electrical Engineering, Tomsk Polytechnic, Russia, Assistant Professor, 1977-1979, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Fellow, IEEE; Fellow, Institution of Engineering and Technology; American Geophysical Union; Fellow, American Meteorological Society; IEEE Power Engineering Society; IEEE EMC Society

7. Honors and awards – Life Member of the Advisory Committee of the Centre of Excellence on Lightning Protection; 2005 IET Fellow; 2004 AMS Fellow; 2003 IEEE Fellow; 2004 NLSI Annual Recognition Award; 2001 IEEE Power Engineering Society Surge Protective Devices Committee, Prize Paper Award; 2001 and 2007 University of Florida Research Foundation Professorship (three year award); 1999 ASEE Southeastern Section Medallion Certificate, Research Unit Award

8. Service activities (within and outside of the institution) – Faculty Development Committee, Member, 2006; Electromagnetics and Energy Systems Division, Chair, 2005-2010; Graduate Recruiting and Aid Committee, Member, 2005-date; Course Committees for EEL 3211 and EEL 3473, Member; Chair for EEL 3472; Technical Program Committee on Lightning for the biennial International Zurich Symposium on Electromagnetic Compatibility, Chairman, 1997-2009; CIGRE Working Group WG C4-407 “Lightning Parameters for Engineering Applications”, Convener, 2008-2011; CIGRE Working Group WG C4-410 “Lightning Striking Characteristics for Very High Structures”, Member, 2010-2013; National Fire Protection Association Committee on the Standard for the Installation of Lightning Protection Systems, Member, 2004–2010; Underwriters Laboratories Standards Technical Panel for Surge Protective Devices, Member, 2003-date; Scientific Committee of the International Conference on Lightning Physics and Effects and GROUND, Member, 2004-date; Steering Committee of the International Project on Electromagnetic Radiation from Tall Structure Lightning, Member (U.S. Representative), 2002-date; National Lightning Safety Institute, Board of Advisors, Member, 2002-date; Steering Committee of the International Symposium on Lightning Protection, Member, 1998-date; International Commission on Atmospheric Electricity, Member, 1999-date; Scientific Committee of the International Conference on
Lightning Protection (ICLP), Member, 2000-date; Editor or Associate Editor for three technical journals


10. Briefly list the most recent professional development activities – Guest Professorships: Doshisha University, Japan, June-July 2009; attended various conferences including annual International Zurich Symposium on Electromagnetic Compatibility and the International Symposium on Lightning Protection
1. Name – **KATHIE S. RUSSELL**

2. Education – MA, English, Florida Atlantic University, 2000; BA, English, Florida Atlantic University, 1996; Certificate, Teaching English as a Second Language, Cambridge University, 1992

3. Academic experience – institution, rank, title (chair, coordinator, etc. if appropriate), when (ex. 1990-1995), full time or part time University of Florida, Electrical & Computer Engineering, Associate Adjunct Professor, part time; Santa Fe College, Associate Adjunct Professor, 2004-date, part time; Nova Southeastern University, Adjunct Instructor, 2000-present, part time; American British Academy, Teacher, 1991-1994, full time

4. Non-academic experience – None

5. Certifications or professional registrations - None

6. Current membership in professional organizations - None

7. Honors and awards – Nominee, Adjunct Professor of the Year, Broward Community College

8. Service activities (within and outside of the institution) – Newsletter Editor, ECE Department, University of Florida, 2012-date

9. Briefly list the most important publications and presentations from the past five years – None

10. Briefly list the most recent professional development activities - None
1. Name – **ERIC M. SCHWARTZ**

2. Education – PhD, ECE, University of Florida, 1995; MS, ECE, University of Florida, 1989; BSEE, University of Florida, 1984; BSME, University of Florida, 1984

3. Academic experience – Electrical and Computer Engineering, University of Florida, Master Lecturer and Associate Director, Machine Intelligence Laboratory, 2008-date, full time; Electrical & Computer Engineering, University of Florida; Senior Lecturer, 2004-2008, full time; Electrical & Computer Engineering, University of Florida, Lecturer, 1998-2004, full time; Electrical & Computer Engineering, University of Florida, Visiting Assistant Professor, 1995-1998, full time

4. Non-academic experience – Allen-Bradley Corporation, Cleveland, Ohio, Engineer, Summer 1998, Developed fixed point programs for a digital signal processor used in machine controllers and developed some alternate control techniques for estimation of unknown parameters, full time

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers

7. Honors and awards – 2005-Anderson/CLAS Scholar Faculty Honoree; 2004-Anderson/CLAS Scholar Faculty Honoree; 2003-Anderson/CLAS Scholar Faculty Honoree; 2003-UF Teacher of the Year; 2003-College of Engineering Teacher of the Year; 2002-Anderson/CLAS Scholar Faculty Honoree; 2001-Anderson/CLAS Scholar Faculty Honoree; 2000-Anderson/CLAS Scholar Faculty Honoree

8. Service activities (within and outside of the institution) – Student Chapter Advisor, IEEE UF Student Chapter, Fall 2001-date; Member, College of Engineering Scholarship, Fellowship & Awards Committee, Fall 2006-date; Member, Computer Engineering ABET Committee, Spring 2006-present; Treasurer, IEEE Gainesville Section, 2002-date


10. Briefly list the most recent professional development activities – Attend various conferences.
1. Name – **JOHN M. SHEA**

2. Education – PhD, EE, Clemson University, 1998; MS, EE, Clemson University, 1995; BSEE, CprE, Clemson University, 1993

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2005-date, full time; Assistant Professor, Electrical & Computer Engineering, University of Florida, 1999-2005, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers

7. Honors and awards – 2004 Finalist for Eta Kappa Nu Outstanding Young Electrical Engineer; 1996 Fred W. Ellersick Award for Best Paper, IEEE Military Communications Conference

8. Service activities (within and outside of the institution) – ECE Faculty Search Committee, ECE Ph.D. Exam Committee; 2008-present Editor, IEEE Transactions on Wireless Communications; 2010-present Editor, IEEE Wireless Communications magazine; Reviewer for several publications; 2006,2007,2010,20011 Chair, IEEE Gainesville Section; 2010 Technical Program Committee Chair, IEEE Military Communications Conference, Unclassified Program; 2007-2009 Steering Committee Member, IEEE Military Communications Conference; Technical Program Committee: Member 2010 IEEE Global Communications Conference - Communications Theory Symposium, 2010 IEEE International Conference on Communications - Signal Processing for Communications Symposium, 2009 IEEE International Conference on Communications - Wireless Communications Symposium, 2008 IEEE International Conference on Communications - Cooperative Networks Workshop, 2007 IEEE Global Communications Conference Wireless - Communications Symposium


10. Briefly list the most recent professional development activities – Participated in many conferences, including IEEE Military Communications Conference, IEEE Global Communications Conference, IEEE International Conference on Communications, Annual Conference on Information Sciences and Systems, Allerton Conference on Communication, Control, and Computing
1. Name – RAMAKANT SRIVASTAVA

2. Education – PhD, Physics, Indiana University, 1973; MS, Physics, Indiana University, 1969; BS, Physics, Agra University, 1961

3. Academic experience – Electrical & Computer Engineering, University of Florida, Professor Emeritus, 2010-date, part time; Electrical & Computer Engineering, University of Florida, Professor, 1990-2010, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 1985-1990, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Optical Society of America

7. Honors and awards – Fulbright Lecture/Research Award, 2001; Fellow, Optical Society of America, 1999; UF Teacher of the Year, 1994; Outstanding Undergraduate Instructor Award, Department of Electrical Engineering, 1994; State of Florida Teaching Incentive Program (TIP) Award, 1993; Florida Blue Key Distinguished Faculty Award, 1991

8. Service activities (within and outside of the institution) – Served on various committees in the College and in the Department: Curriculum Committee, ABET Committee, various course committees, Awards Committee; UNDP Visiting Scientist, Research & Development Center, Telebras, Campinas (Brazil); UNDP Visiting Scientist, Indian Institute of Technology, New Delhi, India; Guest worker in the Optical Electronic Metrology Group in the Electromagnetic Technology Division, National Bureau of Standards Boulder Laboratories. Research on single-mode optical fiber characterization techniques

9. Briefly list the most important publications and presentations from the past five years – None

10. Briefly list the most recent professional development activities – None
1. Name – **GREGORY STITT**

2. Education – PhD, CSC, University of California-Riverside, 2007; BS, CSC, University of California-Riverside, 2000

3. Academic experience – Electrical & Computer Engineering, University of Florida, full time, 2007-date, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineering, Association of Computing Machinery

7. Honors and awards – Invited speaker at NSF Workshop on Interdisciplinary Challenges Beyond the Scaling Limits of Moore’s Law, August 2010

8. Service activities (within and outside of the institution) – Course development (EEL5934/4930 Reconfigurable Computing); PhD Exam Committee; Program committee of The 19th Annual IEEE International Symposium on Field-Programmable; Custom Computing Machines (FCCM); Program committee of the IEEE/ACM The International Conference on Hardware-Software Codesign and System Synthesis (CODES/ISSS); Co-chair of the International Conference on Engineering of Reconfigurable Systems and Algorithms (ERSA); Reviewer for the IEEE/ACM Design Automation Conference (DAC); Reviewer for Springer Design Automation for Embedded Systems (DAES); Reviewer for ACM Transactions on Design Automation of Electronic Systems (TODAES); Reviewer for IEEE Embedded Systems Letters (ESL); Reviewer for IEEE Transactions on Very Large Scale Integration Systems (TVLSI); Reviewer for ACM Transactions on Embedded Computing Systems (TECS); Reviewer for Journal of Computer Science and Technology (JCST); Reviewer for ACM International Conference on Compilers,Architectures, and Synthesis of Embedded Systems (CASES)

9. Briefly list the most important publications and presentations from the past five years –


10. Briefly list the most recent professional development activities – Attended various conferences including SIGPLAN conference on Languages, compilers, and tools for embedded systems
1. Name – **SCOTT THOMPSON**

2. Education – PhD, EE, University of Florida, 1992, MS, EE, University of Florida, 1988, BSEE, University of Florida, 1987

3. Academic experience – Electrical & Computer Engineering, University of Florida, Professor and Director of Advanced Devices and Semiconductor Processing Center, 2008-date, full time; Electrical & Computer Engineering, University of Florida, 2004-2008, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers


8. Service activities (within and outside of the institution) – EEE 4329 Course Committee Chair

9. Briefly list the most important publications and presentations from the past five years – 
   

10. Briefly list the most recent professional development activities – None
1. Name – MAURICIO TSUGAWA

2. Education – PhD, ECE, University of Florida, 2009; MS, EE, Universidade de São Paulo, 2001; BSEE, Universidade de São Paulo, 1998

3. Academic experience – Electrical & Computer Engineering, University of Florida, Research Scientist, 2009-date, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – None

7. Honors and awards – None


9. Briefly list the most important publications and presentations from the past five years –
“User-level Virtual Networking Mechanisms to Support Virtual Machine Migration Over Multiple Clouds,” Tsugawa, Maurício; Riteau, Pierre; Matsunaga, Andréa; and Fortes, José, IEEE International Workshop on Management of Emerging Networks and Services, Miami, Florida, 2010, pp. 588-592

“Characterizing user-level network virtualization: performance, overheads and limits,” Tsugawa, Maurício; and Fortes, José A.B., International Journal of Network Management, 2009


“CloudBLAST: Combining MapReduce and Virtualization on Distributed Resources for Bioinformatics Applications,” Matsunaga, Andréa; Tsugawa, Maurício; and Fortes, José A.B., Proceedings of the 4th IEEE International Conference on e-Science, p.222-229, 07-12 December, 2008.

“Getting on the Virtual Bus: By way of some common virtualization tools, agencies can make SOA their next stop for legacy apps,” Matsunaga, Andréa; Tsugawa, Maurício; and Fortes, José A.B., FEDTECH, v.5, number 2, p.31-32, May 2008

10. Briefly list the most recent professional development activities – None
1. Name – ALLEN TURNER

2. Education – PhD, Electrical Engineering, Purdue University, 1986; MS, Electrical Engineering, University of South Florida, 1980; BSEE, University of South Florida, 1977

3. Academic experience – Electrical & Computer Engineering, University of Florida, Lecturer, 2011-date, full time; Agricultural & Biological Engineering, University of Florida, Lecturer, 2003-2010, full time; Clemson University, Associate Professor, 1993-1996, full time; Clemson University, Assistant Professor, 1986-1993, full time

4. Non-academic experience – 2001-2003, Lockheed Martin Naval Electronics and Surveillance Systems, Rail Systems Architect, full time, wireless technologies that LM could utilize in their train control and defense projects; GeoFocus, Director of Engineering, 1996-2001, full time, Lead team that designed, developed and implemented tracking and data acquisition systems utilizing GPS and wireless technologies; Wright-Patterson Air Force Base, Consultant, 1987-1988, part-time, Developed and implemented a computer control system for a 5MW power supply; Senior Engineer, Trak Microwave, Involved in microwave amplifier design, mechanical design, parts procurement, component assessment, manufacturing, contract bids and computer programming

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineers.


8. Service activities (within and outside of the institution) – None

9. Briefly list the most important publications and presentations from the past five years – None

10. Briefly list the most recent professional development activities – None
1. Name – **MARTIN A. UMAN**

2. Education – PhD, Princeton University, 1961; MA, Princeton University, 1959; BSEE, Princeton University, 1957


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Fellow, American Geophysical Union, Fellow, American Meteorological Society, Life Fellow, Institute of Electrical and Electronic Engineers


8. Service activities (within and outside of the institution) – Departmental Faculty Search Committee 2009-2010; Search Committee for Dean of College of Engineering 2008; Faculty Development Committee 2007-2008, Sabbatical and Professional Development Leave Program Selection Committee, 2004 - date; Search Committee for the Intel Chair Search, 2004-date; Departmental Personnel Board, 2003-date; Distinguished Professor Candidate Evaluation Committee 2004 – date; UF Financial Resources Committee, June 1991-date

9. Briefly list the most important publications and presentations from the past five years – Book: The Art and Science of Lightning Protection, Cambridge University Press, 2008


M. V. Stapleton, D. M. Jordan, M. A. Uman, T. Morimoto, T. Ushio, and Z.I. Kawasaki


10. Briefly list the most recent professional development activities – Attended Asia-Pacific Symposium on Electromagnetic Compatability, Beijing, China; AGU Chapman Conference on the Effects of Thunderstorms and Lightning in the Upper Atmosphere; International Conference on High Voltage Engineering and Application; International Conference on Lightning Protection

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1. Name – ANT URAL

2. Education – PhD, EE, Stanford University, 2001; MS, EE, Stanford University, 1997; BSE, EE and Engineering Physics, Princeton University, 1995

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2010-date, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2003-2010, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None


7. Honors and awards – 2006-Junior Faculty Development Award, Southeastern Center for Electrical Engineering Education

8. Service activities (within and outside of the institution) – Reviewer for University of Florida Research and Graduate Programs Research Opportunity Incentive Fund; Member of Graduate Admissions and Aid Committee; Member of several course development committees; Referee for journals including Physical Review Letters Physical Review B, EPL, Applied Physics Letters, Journal of Applied Physics, Nanotechnology Nanoscale, Semiconductor Science and Technology New Journal of Physics, Current Applied Physics, Critical Reviews in Solid State and Material Sciences, and Carbon; Referee for Materials Research Society Proceedings; Referee for proposals for American Chemical Society Petroleum Research Fund


“Patterned growth of silicon oxide nanowires from iron ion implanted SiO$_2$ substrates,” Y. Choi, J. L. Johnson, and Ant Ural, *Nanotechnology* 20, 135307 (2009)


1. Name – TAN F. WONG

2. Education – PhD, ECE, Purdue University, 1997; MSEE, Purdue University, 1992; BS, EE, The Chinese University of Hong Kong, 1991

3. Academic experience – Electrical & Computer Engineering, University of Florida, Professor, 2009-date, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 2004-2009, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 1998-2004, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers

7. Honors and awards – 2003-IEEE Senior Member; 2000-Oak Ridge Associated Universities Ralph E. Power Junior Faculty Enhancement Award

8. Service activities (within and outside of the institution) – Departmental ABET Committee, Curriculum Committee, Budget Committee, Faculty Search; 2009-date Associate Editor, IEEE Signal Processing Letters; 2002-2006 Editor, Wideband & Multiple Access Wiress Systems, IEEE Transactions on Communications; 2002-2006 Editor-in-Chief, IEEE Transactions on Vehicular Technology; 2005-2010 Associate Editor, Journal of Wireless Communications & Mobile Computing; International Advisory Committee Member, Advanced Electronic Communications: Research & Education, Hellenic Aerospace Industry, Greece, 5/04–date; Member of Technical Program Committee for the IEEE Military Communications Conference (MILCOM ’10), San Jose, CA, Nov. 2010; Member of Technical Program Committee for the IEEE International Conference on Communications (ICC ’10) Wireless Communication Symposium, Cape Town, South Africa, May 2010

9. Briefly list the most important publications and presentations from the past five years – “Cooperative transmission in a wireless cluster based on flow management,” D. Chatterjee, T. F. Wong, and T. M. Lok, IEEE Transactions on Communications, Oct. 2010


10. Briefly list the most recent professional development activities – Attended various conferences including the IEEE Military Communications Conference, IEEE International Conference on Communications; IEEE Global Communications Conference; and International Communications and Networking Conference
1. Name – DAPENG “OLIVER” WU

2. Education – PhD, ECE, Carnegie Mellon University, 2003; ME, EE, Beijing University of Posts & Telecommunications, 1997; BE, EE, Huazhong University of Science & Technology, 1990

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2008-date, full time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2003-2008, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers


8. Service activities (within and outside of the institution) – Member of the steering committee for strategic planning for the information technology research area, College of Engineering, University of Florida Oct. 2009–date; Mentor, University Minority Mentor Program, University of Florida Aug. 2008–July 2009; Mentor, Student Science Training Program (for high school students), University of Florida May 2008–Aug. 2008; Member of Search Committee for Associate Dean for Research and Graduate Programs, College of Engineering, University of Florida 2008; Member of Graduate Recruiting and Admissions Committee 2007–date; Member of ABET Committee for EGN 1935 (ECE Adventures) Aug. 2007–date; Member of ABET Committee for EEL 3112 (Circuits 2) Aug. 2004–date; Member of Awards Committee Aug. 2004–date; Member of Faculty Search Committee 2005–2006; Member of Graduate Admissions and Aid Committee 2003–2004; Founding Editor-in-Chief for Journal of Advances in Multimedia 2006–2008; Associate Editor for IEEE Transactions on Wireless Communications 2006–date; Associate Editor for IEEE Transactions on Circuits and Systems for Video Technology 2006–date; Associate Editor for IEEE Transactions on Vehicular Technology 2004–2007; Associate Editor for International Journal of Ad Hoc and Ubiquitous Computing, INDERSCIENCE, 2004–date; Guest Editor for Journal of Visual Communication and Image Representation (JVCI), Special Issue on Network Technologies for Emerging Broadband Multimedia Services, 2008; Member, EDICS committee for IEEE Transactions on Circuits and Systems for Video Technology 2007–date; Technical Program Committee Chair for IEEE INFOCOM 2012, Orlando, FL, USA, March 25–March 30, 2012; Publicity Chair, IEEE Workshop on Signal Processing Advances in
9. Briefly list the most important publications and presentations from the past five years –


“Image Denoising by Bounded Block Matching and 3D Filtering,” Q. Chen and D. Wu, *Signal Processing*, vol. 90, no. 9, pp. 2778-2783, September 2010


10. Briefly list the most recent professional development activities – Attended various conferences including SPIE Defense & Security Symposium; IEEE International Conference on Communications; SIAM International Conference on Data Mining; IEEE Global Telecommunications Conference
1. Name – WENHSING WU

2. Education – MS, EE, University of California-Los Angeles, 1991; BS, Electrophysics, National Chiao Tung University, 1988


4. Non-academic experience – Voice Quality Test Engineer, Lucent Technologies, Mobility Solutions, Whippany, NJ, Performed test and analysis of voice quality of the VOCODER in wireless communication network systems, 2000-2003, full time; Test Engineer, AT&T/Lucent Technologies, Network Wireless Systems, Mt. Olive, NJ, Improve the yield and reduce the test time for functional test of circuit pack (TN1703 Synchronized Clock and Tone, SCT) for CDMA Minicell/Series II base stations. Participate in the design of new products, 1997-2000, full time; FMA/Supplier Quality Engineer, AT&T/Lucent Technologies, Network Wireless Systems, Mt. Olive, NJ, Analyzed failure mechanism in circuit board or system level. Generated failure analysis reports and e-mails to share the information amount engineers; Initiated feedback mechanisms both in the manufacturing area and to various power amplifier suppliers, to identify root cause of quality problems, 1995-1997, full time; Failure Analysis Engineer, Silicon Systems Inc., Tustin, CA, Performed failure analysis on bipolar/CMOS/BiCMOS analog and digital ICs to identify the specific mechanism causing individual reliability test failures; Installed test systems; Performed circuit analysis and diagnosed, navigated through integrated circuits, 1992-1995, full time; IC Product Engineer, Electronic Research and Service Organization, Industrial Technology Research Institute, Hsinchu, Taiwan, 1988-1989, full time

5. Certifications or professional registrations – None

6. Current membership in professional organizations – None

7. Honors and awards – None

8. Service activities (within and outside of the institution) – None


10. Briefly list the most recent professional development activities – None
1. Name – **HUIKAI XIE**

2. Education – PhD, ECE, Carnegie Mellon University, 2002, MS, EE, Tufts University, 1998, MS, EE, Beijing Institute of Technology, 1992; BS, EE, Beijing Institute of Technology, 1989

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor, 2007-date, full time; Peking University, Beijing, China, Guest Professor, 2007-date, part time; Biomedical Engineering, University of Florida, Affiliate faculty, 2005-date, part time; Member, UF Shands Cancer Center, 2005-date, part time; Electrical & Computer Engineering, University of Florida, Assistant Professor, 2002-2007, full time; Lecturer, Institute of Microelectronics, Tsinghua University, Beijing, China, 1995-1996, full time; Research faculty, Institute of Microelectronics, Tsinghua University, Beijing, China, 1992-1996, full time


5. Certifications or professional registrations – None

6. Current membership in professional organizations – American Society of Engineering Education; International Society for Optical Engineering; Optical Society of America; Senior Member, the Institute of Electrical and Electronics Engineers


9. Briefly list the most important publications and presentations from the past five years –

“3D In Vivo optical coherence tomography based on a low-voltage, large-scan-range 2D MEMS mirror,” J. Sun, S. Guo, L. Wu, L. Liu, S.-W. Choe, B. S. Sorg, and H. Xie, Optics Express, vol. 18, 12065-12075 (2010)


10. Briefly list the most recent professional development activities – Attended various conferences including Applied Power Electronics Conference and Exposition; International Conference on Microelectromechanical Systems; Asia Communications and Photonics Conference and Exhibition; Nanoelectronic Devices for Defense & Security Conference
1. Name – **YONG-KYU YOON**

2. Education – PhD, ECE, Georgia Institute of Technology, 2004; MS, ECE, New Jersey Institute of Technology, 1999; MS, EE, Seoul National University, 1994; BS, Seoul National University, 1992

3. Academic experience – Department of Electrical and Computer Engineering, University of Florida, Gainesville, Florida, Associate Professor, 2010-date, full time; Department of Electrical Engineering, Stony Brook University, The State University of New York, Stony Brook, New York, Visiting Assistant Professor, 2007, full time; Department of Electrical Engineering, University at Buffalo, The State University of New York, Buffalo, New York, Assistant Professor, 2006-2010, full time

4. Non-academic experience – Staff Engineer for VLSI and MEMS design, Microsystems, Newark, New Jersey, 1998-1999, full time

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical & Electronic Engineers

7. Honors and awards – 2009-Young Investigator Award, University of Buffalo; 2008-Faculty Early Career Development (CAREER) Award, National Science Foundation; 2007-Best Poster Paper, The 57th ECTC Conference; 2006 Award of Excellence in Graduate Polymer Science Research, the 231st American Chemical Society; 2003-Student Paper Contest Award (2nd Place), IEEE International Microwave Symposium; 2003-Student Poster Competition Award (2nd Place), Industrial Advisory Board Meeting at Georgia Tech Packaging Research Center


9. Briefly list the most important publications and presentations from the past five years – “Microworld: United We Stand,” Jungkwun Kim and Yong-Kyu Yoon, Lab on a Chip, Cover image (p. 669), vol. 10, no. 6, pp. 681-682, March 21, 2010

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10. Briefly list the most recent professional development activities – International Conference of IEEE Micro Electro Mechanical Systems; American Vacuum Society’s International Symposium; International Conference on Miniaturized Systems for Chemistry and Life Sciences; IEEE International Microwave Symposium
1. Name – HENRY ZMUDA

2. Education – PhD, EE, Cornell University, 1984; MS, EE, Cornell University, 1982; BE, Stevens Institute of Technology, 1979

3. Academic experience – Electrical & Computer Engineering, University of Florida, Associate Professor and Undergraduate Coordinator, 2010-date, full time; Electrical & Computer Engineering, University of Florida, Associate Professor, 2006-2009, full time; Associate Professor and Program Coordinator of the Graduate Engineering & Research Center, Eglin Air Force Base, Electrical & Computer Engineering, University of Florida, 1995-2006, full time; Associate Professor, Electrical Engineering, Stevens Institute of Technology, 1988-1995, full time; Assistant Professor, Electrical Engineering, Stevens Institute of Technology, 1983-1988, full time; Electrical Engineering, Cornell University, Visiting Professor, Summer 1985, full time; Electrical Engineering, Cornell University, 1982-1984, Lecturer, full time

4. Non-academic experience – None

5. Certifications or professional registrations – None

6. Current membership in professional organizations – Institute of Electrical and Electronic Engineers; Directed Energy Professional Society; IEEE Transactions on Microwave Theory and Techniques; American Society for Engineering Education; The Directed Energy Professional Society; HKN, the Electrical Engineering Honor Society, Cornell Chapter; Sigma Xi, the National Research Honor Society; Fellowship of Catholic Scholars

7. Honors and awards – None

8. Service activities (within and outside of the institution) – Departmental Curriculum Committee; Departmental ABET Coordinator; Ordained deacon for the Roman Catholic Church assigned to the UF Catholic Student Center


10. Briefly list the most recent professional development activities – Participated in the National Effective Teaching Institute Workshop sponsored by the American Society for Engineering Education; attended the SPIE Defense and Security Symposium and the Annual Tri-Service Radar Symposium
Appendix C – Equipment

A. CISE Departmental Equipment

See Criterion 7, Section B, for a description of computer equipment provided by the CISE department for the support of the Computer Engineering program.

B. ECE Departmental Equipment

In addition to the computer equipment listed in Criterion 7, Section B, the ECE Department owns as multiple copies of the items listed below as is necessary to provide an educationally sound laboratory experience for the students.

- LPKF ProtoLaser S PCB Prototyping System with Laser
- LPKF ProtoMat S62 Circuit Board Plotter / Milling Machines
- LabVolt Electromechanical Training Systems
- Agilent Spectrum Analyzers
- Agilent Function / Waveform Generators
- Tektronix Oscilloscopes and Curve Tracers
- Agilent Mixed Signal Oscilloscopes
- Agilent Triple Output DC Power Supplies
- National Instruments ELVIS II and ELVIS Systems
Appendix D – Institutional Summary

Programs are requested to provide the following information.

The Institution

The University of Florida, Gainesville, FL 32611

J. Bernard Machen, President, University of Florida
Dr. Cammy Abernathy, Dean, College of Engineering

Office of Institutional Planning and Research
University of Florida
P.O. Box 113115
355 Tigert Hall
Gainesville, FL 32611-3115
Phone: (352) 392-0456
Fax: (352) 392-8774
Email: ufdata@aa.ufl.edu

Organizations by which the institution is now accredited and the dates of the initial and most recent accreditation evaluations:

- The University of Florida is accredited by the Commission on Colleges of the Southern Association of Colleges and Schools to award bachelor, master, specialist and engineer, as well as doctoral and professional degrees. The last review was in 2003. A copy of the self-study is located on the web at: http://fora.ua.ufl.edu/Provost/Pages/2003-SACS-Steering-Committee/SACSCOCSelf-StudyCommitteeReports

- The program of study in Computer Engineering that leads to the degree of Bachelor of Science in Computer Engineering resides in the Department of Computer and Information Science and Engineering (which is responsible for ABET accreditation) and in the Department of Electrical and Computer Engineering. The program is accredited by ABET. Our last accreditation evaluation was October 2006.

Type of Control

Public, comprehensive, land-grant, research university.
Educational Unit

The Computer Engineering program is co-located in the Departments of Computer and Information Science and Engineering, and Electrical and Computer Engineering. The administrative chain of responsibility is:

- Dr. Herman Lam, ECE, Computer Engineering Program Director.
- Dr. Paul Gader, interim Chair, Department of Computer and Information Science and Engineering
- Dr. John Harris, Chair, Department of Electrical and Computer Engineering
- Dr. Cammy Abernathy, Dean, College of Engineering.
- Dr. Joseph Glover, Provost and Senior Vice President for Academic Affairs
- Dr. J. Bernard Machen, President

Academic Support Units

Please refer to the separate Institution Profile Volume provided for this information.

- Dr. James Keesling, Chairman, Department of Mathematics
- Dr. Michael Daniels, Chairman, Department of Statistics
- Dr. John Yelton, Chairman, Department of Physics
- Dr. Daniel R. Talham, Chairman, Department of Chemistry
- Dr. Alice Harmon, Chairman, Department of Biology

Non-academic Support Units

- Dr. Judith Russell - Dean of University Libraries
- Dr. Fedro Zazueta, Professor & Associate CIO, UF Academic Technology Office
- Dr. Heather White, Interim Director, UF Career Resource Center

There are a number of units that support the engineering academic programs. In the college, the Office of Academic Affairs (AA) provides an overall direction for the college’s curricula. The Office of Student Affairs (OSA) advises and counsels students, maintains student records, and provides an interface with other supporting colleges.

Within the OSA, a variety of services is provided to students, with emphasis on freshmen and sophomores and transfer students from community colleges and other institutions. The OSA is staffed by eight (8) faculty and staff members under the direction of the Associate Dean of Student Affairs. It coordinates all student advising within the College of Engineering, and serves as liaison between academic departments within the college and with university-wide student services and facilities. The OSA is also responsible for developing and implementing other student support services including career/lifestyle counseling, success workshops, study halls, tutoring, and mentoring. The OSA informs students of available educational resources including co-op and internship opportunities,
scholarships, and tuition waivers. The OSA is also engaged in programs targeted at minorities and women, pre-college outreach activities, and recruitment and retention of quality students.

The College of Engineering has established a number of programs to assist incoming students. These include:

- Successful Transition through Enhanced Preparation for Undergraduate Students (STEPUP)
- Engineering Freshman Transition Program (EFTP)
- Transfer Student Transition Program (TSTP)

EFTP provides assistance in helping students acclimate and prepare for the fall term as engineering students. It is a residential program open to all first-year engineering students. Selected students arrive on campus during the summer B session to get an opportunity to discover the campus prior to the arrival of the rest of the UF students. EFTP is comprised as an intense two-credit course offered during summer B to assist students in adjusting to the level, speed and style of college instruction in foundation courses (Calculus & Chemistry). Additionally, students also receive the following benefits:

- Orientation of Engineering resources
- Student Success Workshops
- Career Resource Center presentations
- Industry representative presentations
- Team Building Project
- Study Groups

EFTP continues throughout the fall and spring semesters of the student's first year.

Along with their regular classes, selected students spend the fall and spring semester’s acclimating to the demands of being engineering students with the help of peer mentors. Mentors communicate with their assigned students each week to assist in answering questions and offering resources to help make every student successful in engineering, allowing the mentors to not only point out resources but also provide tailored support to each student.

The Management Information Systems (MIS) section of the Engineering Fiscal & Personnel Office makes available comprehensive and high quality services to meet the information technology needs of the College and its students. The office provides technical and administrative support, network services, information technology planning and many other services needed to support the college’s computers and computer laboratories.
The Engineering Fiscal and Personnel Office provides support for student employment and processes fellowships, scholarships, and assistantships. The Office of Engineering Research offers research support for undergraduate and graduate students. At the university level, a number of organizations assist engineering and other students with financial aid, counseling, career planning, health-related, and other services [UF Student Support Services].

Credit Unit

The ABET self-study template states:

*It is assumed that one semester or quarter credit normally represents one class hour or three laboratory hours per week. One academic year normally represents at least 28 weeks of classes, exclusive of final examinations. If other standards are used for this program, the differences should be indicated.*

We concur. Each class period is normally 50 minutes each.
Table D-1. Program Enrollment and Degree Data

Computer Engineering

<table>
<thead>
<tr>
<th>Academic Year</th>
<th>Enrollment Year</th>
<th>Total Undergrad</th>
<th>Total Grad</th>
<th>Degrees Awarded</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>1st</td>
<td>2nd</td>
<td>3rd</td>
<td>4th</td>
</tr>
<tr>
<td>2011-2012</td>
<td>FT</td>
<td>77</td>
<td>50</td>
<td>87</td>
</tr>
<tr>
<td></td>
<td>PT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2010-2011</td>
<td>FT</td>
<td>68</td>
<td>54</td>
<td>105</td>
</tr>
<tr>
<td></td>
<td>PT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2009-2010</td>
<td>FT</td>
<td>79</td>
<td>61</td>
<td>106</td>
</tr>
<tr>
<td></td>
<td>PT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2008-2009</td>
<td>FT</td>
<td>78</td>
<td>62</td>
<td>98</td>
</tr>
<tr>
<td></td>
<td>PT</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>2007-2008</td>
<td>FT</td>
<td>95</td>
<td>65</td>
<td>101</td>
</tr>
<tr>
<td></td>
<td>PT</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Give official fall term enrollment figures (head count) for the current and preceding four academic years and undergraduate and graduate degrees conferred during each of those years. The "current" year means the academic year preceding the fall visit. (*) data for Spring 2012 not yet available.

FT--full time
PT--part time
Table D-2a. Personnel: CISE

Computer Engineering

Year*: Fall 2011

<table>
<thead>
<tr>
<th>HEAD COUNT</th>
<th>FTE²</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>FT</td>
</tr>
<tr>
<td>Administrative³</td>
<td>3</td>
</tr>
<tr>
<td>Faculty (tenure-track)</td>
<td>27</td>
</tr>
<tr>
<td>Other Faculty (excluding student Assistants)</td>
<td>8</td>
</tr>
<tr>
<td>Student Teaching Assistants</td>
<td>7</td>
</tr>
<tr>
<td>Student Research Assistants</td>
<td>29</td>
</tr>
<tr>
<td>Technicians/Specialists</td>
<td>5</td>
</tr>
<tr>
<td>Office/Clerical Employees</td>
<td>9</td>
</tr>
<tr>
<td>Others⁴</td>
<td></td>
</tr>
</tbody>
</table>

Report data for the program being evaluated.

1 Data on this table should be for the fall term immediately preceding the visit. Updated tables for the fall term when the ABET team is visiting are to be prepared and presented to the team when they arrive.

2 For student teaching assistants, 1 FTE equals 20 hours per week of work (or service). For undergraduate and graduate students, 1 FTE equals 15 semester credit-hours (or 24 quarter credit-hours) per term of institutional course work, meaning all courses — science, humanities and social sciences, etc. For faculty members, 1 FTE equals what your institution defines as a full-time load.

3 Persons holding joint administrative/faculty positions or other combined assignments should be allocated to each category according to the fraction of the appointment assigned to that category.

4 Specify any other category considered appropriate, or leave blank.
Table D-2b. Personnel: ECE

Computer Engineering

Year¹: Fall 2011

<table>
<thead>
<tr>
<th>Category</th>
<th>HEAD COUNT</th>
<th>FTE²</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>FT</td>
<td>PT</td>
</tr>
<tr>
<td>Administrative³</td>
<td>39</td>
<td>0</td>
</tr>
<tr>
<td>Faculty (tenure-track)</td>
<td>39</td>
<td>0</td>
</tr>
<tr>
<td>Other Faculty (excluding student Assistants)</td>
<td>9</td>
<td>16</td>
</tr>
<tr>
<td>Student Teaching Assistants</td>
<td>37</td>
<td>0</td>
</tr>
<tr>
<td>Student Research Assistants</td>
<td>183</td>
<td>0</td>
</tr>
<tr>
<td>Technicians/Specialists</td>
<td>9</td>
<td>0</td>
</tr>
<tr>
<td>Office/Clerical Employees</td>
<td>25</td>
<td>0</td>
</tr>
<tr>
<td>Others⁴</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Report data for the program being evaluated.

² Data on this table should be for the fall term immediately preceding the visit. Updated tables for the fall term when the ABET team is visiting are to be prepared and presented to the team when they arrive.

² For student teaching assistants, 1 FTE equals 20 hours per week of work (or service). For undergraduate and graduate students, 1 FTE equals 15 semester credit-hours (or 24 quarter credit-hours) per term of institutional course work, meaning all courses — science, humanities and social sciences, etc. For faculty members, 1 FTE equals what your institution defines as a full-time load.

³ Persons holding joint administrative/faculty positions or other combined assignments should be allocated to each category according to the fraction of the appointment assigned to that category.

⁴ Specify any other category considered appropriate, or leave blank.
Signature Attesting to Compliance

By signing below, I attest to the following:

That _______________________ (Name of the program(s)) has conducted an honest assessment of compliance and has provided a complete and accurate disclosure of timely information regarding compliance with ABET’s *Criteria for Accrediting Engineering Programs* to include the General Criteria and any applicable Program Criteria, and the ABET *Accreditation Policy and Procedure Manual*.

________________________________
Dean’s Name (As indicated on the RFE)

________________________________   ________________
Signature                               Date