

# Correct resolution rendering of trimmed spline surfaces

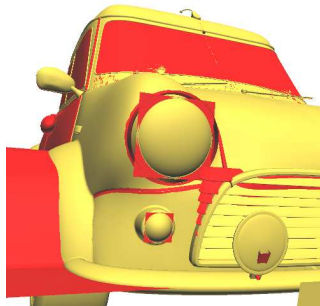
Ruijin Wu and Jörg Peters

University of Florida

SPM 2014

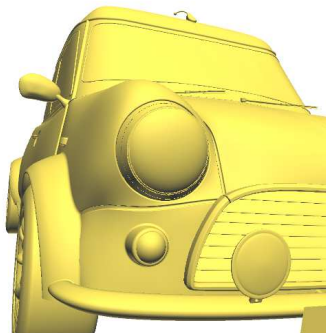
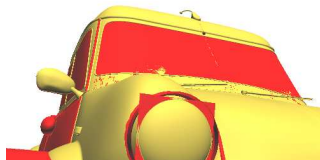
# Industrial Workflow: Overfit + Trim

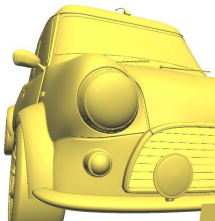
- Capture shape w/o considering other surfaces



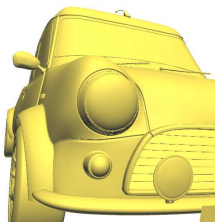
# Industrial Workflow: Overfit + Trim

- Capture shape w/o considering other surfaces
- Trim the surfaces back to match constraints



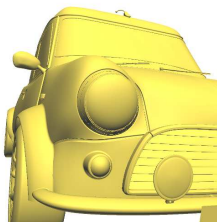


Practice



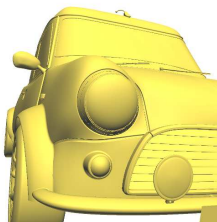
## Practice

- **Accuracy** currently: predefined triangulation level



## Practice

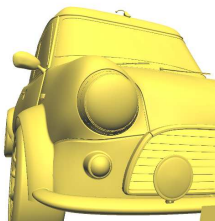
- **Accuracy** currently: predefined triangulation level
- **Latency** adjust trim, modeler recomputes triangles



## Practice

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## Theory



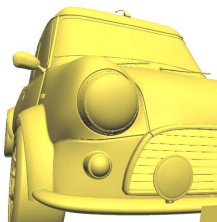
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## Theory

- **Accuracy** domain  $\leftrightarrow$  projected image resolution





## Practice

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## Theory

- **Accuracy** domain  $\leftrightarrow$  projected image resolution
- **Latency** Lean parallel data structures

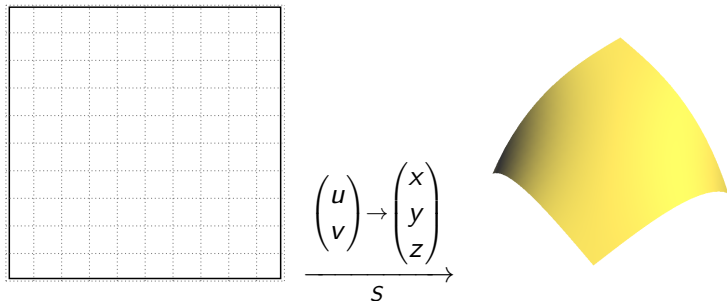
# Goal

- High precision rendering
- Interactive frame rate



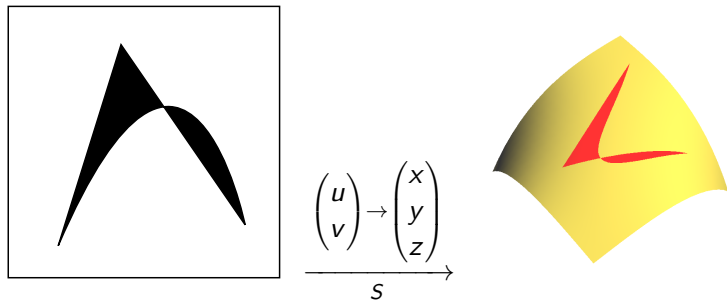
- 1 Basics: Rendering Trimmed Surfaces
- 2 Earlier approaches
- 3 Correct resolution trimming
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- 5 Leveraging Correct Tessellation and the Graphics Pipeline
- 6 Comparisons
- 7 Summary

# Surface and Trim Curve



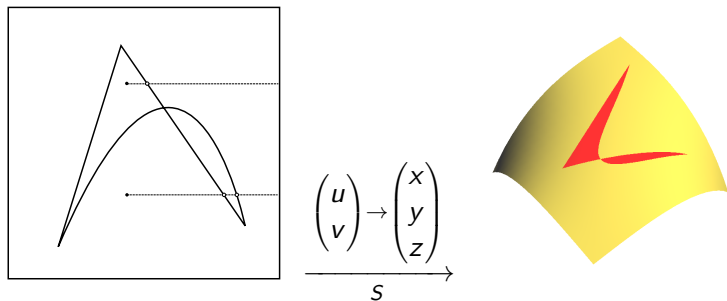
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# Surface and Trim Curve



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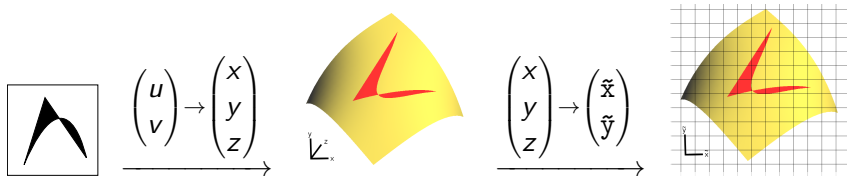
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- “inside” is determined by ray test *in the  $uv$  domain*.

# Outline

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# Ray trace or trim texture?

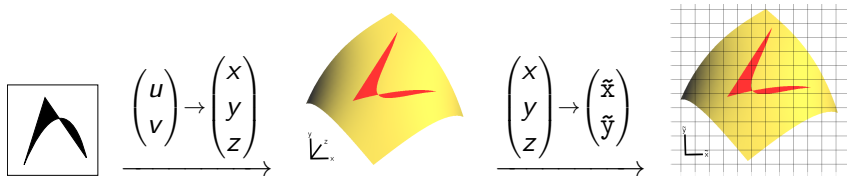
Map in from  $(u, v)$  to  $(\tilde{x}, \tilde{y})$ :





# Ray trace or trim texture?

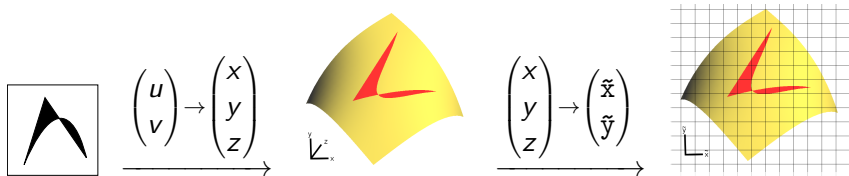
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Assume: Each pixel  $P(x(u,v))$  knows its  $uv$ .

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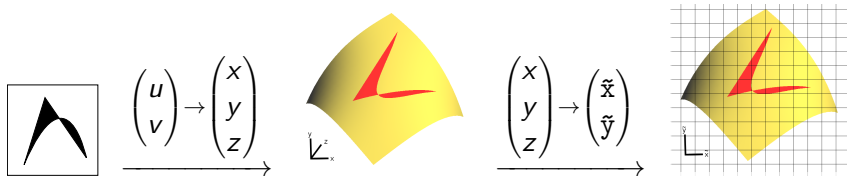


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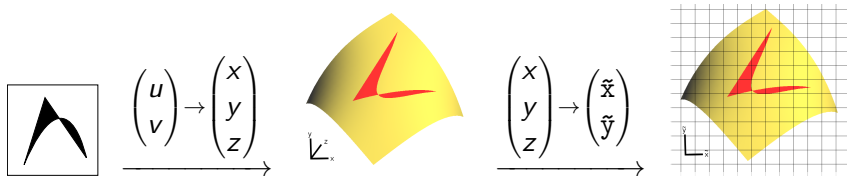


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+ precise

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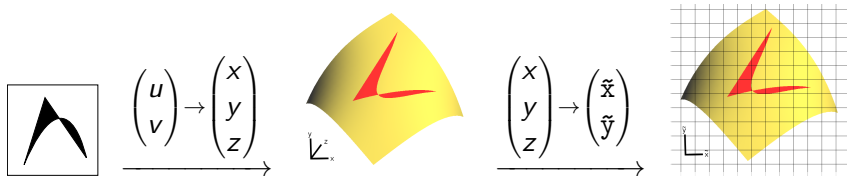


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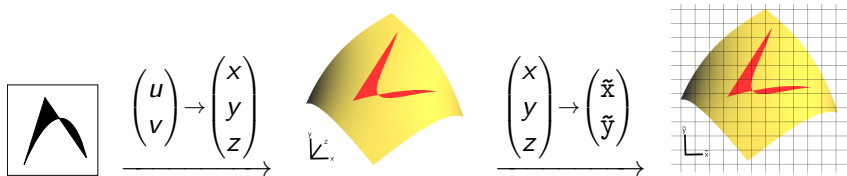


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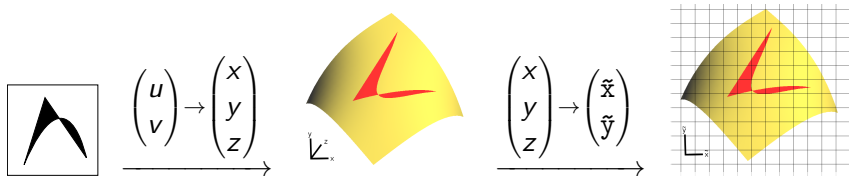


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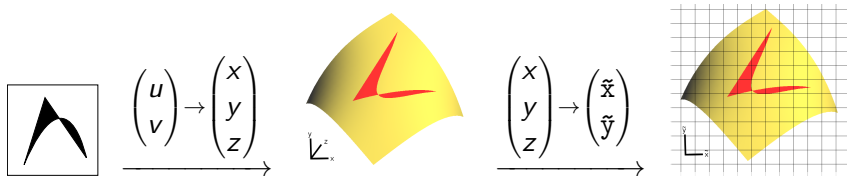


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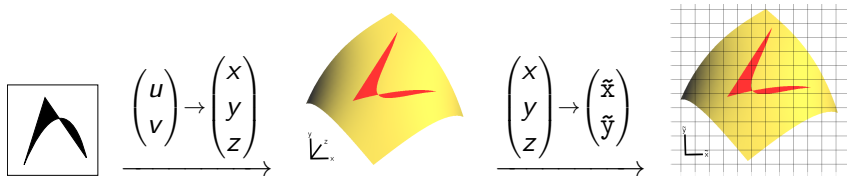
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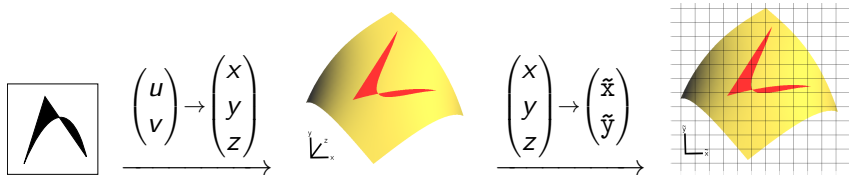


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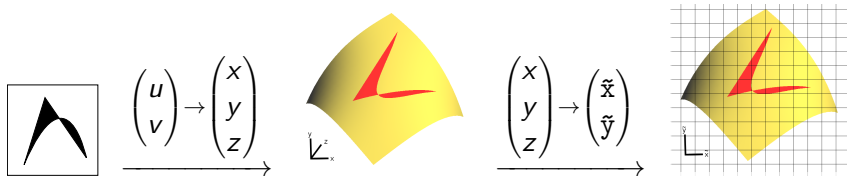


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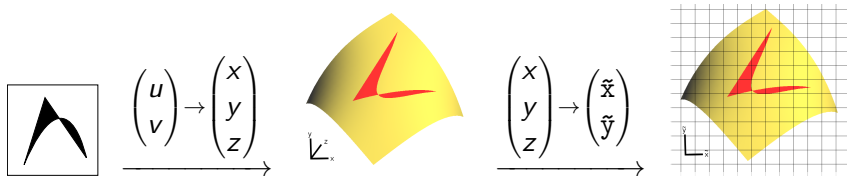


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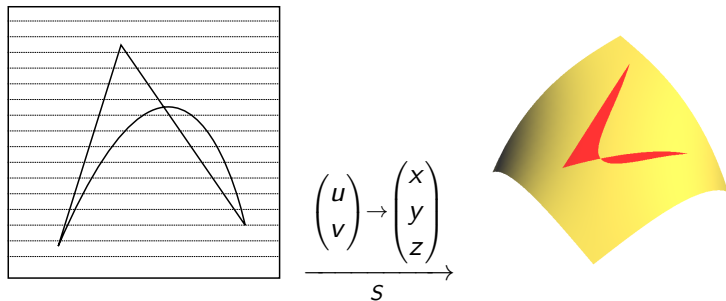
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  - resolution - (re)computation (parallel write), - space
- hybrid = **robust + fast + sufficient precision** ?

# Outline

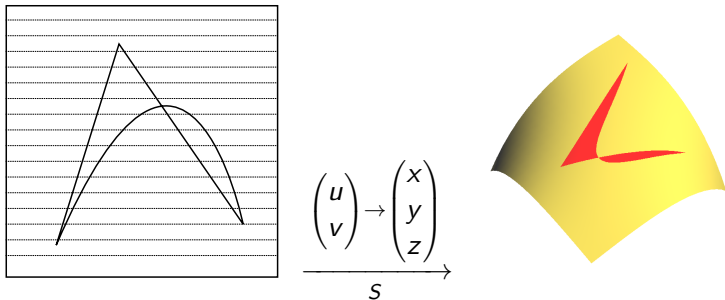
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# Predicting Correct Resolution



- Surface piece  $S$  maps the unit rectangle to 3-space.
- Trim curves define and restrict the domain of the surface.
- “inside” is determined by ray test *in the  $uv$  domain*.
- ‘fat’ rays – how fat?

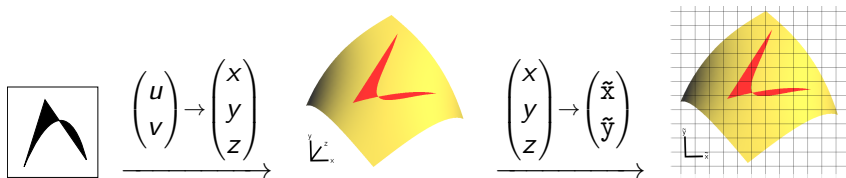
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- ‘fat’ rays – how fat?      ‘simplify’ trim curve – how simple?

# Predicting correct resolution

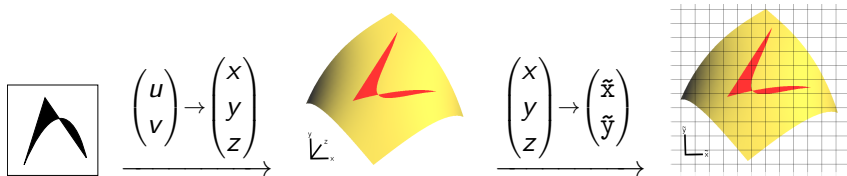
Map in from  $(u, v)$  to  $(\tilde{x}, \tilde{y})$ :





# Predicting correct resolution

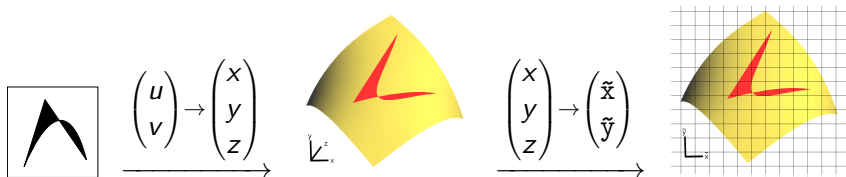
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'pull back' pixel grid    need **distinct pre-images**

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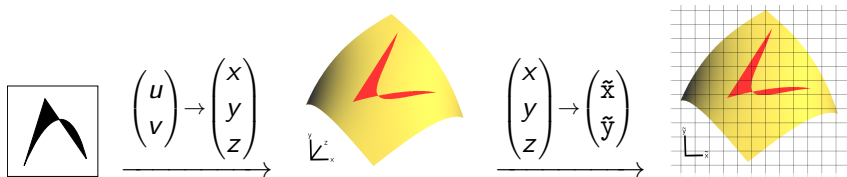
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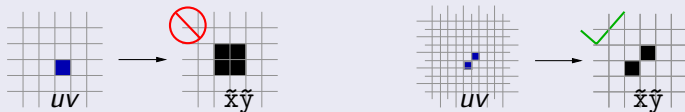
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## Correct Resolution



# Math 101: Determine the $v$ -scan line density

The screen space distance between two  $v$ -scan lines is:

$$|\tilde{\mathbf{x}}(u, v) - \tilde{\mathbf{x}}(u, v + h)| = h |\tilde{\mathbf{x}}_v(u, v^*)|, \quad \tilde{\mathbf{x}}_v := \frac{\partial \tilde{\mathbf{x}}}{\partial v}.$$

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If, for all  $v \in V_i$  and the  $v$ -scan line *spacing*  $h > 0$ ,

$$\begin{aligned} h \rho_i(v) &< 1, \\ \rho_i(v) &:= \max \left\{ \sup_u |\tilde{\mathbf{x}}_v(u, v)|, \sup_u |\tilde{\mathbf{y}}_v(u, v)| \right\}, \end{aligned}$$

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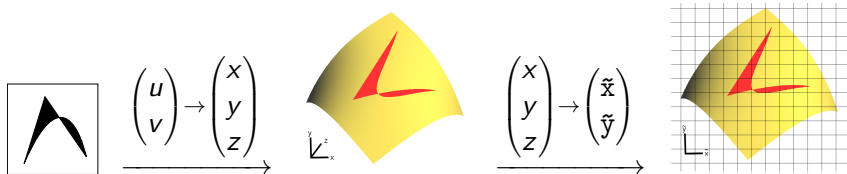
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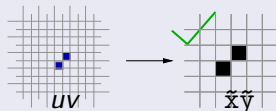
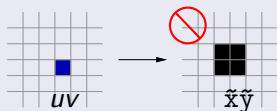
then the  $\tilde{\mathbf{x}}$ -distance between the screen images of the two  $v$ -scan lines  $\tilde{\mathbf{x}}(u, v_j)$  and  $\tilde{\mathbf{x}}(u, v_j + h)$  is *less than a pixel* and so is the  $\tilde{\mathbf{y}}$ -distance.

# Summary: Predicting correct resolution

Map in from  $(u, v)$  to  $(\tilde{x}, \tilde{y})$ :



## Correct Resolution



## $v$ -scan line spacing

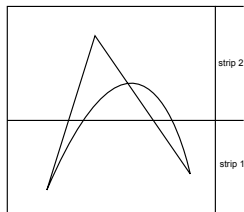
$$h < 1/\rho_i(v) \quad \rho_i(v) := \max\left\{\sup_u |\tilde{x}_v(u, v)|, \sup_u |\tilde{y}_v(u, v)|\right\}$$

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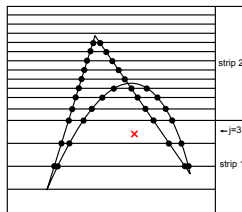


# Two-level scanline hierarchy



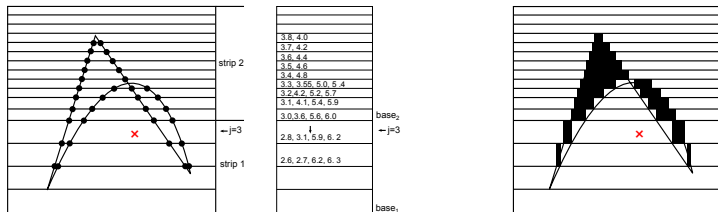
- Uniformly partition the domain into  $n_V$   $v$ -strips. ( First level )

# Two-level scanline hierarchy



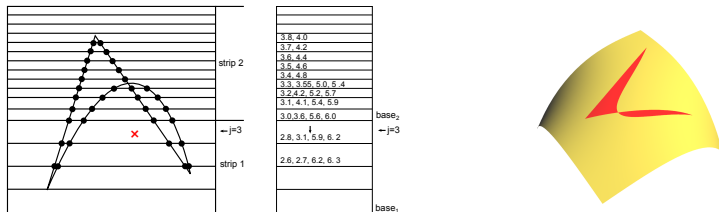
- Uniformly partition the domain into  $n_V$   $v$ -strips. ( First level )
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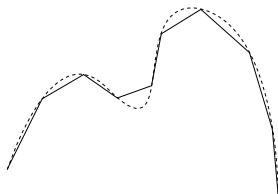
- Uniformly partition the domain into  $n_V$   $v$ -strips. ( First level )
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- Store the  $u$ -coordinate of intersections for each  $v$ -scan line.
- Trim test: Look up position of each pixel's pre-image in the table.

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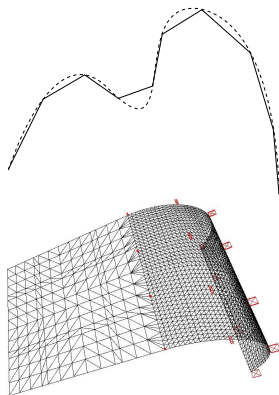
# Tessellation of curve and surface

- The trim curve is tessellated into line segments.



# Tessellation of curve and surface

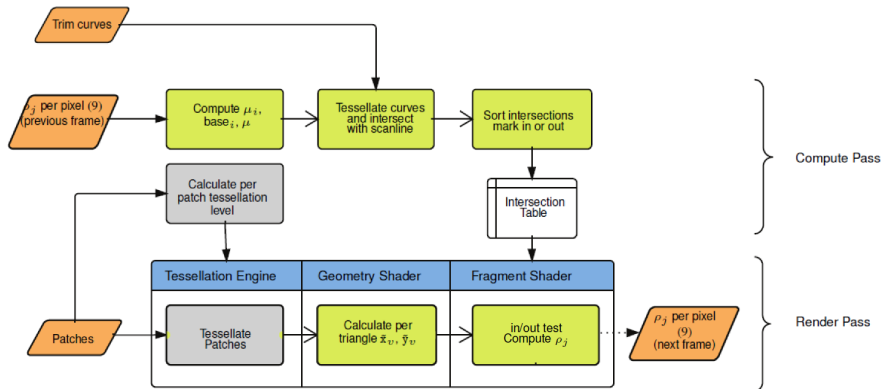
- The trim curve is tessellated into line segments.
- The surface is tessellated into triangles.



Subdividable linear efficient function envelopes = SLEFE

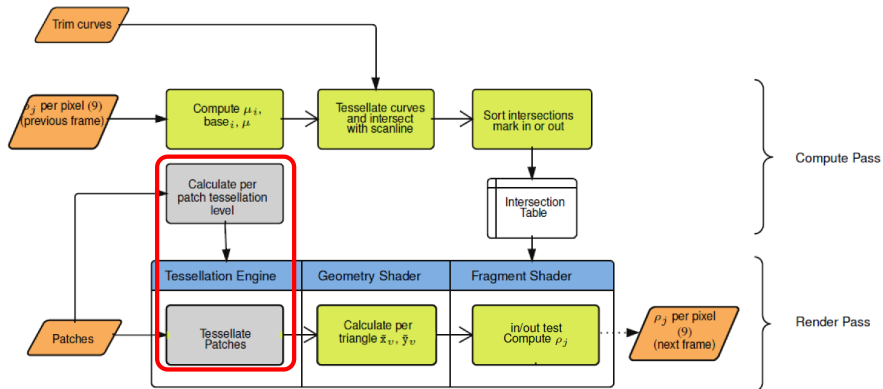
A tight bound of the deviation between the curve/surface and its piecewise linear approximation [Yeo et al 2012].

# Using the GPU pipeline



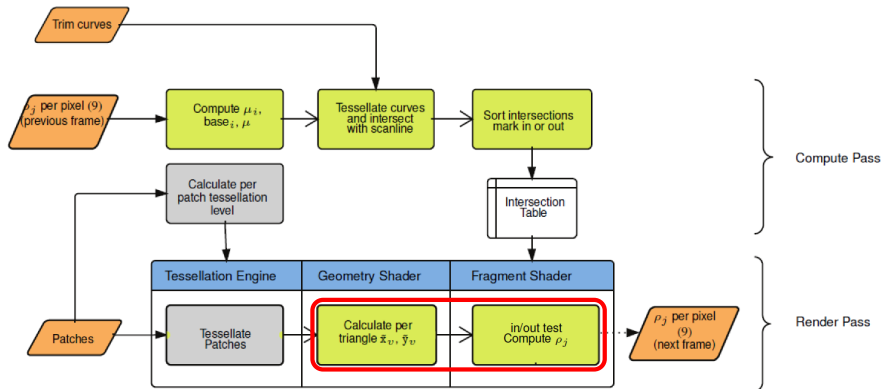


# Using the GPU pipeline



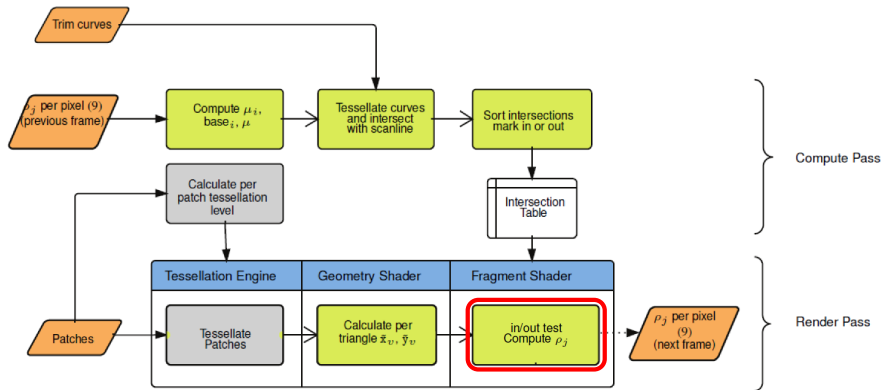
- iPass algorithm [Yeo et al. 2012] for surface rendering  
[https://bitbucket.org/surflab/ipass\\_gl4](https://bitbucket.org/surflab/ipass_gl4)

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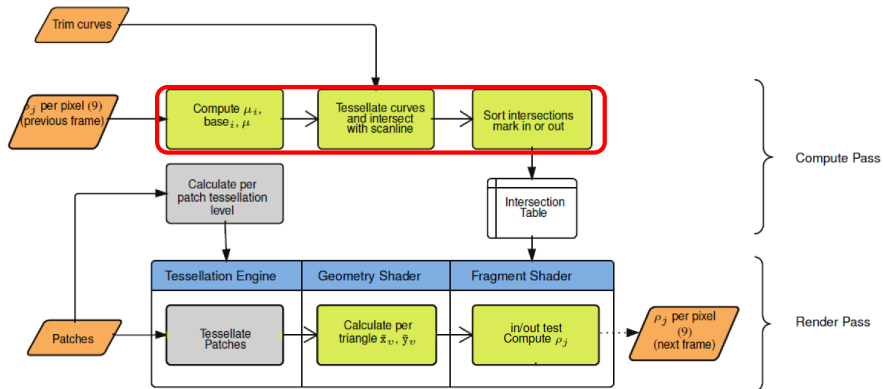
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- in/out test per pixel

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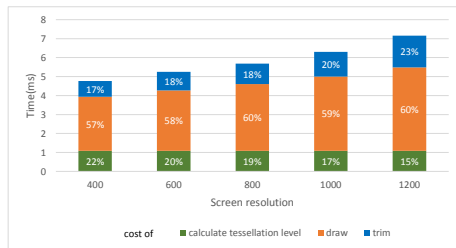
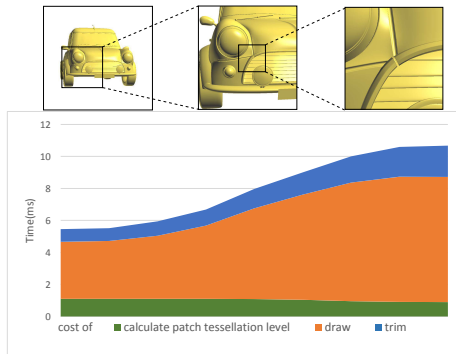
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- Build  $u$ -intercept table

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# Performance

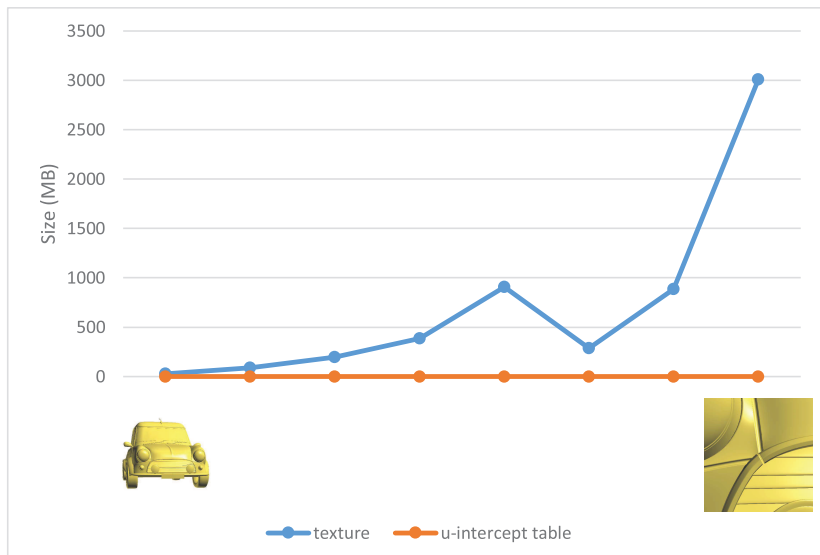
Real-time interactive frame rate.



(a) Performance at different zoom levels. (b) Performance at different screen resolution.

# GPU memory usage

Compare to texture based technique



# Outline

- 1 Basics: Rendering Trimmed Surfaces
- 2 Earlier approaches
- 3 Correct resolution trimming
- 4 Data structure for maximally Fat Correct Scan Lines
- 5 Leveraging Correct Tessellation and the Graphics Pipeline
- 6 Comparisons
- 7 Summary**



# Contributions

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- Questions?