Cross-Layer Network Survivability Under Multiple Cross-Layer Metrics

Zhili Zhou, Member, IEEE, Tachun Lin, Member, IEEE,
Krishnaiyan Thulasiraman, Fellow, IEEE, Guoliang Xue, Fellow, IEEE, and Sartaj Sahni, Fellow, IEEE

Abstract—Given a cross-layer network with logical and physical topologies, the survivable logical topology routing problem is to route each link in the logical layer with a path in the physical topology between the end nodes of a logical link such that the logical topology remains connected after a physical link fails. The Mixed-Integer Linear Programming (MILP) formulation to determine such a routing has been considered in a recent paper [1]. Using this formulation as a basic building block, in this paper we present unified MILP formulations to determine a survivable logical topology routing that also satisfies one of four cross-layer metrics: (1) minimizing the number of logical links to be added to guarantee the existence of survivable logical topology routing; (2) maximizing the capacity of the logical topology; (3) maximizing the connectivity of the logical topology after a physical link failure; and (4) maximizing the minimum cross-layer cut. We also provide heuristics for these problems and compare the performance of these heuristics and MILPs using extensive simulations.

Index Terms—Cross-layer networks, cross-layer survivability, network augmentation, cross-layer reliability metrics, IP-over-WDM optical networks, network virtualization, mathematical programming.

I. INTRODUCTION

Over the last decade, an explosive growth in mobile and Internet traffic has pushed demands for higher capacity in data transmission of telecommunication networks. The data transmission rate of fiber optics networks has reached 305 Tbps according to a recent literature [2]. When failure occurs under large-capacity transmission, it causes broader impacts and catastrophic results. Thus, reliability has become an issue of great interest in the design of modern telecommunication networks, and has been intensively studied in single-layer communication networks.

In this paper, we focus on assessing cross-layer network reliability. An example of a cross-layer network architecture is Internet Protocol over Wavelength Division Multiplexing (IP-over-WDM) networks, which is composed of logical (upper-layer, IP) and physical (lower-layer, WDM) networks. The demands of a link in the logical network are transmitted through a path connecting the corresponding node pair in the physical network. This logical-link to physical-path mapping is called cross-layer mapping. We will use logical, upper-layer, and IP networks interchangeably, as well as physical, lower-layer, and WDM networks. We also refer to the topologies of the IP and WDM networks as logical and physical topologies, respectively.

In a logical topology, nodes and links represent IP routers and links connecting them, respectively. Similarly, physical nodes represent optical cross-connect (OXC) and optical add-drop multiplexer (OADM), while physical edges connecting them represent optical fibers. A lightpath is a cross-layer mapping of a logical link onto a path connecting corresponding physical nodes, through which transmission occurs on a single wavelength thereby bypassing opto-electro-optic (O-E-O) conversions on the intermediate nodes of the path.

Another example of cross-layer networks is encountered in the study of network virtualization, where the virtual and real (physically existing) networks are, respectively, the logical and physical networks. Under the assumption that each virtual node is mapped to a physical node, the implementation of virtual link is also realized through the cross-layer mapping.

Due to the fact that each physical link may carry traffic/demands of multiple logical links, a single physical link failure could disconnect multiple logical links in cross-layer networks. This has given rise to an extensive interest in the study of reliability issues in cross-layer networks under multiple survivability evaluation metrics. A general definition of cross-layer network survivability is to identify a cross-layer mapping so that the logical network remains connected after any single physical link failure. A mapping which satisfies this evaluation metric is called a survivable cross-layer mapping. This survivability study is applicable to any cross-layer networks. It is obvious that survivability of a mapping can be guaranteed if the lightpaths corresponding to this mapping are all link-disjoint. However, this is only a sufficient condition.

Furthermore, with the consideration of logical link demands and physical link capacities, the evaluation metric for cross-layer mapping has been extended from pure survivability metric to an integrated one satisfying survivability and demands. This extension metric was first introduced by Lin et al. [1][3]. A cross-layer mapping is called a weakly survivable mapping if the logical network remains connected after a single physical link failure. A weakly survivable routing that also satisfies physical link capacity and logical demand constraints is called strongly survivable. Figure 1 illustrates weakly and strongly survivable cross-layer mappings. Here the vertical dotted lines show the correspondence between logical and
physical nodes. Also, the capacities of physical links are shown on the corresponding links. Figure 1(a) provides topologies and attributes of logical and physical networks. Figures 1(b) and 1(c) illustrate survivable cross-layer mappings with partial and full demand satisfaction, namely weakly and strongly survivable mapping, correspondingly.

Most of the papers in the literature deal with determining a survivable routing of logical links. In contrast, our focus in this paper is to study integrated design of cross-layer mappings that satisfy certain quality of service requirements besides the survivability condition. For this purpose we identify three cross-layer metrics and present MILPs to determine a routing that maximizes one of these metrics. We also provide an MILP for the survivable logical topology augmentation problem. Our MILPs start with an MILP for the survivable logical topology routing problem. We have chosen the MILP given in [1] which enforces connectivity requirement through the existence of a spanning tree and does not require exponential number of variables used by other ILPs. This MILP is reviewed in Section III after a survey of literature on related works in Section II. All the MILPs developed in this paper use this MILP as a building block. The main contributions of the paper are summarized below.

- **Minimum Logical Topology Augmentation Problem:** Given a logical topology, the augmentation problem is to add a minimum number of additional links to the logical topology that guarantees the existence of a survivable routing of all the logical links of the augmented logical graph. Two earlier works that considered this problem are Thulasiraman et al. [4] and Liu and Ruan [5]. Neither of these two approaches solves the minimum augmentation problem. In Section IV we give an MILP that solves the minimum augmentation problem.

- **Introduction of a new metric “after-failure connectivity”** and determining a routing that maximizes after-failure connectivity: Suppose a logical graph is \(k\)-connected (That is, at least \(k\) logical links have to be disconnected to disconnect the logical graph). A survivable routing only guarantees that the logical graph will remain 1-connected after a physical link failure. Since one physical link failure may disconnect several logical links, a number of links (more than \(k\) links) may get disconnected when two or more failures occur, causing disconnection of the logical graph. To achieve a routing that can survive more than one physical link failure, we need to ensure that the connectivity of the logical graph is as high as possible after a single physical failure. With this in view in Section V-A we define a new metric called after-failure connectivity of the logical graph that is the connectivity of the logical graph after a single physical failure. The larger the value of after-failure connectivity of a routing the better will be its ability to survive multiple physical faults. Thus the after-failure connectivity of a routing is a measure of the ability of the logical graph to remain connected after multiple physical link failures. Our interest is to determine a routing that has as high a value of after-failure connectivity as possible. We give in Section V-A an MILP to determine a survivable routing that maximizes the after-failure connectivity of the logical graph. Our MILP has two significant features: 1) Normally calculating the connectivity of the logical graph after each physical failure would require \(n(n-1)/2\) maximum flow computations. Using a result in network flow theory we reduce the problem to one of computing \(n\) maximum flow computations. 2) We are required to calculate the connectivity of the logical graph after each failure. In other words, connectivity computation has to be repeated on \(m\) after-failure logical graphs. Normally this would require solving \(m\) separate integer linear programs if we explicitly generate the logical graph after each one of the \(m\) physical failures. See [6] where such an approach was taken in a different context. In our MILP we implicitly generate the after failure graphs so that just one MILP is enough to calculate the after-failure connectivity.

- **Introduction of a new metric “logical capacity”** and determining a routing that maximizes logical capacity: Given a survivable routing. Let us associate with each logical link \((s, t)\) a capacity equal to the capacity of the corresponding lightpath. Using these link capacities let us find the maximum \(s-t\) flow between the nodes \(s\) and \(t\) of each logical link. The capacity of the logical graph is the minimum of the maximum \(s-t\) flows over all pairs of nodes in the logical graph constructed. When a physical link fails, several logical links could fail. The
larger the capacity of the logical graph under a given routing the more will be the capacity available for routing after a physical link failure. Thus the logical capacity is a measure of the available capacity after a failure. In Section V-B we provide an MILP that achieves a routing that maximizes the capacity of the logical graph. This is in contrast to our DRCN paper [3] that achieves a routing that maximizes the total capacity (sum of all logical link capacities, not logical capacity as defined above) available before and after a physical link failure.

- Determining a routing that maximizes MCLC (Minimum Cross Layer Cut): In [7] Lee and Modiano defined the concept of MCLC (minimum cross layer cut). MCLC value of a given survivable logical topology routing is the minimum number of physical link failures that would cause the logical layer to become disconnected. For example, if the MCLC value of a routing is 5, this means that the logical graph will remain connected after any set of 5 simultaneous physical link failures. In Section V-C we give an MILP that determines a routing that maximizes MCLC, in contrast to the work in [7] that determines the MCLC value of a given routing.

In Section VI we provide heuristics for all the optimization problems considered in earlier sections. In Section VII we provide extensive simulation results evaluating the performance of the heuristics and the corresponding MILPs.

II. RELATED WORK

A typical network reliability problem is to calculate efficiently the probability that a specified set of nodes can communicate with each other at a given time. The research works in the literature developed exact solution, Monte Carlo simulation, and the polynomial expression for network reliability estimations in single layer networks. [8] provided a survey for early exact factoring algorithms with domination theory. [9][10][11] utilized network transformation and reduction for network reliability estimation. Dikbiyik et al. [12] utilized pre-provisioning/reprovisioning and hold-lightpath schemes which balanced optical network availability, resource efficiency, and protection through excess capacity management. These research works were for single-layer networks and cannot estimate cross-layer network reliability, as they assumed that link failures are independent. In fact, a single physical link failure may cause multiple failures in the logical network. Monte Carlo simulation was also used for estimating single-layer reliability for some fixed link failure probability. In [13], a Monte Carlo method is given to estimate the failure probability with random network link states. With Monte-Carlo simulation, network reliability could be approximated to an arbitrary accuracy. The computational performance of simulations was restricted by a large number of iterations and repeating them with different reliability indices. A randomized fully polynomial approximation scheme for the all-pairs reliability problem was given in [14]. A comprehensive study of combinatorial aspects of the network reliability problem may be found in [15]. Recently, there has been interest in the study of the network reliability problem when links and nodes are broken in a probabilistic manner. [16][17] discussed this problem and have several references to early related works.

As noted earlier, the survivability of a logical topology mapping can be guaranteed if the lightpaths in the physical topology corresponding to this mapping are all link-disjoint. Since finding mutually disjoint paths between multiple pairs of nodes is NP-complete [18], the cross-layer network reliability problem with survivability as an evaluation metric is also NP-complete. Modiano and Narula-Tam [19] have given a necessary and sufficient condition for a cross-layer mapping to be survivable under a single physical link failure in IP-over-WDM networks, and formulated the problem as an Integer Linear Program (ILP). Todimala and Ramamurthy [20] adapted the concept of Shared Risk Link Group (SRLG) introduced in [21] and also computed the routing through an ILP formulation. Extensions of [19] were given by Kan et al. in [6], which discussed the relationship between survivable lightpath routing and spare capacity requirements on the logical links satisfying the original traffic demands after failures. Lin et al. [1][3] discussed rerouting disconnected logical links due to a physical link failure to maximize the sum of satisfied demands. [3] also studied spare capacity assignment to physical links to guarantee availability of capacities for rerouting after a failure. Similar to [3], Vadrevu et al. [22] also proposed ILPs and heuristics addressing survivability and reroutability for logical links with backup capacity sharing after any single physical link failure.

A common drawback of ILP approaches is that they are not scalable as the network size increases. Hence, heuristic approaches were designed to overcome such issues. Kurant and Thiran [23] proposed the Survivable Mapping by Ring Trimming (SMART) framework, which maps the links of certain subgraphs of the given logical graph into link-disjoint paths. Lee et al. [24] solved the same problem utilizing the concept of ear-decomposition in graph theory. Javed et al. [25][26] obtained improved heuristics based on SMART. Using duality theory in graphs, a generalized theory of logical topology survivability was given in [27][28][29]. A new structural approach based on logical protecting spanning tree set concept was introduced in [30]. Lin et al. [31] also presented an integrated approach to design survivable logical topology routing and localize physical link failures. Operations research techniques, such as column generation, have been incorporated into MILP formulations to solve similar problems. Jaumard and Hoang [32] proposed an MILP formulation and a solution approach based on the column generation technique which can generate exact solutions for different scales of logical networks. Two works related to disaster aware survivability in the context of cloud computing and survivable infrastructure designs are reported in [33][34].

III. WEAKLY SURVIVABLE ROUTING MAXIMIZING TOTAL DEMAND SATISFIED: REVIEW OF AN MILP [1][3]

We use the terms network and topology, edge and link, node and vertex, interchangeably throughout the paper. Let $G_L = (V_L, E_L)$ be a logical network and $G_P = (V_P, E_P)$ be a physical network in a cross-layer network. Let $(i, j)$ be a
physical link and \((s,t)\) be a logical link. Capacity on physical link \((i,j)\) is \(c_{ij}\) and demand on logical link \((s,t)\) is \(d_{st}\). We assume that both the logical and physical networks are at least two-edge connected.

**Definition 1**: A logical topology mapping in a cross-layer network with logical topology \(G_L = (V_L, E_L)\) and physical topology \(G_P = (V_P, E_P)\) is **weakly survivable** if \(G_L\) remains connected, after a link failure in \(G_P\).

In this section we review an MILP given in [3] to determine a weakly survivable routing in a cross-layer network that maximizes the sum of all logical demands satisfied subject to the limits on the capacities of physical link. This MILP formulation to be called WSR-MD is the basis of all the MILP formulations given in the rest of the paper.

**WSR-MD** : MILP to determine a weakly survivable logical topology routing maximizing the total demand.

### Variables used in MILP formulation WSR-MD

<table>
<thead>
<tr>
<th>Variable</th>
<th>Description</th>
</tr>
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<tbody>
<tr>
<td>(y_{ij}^st)</td>
<td>binary variable indicates whether the logical link ((s,t)) (\in\ E_L) is routed through the physical link ((i,j) \in E_P). If yes, (y_{ij}^st = 1), otherwise, (y_{ij}^st = 0).</td>
</tr>
<tr>
<td>(r_{ij}^{st})</td>
<td>flow on physical link ((i,j)) due to lightpath ((s,t))</td>
</tr>
<tr>
<td>(r_{st}^{st})</td>
<td>fractional variable for connectivity constraints. (r_{st}^{st} = 0) if logical link ((s,t)) is routed through physical link ((i,j)).</td>
</tr>
<tr>
<td>(\rho_{st})</td>
<td>the capacity for the logical link ((s,t)), where (\rho_{st}) is the smallest capacity of links in the lightpath.</td>
</tr>
<tr>
<td>(c_{ij})</td>
<td>capacity on the physical link ((i,j)), (c_{ij} = c_{ji}).</td>
</tr>
<tr>
<td>(d_{st})</td>
<td>demand for the logical link ((s,t)). (d_{st} = d_{ts}).</td>
</tr>
<tr>
<td>(M)</td>
<td>a large positive number</td>
</tr>
</tbody>
</table>

\[
\text{max} \sum_{(s,t) \in E_L} \rho_{st} \\
\text{s.t.} \sum_{(j,i) \in E_P} y_{ij}^st - \sum_{(j,i) \in E_P} y_{ji}^st = \begin{cases} 1, & \text{if } i = s, \\ -1, & \text{if } i = t, \\ 0, & \text{if } i \neq \{s,t\}, \end{cases} \\
\quad i \in V_P, (s,t) \in E_L \tag{2} \\
y_{ij}^st + y_{ji}^st \leq 1, \quad (i,j) \in E_P, (s,t) \in E_L \tag{3} \\
\sum_{(i,j),(j,i) \in E_P} (y_{ij}^st + y_{ji}^st) \leq 2, \quad (s,t) \in E_L \tag{4} \\
\sum_{(i,j) \in E_P} f_{ij}^st - \sum_{(j,i) \in E_P} f_{ji}^st = \begin{cases} \rho_{st}, & \text{if } i = s, \\ -\rho_{st}, & \text{if } i = t, \\ 0, & \text{if } i \neq \{s,t\}, \end{cases} \\
\quad i \in V_P, (s,t) \in E_L \tag{5} \\
\rho_{st} \leq d_{st}, \quad (s,t) \in E_L \tag{6} \\
f_{ij}^st \leq My_{ij}^st \text{ and } f_{ji}^st \leq My_{ji}^st, \quad (i,j) \in E_P, (s,t) \in E_L \tag{7} \\
\sum_{(s,t) \in E_L} (f_{ij}^st + f_{ji}^st) \leq c_{ij}, \quad (i,j) \in E_P \tag{8} \\
\sum_{(s,t) \in E_L} r_{ij}^{st} - \sum_{(t,s) \in E_L} r_{ts}^{st} = \begin{cases} -1, & \text{if } s = v_1, \\ 0, & \text{if } s \neq v_1, \end{cases} \\
\quad v_1 \in V_L, (i,j) \in E_P \tag{9} \\
0 \leq r_{st}^{ij} \leq 1 - (y_{ij}^st + y_{ji}^st), \quad (i,j) \in E_P, (s,t) \in E_L \tag{10} \\
0 \leq r_{ts}^{ij} \leq 1 - (y_{ij}^st + y_{ji}^st), \quad (i,j) \in E_P, (s,t) \in E_L \tag{11} \\
y_{ij}^st \in \{0,1\}, r_{ij}^{st}, f_{ij}^st, \rho_{st} \geq 0, \quad (s,t) \in E_L, (i,j) \in E_P \tag{12} \\
\]

Constraint (2) with binary variable \(y_{ij}^st\) provides a cross-layer routing for each logical link \((s,t)\) with single unit flow. Constraint (3) guarantees that a logical-layer routing is not routed through a physical-layer link in its two directions. Constraint (4) eliminates other loops by avoiding a logical layer routing to revisit the same node on the physical routing. Without constraints (3) and (4), constraint (2) itself will not be able to avoid self-loops and re-entry of the same node. Constraints (5)-(6) push a flow of value \(\rho_{st}\) through the logical layer routing selected for logical link \((s,t)\). Constraint (7) guarantees that each physical link \((i,j)\) carries flow only if the logical layer routing is routed through at least one direction of the physical link. Here \(M\) is a very large number greater than the maximum link capacity. Constraint (8) requires that the total flow of all the logical layer routings carried by each physical layer link is no more than the corresponding physical link capacity. Constraints (9)-(11) ensure that the logical network after the failure of each physical link contains a spanning tree. Constraint (9) is used to guarantee the existence of a logical spanning structure after a physical link failure. Constraints (10) and (11) make sure that the spanning tree
structure is composed of the logical edges not disconnected by the failure of a physical link \((i, j)\). We wish to note that the survivability constraints (9)-(11) were first employed in Deng et al.'s paper [35].

IV. MINIMUM LOGICAL TOPOLOGY AUGMENTATION FOR GUARANTEED WEAKLY SURVIVABLE ROUTING

Given a cross-layer network, it is possible that the logical topology does not permit a weakly survivable routing. In such a case we need to add additional logical links to guarantee the existence of a weakly survivable routing. This augmentation problem was earlier considered in [4][5]. In this section we now show how to enhance the WSR-MD algorithm to accommodate steps to augment the logical network with minimum number of additional links so that the augmented network admits a weakly survivable routing. It is shown in [4] that such an augmentation is always possible if the physical network is at least 3-edge connected.

Let \(L_L\) be the collection of all ordered pairs of vertices in the logical network. Some of these ordered pairs may not represent links in the original logical network. We introduce auxiliary variable \(h_{st}\) to indicate whether \((s, t)\) is a link in the logical network. \((s, t)\) could be a link in the original logical network or was added during augmentation. We let \(g_{st}\) be the variable which indicates whether \((s, t)\) is a link in the original logical network \(G_L\). Then, we have

**Logical link augmentation constraints:**

\[
g_{st} \leq h_{st}, \quad (s, t) \in L_L \tag{12}
g_{ij}^s \leq h_{st}, \quad (s, t) \in L_L, (i, j) \in E_P \tag{13}
\]

\[
\sum_{(i, j) \in E_P} g_{ij}^s - \sum_{(i, j) \in E_P} g_{ji}^s = \begin{cases} h_{st}, & \text{if } s = t, \\ h_{st}, & \text{if } i = t, \\ 0, & \text{if } i \neq \{s, t\}, \end{cases} \tag{14}
\]

\[
i \in V_P, (s, t) \in L_L, \quad g_{st}, h_{st} \in \{0, 1\}, (s, t) \in L_L. \tag{15}
\]

Constraint (12) builds the connection between the indicator \(g_{st}\) and variable \(h_{st}\). If \((s, t)\) is a link in the given logical network (that is, \(g_{st} = 1\)), then, \((s, t)\) exists in the augmented network (that is, \(h_{st} = 1\)). So, in this case, \(g_{st} \leq h_{st}\). If \(g_{st} = 0\), (12) is trivially satisfied. Constraint (13) indicates that a logical routing is generated only if \((s, t)\) is in the augmented network. Constraint (14), similar to constraint (2), provides logical routings for original and augmented links in the logical topology.

The MILP formulation for minimum logical topology augmentation problem is as follows.

**WSR-MLA:** Minimal logical topology augmentation for guaranteed weakly survivable routing

\[
\min \sum_{(s, t) \in L_L} h_{st} \\
\text{s.t. Constraints (3),(4),(9) to (11), (12)-(15)}
\]

V. WEAKLY SURVIVABLE ROUTING UNDER CROSS-LAYER METRICS

In this section we develop MILP formulations to determine a survivable logical topology routing that maximizes one of three cross layer metrics.

A. Maximizing the After-failure Connectivity of the Logical Topology

The connectivity of a graph is the smallest number of edges whose removal disconnects the graph [36]. Our interest is to find a survivable logical topology routing that maximizes the after-failure connectivity of the logical topology after a single physical link failure. This is equivalent to finding a survivable logical routing that maximizes the number of edges remaining in any cut of \(G_L\) after any physical link failure.

In the following \(G^j_L\) will refer to the graph that results from deleting those links in \(G_L\) which get broken when physical link \((i, j)\) fails. Let \(k_{ij}(s, t)\) denote the maximum number of link-disjoint paths between logical nodes \(s\) and \(t\) in \(G^j_L\) which is the \(s - t\) connectivity of \(G^j_L\) [36]. Then the after-failure connectivity of \(G_L\) under a given routing is \(\min_{s, t \in V_L, (i, j) \in E_P} \{k_{ij}(s, t)\}\). Our objective is to determine a survivable logical routing that maximizes the after-failure connectivity of \(G_L\).

Let \(k^j_{vw}(s, t)\) denote the flow on link \((v, w) \in E_L\) due to an \(s - t\) flow in \(G^j_L\). Picking a node \(s_0\), the following MILP will calculate the maximum number of link disjoint \(s_0 - t\) paths for all \(t \in V_L\) in each \(G^j_L\), and then pick minimum of these numbers as the after-failure connectivity of \(G_L\). Note that we do not have to calculate the maximum number of link disjoint paths between all pairs of logical nodes \(s\) and \(t\). See [37] for correctness of this approach.

**Given** \(s_0 \in V_L, t \in V_L\)

\[
\max k = \max \left\{ \sum_{(v, w) \in E_L} k_{vw}(s_0, t) - \sum_{(w, v) \in E_L} k_{vw}(s_0, t) \right\} \quad \tag{16}
\]

\[
\begin{cases}
k_{ij}(s_0, t), & \text{if } v = s_0, \\
-k_{ij}(s_0, t), & \text{if } v = t, \\
0, & \text{if } v \neq \{s_0, t\},
\end{cases} \quad \begin{cases}
(v, w) \in E_L, (i, j) \in E_P, t \in V_L \\
v \in V_L
\end{cases} \tag{17}
\]

\[
(i, j) \in E_P
\]

\[
k_{ij}^j(s_0, t) + k_{vw}^j(s_0, t) \leq 1, \tag{18}
\]

\[
\begin{cases}
k_{ij}^j(s_0, t), & (i, j) \in E_P, (v, w) \in E_L \tag{19}
\end{cases}
\]

\[
k_{ij}^j(s_0, t) \leq 1 - y_{iwj}, \quad (i, j) \in E_P, (v, w) \in E_L \tag{20}
\]

\[
k_{ij}^j \in B, k_{ij}^j(s_0, t) \in \mathbb{Z}^+, \quad (i, j) \in E_P, (v, w) \in E_L \tag{21}
\]

Note that constraints (18)-(19) guarantee that logical routings of \((s_0, t)\) generated by constraint (17) are link-disjoint. Constraint (20) guarantees only edges that are not broken due to \((i, j) \in E_P\) failure are considered when calculating \(k_{ij}^j(s_0, t)\). This interesting feature helps us avoid to calculate \(G^j_L\) explicitly for each \((i, j)\), thereby, allowing us to provide one single formulation instead of several formulations, one for each physical link \((i, j)\).

Now using (16) in place of (1) and adding the constraints (17)-(22) to the MILP (WSR-MD) we get the following
MILP WSR-AFC that determines a survivable logical topology routing that maximizes the after-failure connectivity of $G_L$.

**WSR-AFC**: Weakly survivable routing maximizing after failure connectivity of logical topology

$$\max k$$

$$s.t. \text{ Constraints (2) - (4), (9) - (11), (17) - (22)}$$

We wish to note that after-failure connectivity is a measure of the ability of a logical topology routing to provide survivability against multiple failures. If this connectivity is larger, then one can expect the routing to provide survivability against a larger number of physical link failures.

**B. Maximizing the Capacity of the Logical Topology**

Given a routing that achieves a demand of $\rho_{vw}$ on logical link $(v, w)$. Let $\Psi(s, t)$ be the maximum flow between any pair of nodes $s, t \in V_L$, while treating $\rho_{vw}$ as the capacity of link $(v, w) \in E_L$. Then we define the capacity of $G_L$ under the given routing as $\min_{s,t \in V_L} \{\Psi(s, t)\}$. Our interest is to determine a survivable logical routing that maximizes logical capacity. We proceed as follows.

Let $f_{vw}(s, t)$ be the flow on logical link $(v, w)$ due to a flow $\Psi(s, t)$ between nodes $s$ and $t$ in $V_L$. The following linear program determines $\Psi(s, t)$ for each pair of nodes $s$ and $t$ in $V_L$ and picks the minimum of these maximum flows which is the capacity of $G_L$.

$$\max \tilde{f}$$

$$\begin{align*}
\sum_{(v, w) \in E_L} f_{vw}(s, t) - \sum_{(w, v) \in E_L} f_{vw}(s, t) \\
= \begin{cases} \\
\Psi(s, t), \quad &v = s, \quad s, t, v \in V_L \\
-\Psi(s, t), \quad &v = t, \quad s, t, v \in V_L \\
0, \quad &v \neq \{s, t\}, \quad s, t, v \in V_L
\end{cases} \\
= \begin{cases} \\
\Psi(s, t), \quad &s, t \in V_L \\
0, \quad &s, t \in V_L, \quad (v, w) \in E_L
\end{cases}
\end{align*}$$

$$\begin{align*}
\sum_{s \in V_L} f_{sv} - \sum_{t \in V_L} f_{tv} + \tilde{f} \leq \Psi(s, t), \\
\tilde{f} \geq 0,
\end{align*}$$

Using the objective (23) in place of (1) and adding the constraints (24)-(27) to the MILP (WSR-MD), we get the following MILP that determines a survivable logical topology routing that maximizes the capacity of the logical topology $G_L$.

**WSR-MCLC**: Weakly survivable routing with maximal logical capacity

$$\max \tilde{f}$$

$$s.t. \text{ Constraints (2) to (11), and (24) to (27)}$$

**C. Maximizing the Min-Cross-Layer Cut**

Given a logical topology routing $\mathcal{R}$, the minimum cross-layer cut $MCLC(\mathcal{R})$ of $\mathcal{R}$ is defined in [7] as the smallest number of physical link failures that will disconnect the logical topology. We wish to find a routing $\mathcal{R}$ that has the maximum value for $MCLC(\mathcal{R})$. If this maximum value is greater than or equal to one, then that routing will be survivable.

In [7] Lee and Modiano showed that finding a routing that has the largest MCLC value is NP-complete. So they presented an ILP to find a routing that minimizes a quantity whose reciprocal gives a lower bound on the MCLC value. Lee and Modiano [7] also presented an ILP $M_{MCLC}$ to find the MCLC value of a given logical topology routing. We now show how $M_{MCLC}$ can be integrated with the constraints (1)-(5) of Section III to yield an ILP for finding a logical topology routing with the largest MCLC value. This ILP is given in (28) - (35).

**WSR-MCLC**: Weakly survivable routing with maximum MCLC value

$$\min \lambda$$

$$\begin{align*}
\lambda \geq \sum_{(i,j) \in E_P} \nu_{ij} \\
\xi_{ij} - \chi_{ij} \leq \nu_{ij}, \quad (i, j) \in E_P & \quad (28) \\
\xi_{ij} \leq y_{ij} & \quad (29) \\
\xi_{ij} \geq \nu_{ij} + y_{ij} - 1, \quad (s, t) \in E_L, (i, j) \in E_P & \quad (30) \\
|\psi_t - \psi_s| \leq \sum_{(i,j) \in E_P} \xi_{ij}, (s, t) \in E_L & \quad (31)
\end{align*}$$

$$\begin{align*}
\sum_{s \in V_L} \psi_s & \geq 1 \\
\psi_0 & = 0 \\
\psi_s, \psi_t, \chi_{ij} \in \{0, 1\}, \lambda \geq 0, & \quad (33)
\end{align*}$$

First we note that the variables $\nu_{ij}, y_{ij},$ and $\psi_t$ used in our ILP correspond to the variables $y_{ij}, f_{ij}^{st}$, and $d_t$ used in $M_{MCLC}$ of [7]. Second, we point out constraint (3) in [7].

$$\psi_t - \psi_s \leq \sum_{(i,j) \in E_P} \nu_{ij}y_{ij}, \quad \forall (s, t) \in E_L$$

should be replaced with.

$$|\psi_t - \psi_s| \leq \sum_{(i,j) \in E_P} \nu_{ij}y_{ij}, \quad \forall (s, t) \in E_L$$

due to the fact that the identification whether $s$ and $t$ is connected, is evaluated by whether $s$ is disconnected from $t$ or $t$ is disconnected from $s$. To obtain these bi-direction information, absolute value is used as shown in constraint (36).

The main difficulty in using $M_{MCLC}$ is that it has a constraint that involves the nonlinear term $\nu_{ij}y_{ij}^{st}$. This constraint needs to be replaced by an equivalent set of constraints. For this purpose we introduced a new variable $\xi_{ij}^{st}$ which indicates whether the routing of $(s, t)$ is impacted by the failure of physical link $(i, j)$. With this new variable constraints (29)-(31) represent a set of constraints equivalent to the following constraint

$$\begin{align*}
\sum_{(i,j) \in E_P} \xi_{ij}^{st} & = \sum_{(i,j) \in E_P} \nu_{ij}y_{ij}^{st}, \quad (s, t) \in E_L.
\end{align*}$$

This follows because $y_{ij}^{st} = 1 \Rightarrow \nu_{ij}y_{ij}^{st} = \nu_{ij}$ and $\xi_{ij}^{st} = \nu_{ij}$ because of (29) and (31). $y_{ij}^{st} = 0 \Rightarrow \nu_{ij}y_{ij}^{st} = 0$ and $\xi_{ij}^{st} = 0$ because of (30).
Then constraint (32) can be further linearized as
\begin{align*}
\psi_l - \psi_s &\leq \sum_{(i,j)\in E_P} \xi_{ij}^{st}, (s, t) \in E_L \\
\psi_s - \psi_t &\leq \sum_{(i,j)\in E_P} \xi_{ij}^{st}, (s, t) \in E_L.
\end{align*}

VI. HEURISTICS

The MILP formulations presented in Sections IV and V require high execution times, though they produce optimal/feasible solutions. To mitigate the computational complexity of these formulations, we present in this section heuristics for all the problems considered.

A. Heuristic for logical topology augmentation

The heuristic augments the given logical topology with additional links so that a survivable routing is guaranteed for the augmented logical topology. The routing corresponding to each logical link is also generated. This procedure is provided in Algorithm 1.

**Algorithm 1 Augment for Survivability**

**Input:** Physical topology \(G_P = (V_P, E_P)\) and logical topology \(G_L = (V_L, E_L)\)

**Output:** A survivable augmented logical topology and its routing

1: Sort all nodes in \(V_L\) by their degrees
2: Find datum node \(\Delta \in V_L\) with the largest degree
3: Let \(G'_L = \emptyset, G_L = G_L\)
4: while \(\exists v \in G'_L\) with degree \(\geq 2\) do
5: Sort all \(v\) by the cost \(C_v = (v's\ logical\ degree) \times (v's\ physical\ degree)\)
6: Select node \(v\) with the largest \(C_v, v' \in V_L\)
7: Find \(v\)'s two adjacent nodes \(v_2\) and \(v_3\) with the largest \(C_{v_2}\) and \(C_{v_3}\)
8: Map \((v, v_2)\) and \((v', v_3)\) into disjoint paths and update \(M_{G'_L}\)
9: \(G'_L = G_L \setminus \{v'\}\)
10: end while
11: while \(\exists v \in G'_L\) with degree \(= 1\) (individual edge \(e \in E_L\)) do
12: Add a parallel edge \(e'\) to \(e\) and find disjoint routings for \(e\) and \(e'\) in \(G_P\)
13: Update the routings in \(M_{G'_L}\)
14: \(G'_L = G'_L \setminus \{e\}\)
15: end while
16: while \(\exists\ isolated\ node\ v \in G'_L\) do
17: Add two edges connecting \(v\) and \(\Delta\) and find two disjoint routing for the two edges.
18: Update the routings in \(M_{G'_L}\)
19: Remove \(v\) from \(G'_L\)
20: end while

The heuristic first sorts all logical nodes by their degrees, and a datum node with the maximum degree is assigned and denoted as \(\Delta\). While there exists logical node \(v\) with degree \(\geq 2\), the heuristic assigns each logical node \(v\) with degree \(\geq 2\) a cost \(C_v\) (degree of logical node) \times (degree of corresponding physical node). The heuristic then selects the logical node with the largest \(C_v\) and chooses two of \(v\)'s adjacent nodes \(v_1\) and \(v_2\) with the largest \(C_{v_1}\) and \(C_{v_2}\). \((v, v_1)\) and \((v, v_2)\) are then mapped into disjoint paths in the physical topology. After that, \(v\) is removed from the logical topology. This procedure is repeated till no logical nodes with degree \(\geq 2\) are left. Next, the heuristic picks a node \(v\) from the remaining logical topology with degree \(= 1\), i.e., an edge \((u, v)\). An edge \((u, v')\) parallel to \((u, v)\) is then added and disjoint mappings for \((u, v)\) and \((u, v')\) in the physical topology are found. This procedure is executed until the elimination of all logical nodes with degree \(= 1\). After the previous steps, if there exists nodes \(v\) with degree \(= 0\), two parallel edges connecting \(v\) and \(\Delta\) are added and they are mapped disjointly in the physical topology. The heuristic is provided in Algorithm 1. Proof of correctness of Algorithm 1 may be found in [4].

As regards computational complexity, the complexity of finding a pair of link-edge disjoint paths dominates the time required, which is \(O(m^{3/2})\). So, the overall complexity is \(O(nm^{3/2})\).

B. Heuristics for maximizing logical capacity

We present a heuristic for maximizing logical capacity in Algorithm 2. To guarantee survivability, the heuristic would still augment the logical topology with additional links. Steps 1-6 are the same as those in Algorithm 1. After selecting the candidate node \(v\) with degree \(\geq 2\) and the largest \(C_v\), instead of mapping its adjacent nodes \(v_1\) and \(v_2\) with the largest \(C_{v_1}\) and \(C_{v_2}\), the \(C_v\)'s are included in a priority list. The heuristic selects two nodes at a time with the highest \(C_v\) (in descending order), finds their disjoint mappings, and determines the minimal capacity of the routing. Among all the routings generated, the one that maximizes the minimal capacity is selected. This procedure is repeated till no logical node with degree \(\geq 2\) is left. After a survivable routing is generated, to determine the maximal logical capacity, we first push the flow for all logical demands. This step is done by repeatedly pushing unit flow for each logical demand until the physical capacity cannot carry more logical demand. Here the physical edge capacity of an edge would be updated if the unit flow is routed through this edge. After the above step, the maximum flow is pushed for every logical node pair.

The complexity is dominated by step 9 in Algorithm 2. At most \(n^2\) pairs of adjacent nodes are picked, and for each pair edge-disjoint paths are to be found. This requires \(O(n^2m^{3/2})\) time. Since this is done for each node, the overall complexity is \(O(nm^{3/2})\).

The complexity of Algorithms 1 and 2 are highly conservative as can be seen from the simulation results.

C. Heuristics for Maximizing the After-Failure Connectivity and the MCLC

These heuristics in Algorithms 3 and 4 start with a spanning tree \(t\), and map their links into paths using a shortest path
Algorithm 2 Survivability and Maximizing Logical Topology Capacity

**Input:** Physical topology $G_P = (V_P, L_P)$, logical topology $G_L = (V_L, E_L)$

**Output:** A logical routing with the maximum capacity

1. Sort all nodes in $V_L$ by their degrees
2. Find datum node $\Delta \in V_L$ with the largest degree
3. $M_{G_L}' = \emptyset$, $G_L' = G_L$
4. **while** $\exists v \in G_L'$ with degree $\geq 2$ **do**
5. Sort all $v$ by the cost $C_v = (v's$ logical degree) $*$ $(v's$ physical degree)
6. Select node $v'$ with the largest $C_{v',v} \in V_L$
7. Sort $v'$ adjacent node $v_i$ by $C_{v_i}$
8. **for** Each $v'$'s adjacent node pair $v_1, v_2$ from sorted $C_{v_i}$ list (in descending order) **do**
9. Map $(v', v_1), (v', v_2)$ disjointly and decide the minimum capacity physical link on the lightpath
10. Record the sum of minimum capacities of the two lightpaths
11. **end for**
12. Select the node pairs $v_1, v_2$ with the largest sum of minimum capacities and update $M_{G_L'}$ with lightpaths of $(v_1, v_2), (v_2, v_2)$
13. $G_{L}' = G\backslash\{v\}$
14. **end while**
15. **while** $\exists v \in G_L'$ with degree $= 1$ (individual edge $e \in E_L$) **do**
16. Add a parallel edge $e'$ to $e$ and find disjoint routings for $e$ and $e'$ in $G_P$
17. Update the routings in $M_{G_L'}$
18. $G_{L}' = G\backslash\{e\}$
19. **end while**
20. **while** $\exists$ isolated node $v \in G_{L}'$ **do**
21. Add two edges connecting $v$ and $\Delta$ and find two disjoint routing for the two edges.
22. Update the routings in $M_{G_L'}$
23. Remove $v$ from $G_L'$
24. **end while**
25. **while** $\exists$ unsatisfied logical demand of $u \in E_L$ and residual physical capacity supporting the demand **do**
26. Push unit demand between edge nodes of $u$ through $u'$s routing
27. Update physical link capacity
28. **end while**
29. **for** all $v, w \in V_L, v \neq w$ **do**
30. Push the maximum flow between $(v, w)$
31. **end for**
32. Return the maximum flow among all $v, w \in V_L$

Algorithm 3 Heuristic: maximizing the connectivity after a single physical link failure

**Input:** $G_P = (V_P, L_P), G_L = (V_L, E_L), V = \emptyset, EM[v] = \emptyset, \forall u \in E_L, w(e) = 1, \forall e \in E_P, \tau_i = \emptyset, i = 0, W$

1. **while** $T \neq E_L$ **do**
2. Generate $\tau_i$, such that $\exists EM[v] = \emptyset, v \in \tau_i$
3. $CB(\tau_i) = E_P$
4. **for** $v \in \tau_i$ **do**
5. **if** $EM[v] \neq \emptyset$ **then**
6. **for** all $k \in EM[v]$ **do**
7. $CB(\tau_i) = CB(\tau_i) \backslash k$
8. $w(k) = 1$
9. **end for**
10. **end if**
11. **end for**
12. $T = T \cup \tau_i$
13. **for all** $v \in \tau_i$ **do**
14. **if** $EM[v] = \emptyset$ **then**
15. Generate $p^v$, $EM[v] = p^v$
16. **for all** $k \in EM[v]$ **do**
17. $CB(\tau_i) = CB(\tau_i) \backslash k$
18. **end for**
19. **end if**
20. **end for**
21. **for all** $k \in E_P$ **do**
22. **if** $k \in CB(\tau_i)$ **then**
23. $w(k) = w(k) + W$
24. **else**
25. $w(k) = 1$
26. **end if**
27. **end for**
28. $i = i + 1$
29. **end while**
30. **if** $E_P \neq \bigcup EM[u], \forall u \in E_L$ **then**
31. AUGMENTATION
32. **end if**
Algorithm 4 Heuristic: maximizing the MCLC

Input: $G_P = (V_P, E_P), G_L = (V_L, E_L), T = \emptyset, EM[u] = \emptyset, \forall u \in E_L, w(e) = 1, \forall e \in E_P, \tau_i = \emptyset, i = 0, 8.$

1: while $T \neq E_L$ do
2: \hspace{1em} Generate $\tau_i$, where $\exists EM[v] = \emptyset, v \in \tau_i$
3: \hspace{1em} CB($\tau_i$) = $E_P$
4: \hspace{1em} for $v \in \tau_i$ do
5: \hspace{2em} if $EM[u] \neq \emptyset$ then
6: \hspace{3em} for all $k \in EM[v]$ do
7: \hspace{4em} CB($\tau_i$) = CB($\tau_i$) \& k
8: \hspace{2em} end for
9: \hspace{1em} end if
10: \hspace{1em} for all $k \in E_P$ do
11: \hspace{2em} if $k \in CB(\tau_i)$ then
12: \hspace{3em} $w(k) = w(k) + W$
13: \hspace{2em} else
14: \hspace{3em} $w(k) = 1$
15: \hspace{2em} end if
16: \hspace{1em} end for
17: \hspace{1em} end for
18: $T = T \uplus \tau_i$
19: \hspace{1em} for all $v \in \tau_i$ do
20: \hspace{2em} if $EM[u] = \emptyset$ then
21: \hspace{3em} Generate $p^v, EM[v] = p^v$
22: \hspace{3em} for all $k \in EM[v]$ do
23: \hspace{4em} CB($\tau_i$) = CB($\tau_i$) \& k
24: \hspace{3em} end for
25: \hspace{2em} for all $k \in E_P$ do
26: \hspace{3em} if $k \in CB(\tau_i)$ then
27: \hspace{4em} $w(k) = w(k) + W$
28: \hspace{3em} else
29: \hspace{4em} $w(k) = 1$
30: \hspace{3em} end if
31: \hspace{2em} end if
32: \hspace{1em} end if
33: \hspace{1em} $i = i + 1$
34: \hspace{1em} end while
35: if $E_P \neq \bigcup EM[u], \forall u \in E_L$ then
36: \hspace{1em} AUGMENTATION
37: end if

VII. COMPUTATIONAL RESULTS

In this section, we report our computational results evaluating the effectiveness and performance of our exact solution approaches (MILP formulations) and heuristics for (1) minimizing logical topology augmentation for weak survivability; (2) survivable routing for maximizing the logical topology capacity; (3) survivable routing for maximizing the connectivity after a single physical link failure; and (4) maximizing the minimal cross-layer cutset.

A. Experimental Setup

The testing cases for both exact solution approaches (MILP formulations) and heuristics algorithms are as follows. We adopted networks introduced in [38][39] as physical topologies. They are DFN, EURO1 (E1), EURO2 (E2), G3, G6, and NSF as shown in Figures 2-7. Two groups of logical topologies are constructed: (a) two-connected planar graph, and (b) the more sparse networks, delaunay triangulation, and spanning tree, denoted as “2CON”, “DELA”, and “TREE”, respectively.

DELA and TREE are used to test the topology augmentation MILP and the heuristic. Two-connected planar graphs are used to test all metrics. Note here that links are randomly removed from 2-connected and delaunay triangulation logical topologies to generate the tested logical networks. For all constructed logical topologies, the logical topologies’ nodes are subsets of physical nodes, that is, $|V_L| = \lfloor 0.5 \times |V_P| \rfloor$ and $\lfloor 0.7 \times |V_P| \rfloor$. We let $r_{L/P}$ denote the “logical to physical node ratio.” We used $r_{L/P} = 0.5$ or 0.7. The detailed information for physical and logical topologies are listed in Tables II and III. In all tested cases, capacities and demands assigned to physical and logical links are randomly generated following uniform distributions in $[1, 10]$ and $[1, 100]$, respectively. We run 10 cases for each tested scenario and report the average performance of these 10 cases.
B. Experimental Results

First, we present the results for the minimizing logical network augmentation. Note that logical network augmentation is triggered only when cross-layer survivable route cannot be generated. We consider “DELA” and “TREE” as logical networks, which are less dense than “2CON”. The performance results for MILP formulation and heuristic for minimizing logical network augmentation for weak survivability are presented in Table IV. We let “PhyG”, “LogG”, \( r_L/P \), “AugEdge”and “Time” represent the names of tested physical network, logical network, the ratio of the number of logical nodes over that of physical nodes, the number of augmented logical edges, and computational time (in seconds.) In all cases, the WSR-MLA formulation does not augment the logical network. The computational time of the heuristic is much less than the MILP approach. We note that for the same physical network, the denser the logical graph (as measured by the ratio \( m/n^2 \)), the less the number of edges required for augmentation. This is because a denser graph can afford to tolerate more link failures before it becomes disconnected. This can be seen from the simulation results. In fact, DELA which is denser then TREE requires no augmentation at all.

Next, we present the performance of the MILPs and heuristics for all four cross-layer evaluation metrics with “2CON” (with logical physical node ratio, 0.5 and 0.7,) as logical topologies. Computational results are reported in Table V and VI. For all testing cases, heuristic algorithms ran significantly faster than exact solution approaches (MILP formulations). For all evaluation metrics except augmentation, we report the average computational time of MILPs which do not need logical augmentation. As we can see, heuristics are much faster.
than the MILPs. In general the computational times increase as the difficulty increases in the order “MinAug”, “MaxCon”, and “MaxMCLC”. Also, to achieve feasible solutions for these four evaluation metrics, the realization of feasible solutions becomes harder and harder.

VIII. SUMMARY AND CONCLUSIONS

In this paper we have presented a comprehensive treatment of mathematical programming frameworks for the survivable logical topology routing problem in capacitated cross-layer optical networks under multiple cross-layer evaluation metrics. We have enhanced the survivability routing formulation WSRMIL given in Section III by developing MILP formulations for (1) minimum logical topology augmentation for guaranteed weakly survivable routing, (2) maximizing the after-failure connectivity of the logical topology, (3) maximizing the capacity of logical topology, and (4) maximizing the minimum cross-layer cut. An interesting feature of our MILP formulations is that the optimization is carried out in a single stage, in contrast to previous approaches that consider logical subgraphs obtained after each physical link failure. For example, see [7]. The contributions reported in this paper assume considerable significance in view of the increasing interest in network virtualization and topology abstraction incorporating survivability requirements [41][42].

REFERENCES


### TABLE V

<table>
<thead>
<tr>
<th>PhyG</th>
<th>MinAug MILP</th>
<th>Heuristic</th>
<th>MaxCapa MILP</th>
<th>Heuristic</th>
<th>MaxConn MILP</th>
<th>Heuristic</th>
<th>MaxMCLC MILP</th>
<th>Heuristic</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
</tr>
<tr>
<td>DFN</td>
<td>3.5 2.727 1.3 6E-04</td>
<td>2 0.127 1 0.003</td>
<td>2 3.043 1 0.003</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>E1</td>
<td>2.7 0.984 1.2 5E-04</td>
<td>2 0.083 1 0.002</td>
<td>2 0.831 1 0.002</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>E2</td>
<td>3.3 6.184 1.3 0.001</td>
<td>2 0.299 1 0.003</td>
<td>2 9.268 1 0.003</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>G3</td>
<td>5.3 0.967 1.3 5E-04</td>
<td>2 0.104 1 0.002</td>
<td>2 1.681 1 0.002</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>G6</td>
<td>3.6 5.274 1.1 9E-04</td>
<td>1 0.348 1 0.003</td>
<td>1 7.162 1 0.003</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>NSF</td>
<td>3.0 1.149 0.8 7E-04</td>
<td>2 0.127 1 0.002</td>
<td>2 1.177 1 0.002</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

### TABLE VI

<table>
<thead>
<tr>
<th>PhyG</th>
<th>MinAug MILP</th>
<th>Heuristic</th>
<th>MaxCapa MILP</th>
<th>Heuristic</th>
<th>MaxConn MILP</th>
<th>Heuristic</th>
<th>MaxMCLC MILP</th>
<th>Heuristic</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
<td>Obj Time</td>
</tr>
<tr>
<td>DFN</td>
<td>3.5 4.325 1.2 8E-04</td>
<td>2 0.213 1 0.003</td>
<td>2 6.802 1 0.003</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>E1</td>
<td>4.0 2.184 1.3 6E-04</td>
<td>2 0.15 1 0.002</td>
<td>2 3.206 1 0.003</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>E2</td>
<td>2.3 33.56 1.0 0.001</td>
<td>2 4.024 1 0.004</td>
<td>2 613.6 1 0.004</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>G3</td>
<td>3.7 1.247 1.4 7E-04</td>
<td>2 0.19 1 0.002</td>
<td>2 16.76 1 0.002</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>G6</td>
<td>3.3 15.3 0.8 0.001</td>
<td>1 0.398 1 0.004</td>
<td>1 11.66 1 0.004</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>NSF</td>
<td>4.0 3.449 0.9 8E-04</td>
<td>2 0.257 1 0.002</td>
<td>2 xx 1 0.003</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

Zhili Zhou (M’11) received her Ph.D. degree in industrial and system engineering from University of Florida, Gainesville, FL, USA in 2010 and B.S. degrees in mathematics from Nanjing University, Nanjing, China in 2003 and 2006, respectively. She is a research staff member at IBM Research Collaboratory, Singapore. Dr. Zhou took second place for the 2011 Pritsker Doctoral Dissertation Award from the Institute of Industrial Engineers for her dissertation. She was the Best Paper Award Runner-up of DRCN’2011 and RNDM’2012. Her research interests are in mixed integer programming, combinatorial optimization, stochastic programming, and quality of service in communication network, transportation network, and power grid. Dr. Zhou is a member of INFORMS.

Tachun Lin (M’08) received the Ph.D. degree in computer science from the University of Oklahoma, Norman, OK, USA, in 2011, and the B.S. and M.S. degrees in computer science from National Chiao Tung University, Hsinchu, Taiwan in 1999 and 2001, respectively. Since 2011, he has been an Assistant Professor of Computing and Technology at Cameron University, Lawton, OK. He was the Best Paper Award Runner-up of DRCN’2011 and RNDM’2012. His research interests include cross-layer network design, network optimization, and graph theory. Dr. Lin is a member of ACM and INFORMS.

Krishnaian Thulasiraman (M’72-SM’84-F’90) received his Ph.D. degree from the Indian Institute of Technology (IIT), Madras, India, in 1968. Since 1994 he holds the Hitachi Chair and is Professor with the School of Computer Science, University of Kentucky, Lexington, KY, USA. His recent interest is in the emerging area of Network Science and Engineering. Dr. Thulasiraman has received several awards and honors: Distinguished Alumni Award of IIT Madras, IEEE Circuits and Systems Society Technical Achievement Award, IEEE CAS Society Golden Jubilee Medal, Senior Fellow of the Japan Society for Promotion of Science, Gopalakrishnan Endowed Chair Professor of IIT Madras, and Fellow of IEEE, AAAS and the European Academy of Sciences.

Guoliang Xue (M’98-SM’99-F’11) received the Ph.D. degree in computer science from the University of Minnesota, Minneapolis, MN, USA, in 1991. He is a Professor of computer science and engineering with Arizona State University, Tempe, AZ, USA. His research interests include quality of service provisioning, resource allocation, survivability, security, and privacy issues in networks. He has received Best Paper Awards at IEEE Globecom’2007, IEEE ICC’2011, IEEE MASS’2011, IEEE Globecom’2011, IEEE ICC’2012, and a Best Paper Runner-up at IEEE ICNP’2010. He is an Associate Editor of the IEEE/ACM Transactions on Networking and IEEE Network, and a member of the Standing Committee of IEEE INFOCOM. He is General Co-Chair of IEEE CNS’2014-IEEE Conference on Communications and Network Security. He was a Keynote Speaker at IEEE LCN’2011, a TPC Co-Chair of IEEE INFOCOM’2010, and a co-Organizer of HCl’2012-ARO Workshop on Human Centric Computing with Collective Intelligence-Challenges and Research Directions. He served on the editorial boards of Computer Networks, IEEE Transactions on Wireless Communications, and IEEE Transactions on Circuits and Systems. He was a Distinguished Lecturer of the IEEE Communications Society. He is an IEEE Fellow.

Sartaj Sahni (M’78-SM’86–F’88) is a Distinguished Professor of Computer and Information Sciences and Engineering at the University of Florida. He is also a member of the European Academy of Sciences, a Fellow of IEEE, ACM, AAAS, and Minnesota Supercomputer Institute, and a Distinguished Alumni of the Indian Institute of Technology, Kanpur. In 1997, he was awarded the IEEE Computer Society Taylor L. Booth Education Award “for contributions to Computer Science and Engineering education in the areas of data structures, algorithms, and parallel algorithms”, and in 2003, he was awarded the IEEE Computer Society W. Wallace McDowell Award “for contributions to the theory of NP-hard and NP-complete problems.” Dr. Sahni was awarded the 2003 ACM Karl Karlstrom Outstanding Educator Award. Dr. Sahni has published over three hundred research papers and written 15 texts. His research publications are on the design and analysis of efficient algorithms, parallel computing, interconnection networks, design automation, and medical algorithms.